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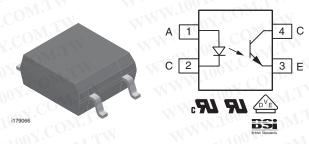
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RoHS

COMPLIANT

**GREEN** 

# Optocoupler Phototransistor Output, SOP-4, Mini-Flat Package, 110 °C Rated



#### 勝 特 力 材 料 886-3-5753170 胜特力电子上淌 86-21-34970699 胜特力电子(深圳 86-755-83298787 Http://www.100y.com.tw

#### **DESCRIPTION**

The 110 °C rated SFH1690AT, SFH1690BT, SFH1690CT, and SFH1690ABT family has a GaAs infrared emitting diode emitter, which is optically coupled to a silicon planar phototransistor detector, and is incorporated in a 4 pin 100 mil lead pitch miniflat package. It features a high current transfer ratio, low coupling capacitance, and high isolation voltage.

The coupling devices are designed for signal transmission between two electrically separated circuits. The SFH1690 series is available only on tape and reel. There are 2000 parts per reel.

#### **FEATURES**

- Operating temperature from 55 °C to + 110 °C
- · SOP (small outline package)
- Isolation test voltage, 3750 V<sub>RMS</sub> (1 s)
- Low saturation voltage
- · Fast switching times
- · Low coupling capacitance
- End-stackable, 0.100" (2.54 mm) spacing
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC



\*\* Please see document "Vishay Material Category Policy": www.vishay.com/doc?99902

#### **APPLICATIONS**

- PLCs
- Telecommunication

#### **AGENCY APPROVALS**

- UL1577, file no. E52744 system code U
- cUL file no. E52744, cUL tested to CSA 22.2 bulletin 5A
- DIN EN 60747-5-2 (VDE 0884)/DIN EN 60747-5-5 (pending) available with option 1
- BSI tested to IEC 60065 and IEC 60950-2001

ORDERING INFORMATION	TN N	W.100Y. COM.T	W W	TON T. CONT. I.A.
S F H 1 6 9		TAPE VI	0 0 1 DE OPTION	SOP-#
AGENCY CERTIFIED/PACKAGE	ONLTW	100 СТ	TR (%)	N. 100 Y. COM
UL, cUL, BSI	50 to 300	50 to 150	100 to 300	100 to 200
SOP-4, Mini flat	SFH1690ABT	SFH1690AT	SFH1690BT	SFH1690CT
VDE, UL, cUL, BSI	50 to 300	50 to 150	100 to 300	100 to 200
SOP-4, Mini flat	COMIT	1. 100 TOO	SFH1690BT-X001	MANA ON C

#### Note

For additional information on the available options refer to option information.



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	NGS (T <sub>amb</sub> = 25 °C, unless otherw	7233322.		
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
INPUT	COM.	COM	KN .	
DC forward current	OM.TW	IFOM	50	mA
Reverse voltage	Y.C. TAN WW.	$V_R$	6	V
Surge forward current	t <sub>p</sub> ≤ 10 μs	I <sub>FSM</sub>	2.5	Α
Power dissipation	COM.	P <sub>diss</sub>	80	mW
Derate linearly from 25 °C	OOX. ONITA	N.100 1.	0.7	mW/°C
OUTPUT	100Y.CO THE WIT	1100Y.C	MITW	
Collector emitter voltage	A COMPANY WAY	V <sub>CEO</sub>	70	V
Emitter collector voltage	1.100 COM. 1	V <sub>ECO</sub>	7	V
Collector current	1100x.	Ic	50	mA
	t <sub>p</sub> ≤ 1 ms	Ic ON	100	mW
Power dissipation	M. To COMP.	P <sub>diss</sub>	150	mW
Derate linearly from 25 °C	COM.	W.100	1.5	mW/°C
COUPLER	TI 100Y.	100	T. COM.TW	
Isolation test voltage between emitter and detector	t = 1 s	V <sub>ISO</sub>	3750	V <sub>RMS</sub>
Operating temperature range	WW. 100X.C WITH	T <sub>amb</sub>	- 55 to + 110	°C
Storage temperature range	WWW. ON. CO. TW	T <sub>stg</sub>	- 55 to + 150	°C
Soldering temperature	max. 10 s dip soldering distance to seating plane ≥ 1.5 mm	T <sub>sld</sub>	260	°C

#### Note

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

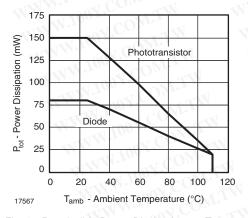


Fig. 1 - Permissible Power Dissipation vs. Temperature



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PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT	Jon COM.	TWW.I	COD	T. T.			
Forward voltage	I <sub>F</sub> = 5 mA	W.	$V_{F}$	W.r.	1.15	1.4	V
Reverse current	$V_R = 6 V$	MM	I <sub>R</sub>	TIM	0.01	10	μΑ
Capacitance	V <sub>R</sub> = 0 V, f = 1 MHz	WWW	Co		14		pF
OUTPUT	M.Ing COM.	-111	W.100	OM	«N		
Collector emitter leakage current	V <sub>CE</sub> = 20 V		I <sub>CEO</sub>	$CO_{M_{1}}$		100	nA
Collector emitter breakdown voltage	I <sub>C</sub> = 100 μA	N W	BV <sub>CEO</sub>	70	TW		V
Emitter collector breakdown voltage	I <sub>E</sub> = - 10 μA	TW V	BV <sub>ECO</sub>	7	WT.IV		V
Collector emitter saturation voltage	I <sub>F</sub> = 10 mA, I <sub>C</sub> = 2.5 mA	IN	V <sub>CEsat</sub>	907.C	0.25	0.4	V
Collector emitter capacitance	V <sub>CE</sub> = 5 V, f = 1 MHz	V.L.	C <sub>CE</sub>	100 .	2.8	. 7	pF
COUPLER	MALL	WILL	MAL	100X.	TMO	N	
Coupling capacitance	f = 1 MHz		C <sub>C</sub>	Your	0.3	W	pF
Capacitance (input to output)	M. 100	M. I	C <sub>IO</sub>	N.In.	0.5		pF

#### Note

 Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANS	<b>FER RATIO</b> ( $T_{amb} = 25  ^{\circ}C$ , u	niess otherwise	specified)	TATIVO.	-7 C	$0_{N_T}$	1
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Ic/I <sub>F</sub>	ON.	SFH1690ABT	CTR	50	any.	300	%
	CO 1 5 may 7 5 V V	SFH1690AT	CTR	50	1.10	150	%
	$I_F = 5 \text{ mA}, V_{CE} = 5 \text{ V}$	SFH1690BT	CTR	100	W.100 -	300	%
	CUTTO	SFH1690CT	CTR	100	100	200	%

#### Note

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering
evaluation. Typical values are for information only and are not part of the testing requirements.

SWITCHING CHAR	RACTERISTICS (T <sub>amb</sub> = 25 °C, unl	ess otherwise	specified)	WW	100X.	Cor.
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Rise time	$V_{CC} = 5 \text{ V}, I_{C} = 2 \text{ mA}, R_{L} = 100 \Omega$	10t <sub>r</sub>	Will	3	V.M.100	μs
Fall time	$V_{CC} = 5 \text{ V}, I_{C} = 2 \text{ mA}, R_{L} = 100 \Omega$	t <sub>f</sub>	TILL	4	100	μs
Turn-on time	$V_{CC} = 5 \text{ V}, I_{C} = 2 \text{ mA}, R_{L} = 100 \Omega$	t <sub>on</sub>	Ur. TW	5	100	μs
Turn-off time	$V_{CC} = 5 \text{ V}, I_{C} = 2 \text{ mA}, R_{L} = 100 \Omega$	t <sub>off</sub>	$O_{Mr}$	3		μs

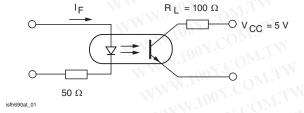


Fig. 2 - Switching Operation (without Saturation)

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SAFETY AND INSULATION RATINGS							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Climatic classification (according to IEC 68 part 1)	M.T.W WW	N.100Y.	COMIT	55/110/21			
Pollution degree (DIN VDE 0109)	WY WY	1007	.00	2		mm	
Comparative tracking index per DIN IEC112/ VDE 0303 part 1, group Illa per DIN VDE 6110 175 399	COWIN M	MM:TOO	175	I,TW	399		
V <sub>IOTM</sub>	COM	V <sub>IOTM</sub>	6000	T. T.		V	
V <sub>IORM</sub>	COMIT	V <sub>IORM</sub>	707	M.r.		V	
DY. WILLIAM WILLIAM	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 25 °C	R <sub>IO</sub>	1001.	MITH	≥ 10 <sup>12</sup>	Ω	
Isolation resistance	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 100 °C	R <sub>IO</sub>	100Y.	TIME	≥ 10 <sup>11</sup>	Ω	
P <sub>SO</sub>	COM.	WW	You	COPY	350	mW	
I <sub>SI</sub>	Ino COM.	- 1111	M. Inc	COM	150	mA	
T <sub>SI</sub>	1001. COM:11.	11	V. 100	COM	165	°C	
Creepage distance	1100Y. OM.TW		5 10	M. OM.	LA	mm	
Clearance distance	TW. CO. TW	V	5	DY.Co	TW	mm	
Insulation thickness between emitter and detector	W.LOOY.COM.TV	1	≥ 0.4	100 A CO.	WT.IV	mm	

#### Note

### **TYPICAL CHARACTERISTICS** (T<sub>amb</sub> = 25 °C, unless otherwise specified)

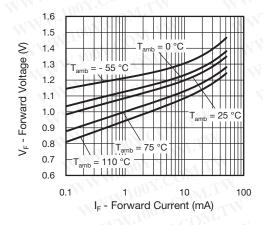


Fig. 3 - Forward Voltage vs. Forward Current

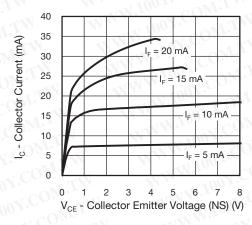


Fig. 4 - Collector Current vs. Collector Emitter Voltage (NS)

As per IEC 60747-5-5, §7.4.3.8.1, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with
the safety ratings shall be ensured by means of protective circuits.

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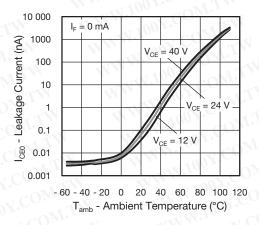


Fig. 5 - Leakage Current vs. Ambient Temperature

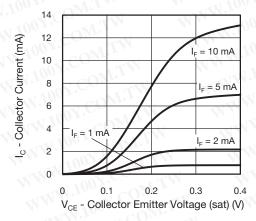


Fig. 6 - Collector Current vs. Collector Emitter Voltage (sat)

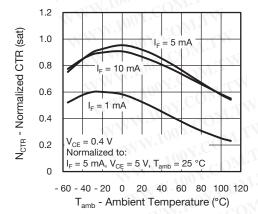


Fig. 7 - Normalized Current Transfer Ratio (sat) vs. Ambient Temperature

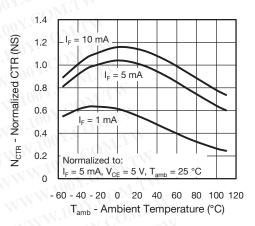


Fig. 8 - Normalized Current Transfer Ratio (NS) vs. Ambient Temperature

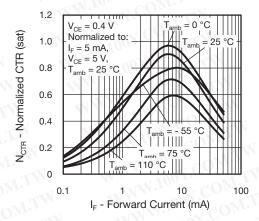


Fig. 9 - Normalized CTR (sat) vs. Forward Current

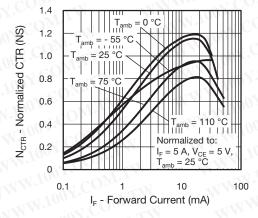


Fig. 10 - Normalized CTR (NS) vs. Forward Current

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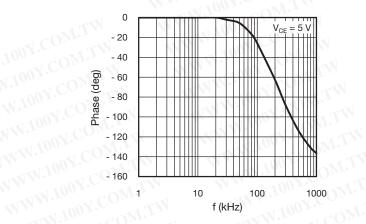


Fig. 11 - F<sub>CTR</sub> vs. Phase Angle

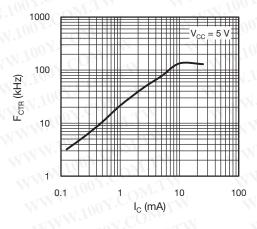


Fig. 12 - F<sub>CTR</sub> vs. Collector Current

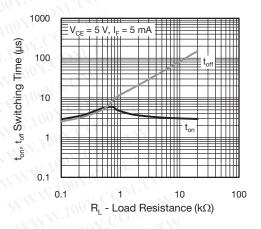
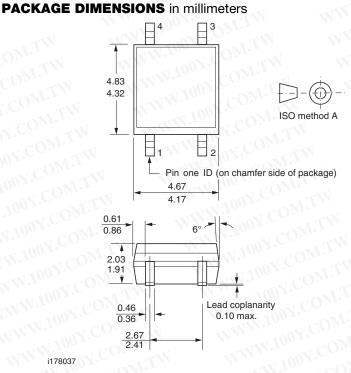


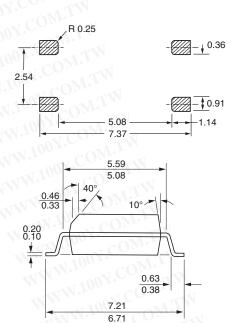
Fig. 13 - Switching Time vs. Load Resistance



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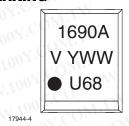
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i178037

### **PACKAGE MARKING**



(example for SFH1690AT)

# 1690B V YWW ■U68X1

(example for SFH1690BT-X001)

#### **Notes**

- The marking of the SFH1690ABT will either show 1690A or 1690B on the first line.
- Tape and reel suffix (T) is not part of the package marking.



### **Legal Disclaimer Notice**

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