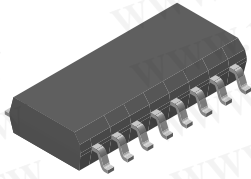
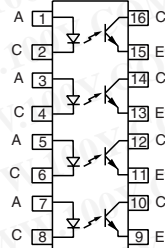


Optocoupler, Phototransistor Output, Quad Channel, SOP-16, Half Pitch Mini-Flat Package



1179076



FEATURES

- SOP (small outline package)
- Isolation test voltage, 3750 V_{RMS} (1.0 s)
- High collector emitter voltage, V_{CEO} = 70 V
- Low saturation voltage
- Fast switching times
- Temperature stable
- Low coupling capacitance
- End stackable, 0.050" (1.27 mm) spacing
- Lead (Pb)-free component
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC



RoHS
 COMPLIANT

DESCRIPTION

The SFH6916 has a GaAs infrared emitter, which is optically coupled to a silicon planar phototransistor detector, and is incorporated in a 16 pin 50 mil lead pitch miniflat package. It features a high current transfer ratio, low coupling capacitance, and high isolation voltage. The coupling devices are designed for signal transmission between two electrically separated circuits.

AGENCY APPROVALS

- UL1577, file no. E52744 system code U
- DIN EN 60747-5-5 available with option 1

ORDER INFORMATION

PART	REMARKS
SFH6916	CTR 50 to 300 %, SOP-16

ABSOLUTE MAXIMUM RATINGS (1)

PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
INPUT				
Reverse voltage		V _R	6.0	V
DC forward current		I _F	50	mA
Surge forward current	t _p ≤ 10 μs	I _{FSM}	2.5	A
Total power dissipation		P _{diss}	80	mW
OUTPUT				
Collector emitter voltage		V _{CE}	70	V
Emitter collector voltage		V _{EC}	7.0	V
Collector current	t _p = 1.0 ms	I _C	50	mA
		I _C	100	mA
Total power dissipation per channel		P _{diss}	150	mW
COUPLER				
Isolation test voltage between emitter and detector (1.0 s)		V _{ISO}	3750	V _{RMS}
Isolation resistance	V _{IO} = 500 V, T _{amb} = 25 °C	R _{IO}	≥ 10 ¹²	Ω
	V _{IO} = 500 V, T _{amb} = 100 °C	R _{IO}	≥ 10 ¹¹	Ω
Storage temperature range		T _{stg}	- 55 to + 125	°C

ABSOLUTE MAXIMUM RATINGS (1)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
COUPLER				
Ambient temperature range		T_{amb}	- 55 to +100	°C
Junction temperature		T_j	100	°C
Soldering temperature (2)	max. 10 s dip soldering distance to seating plane \geq 1.5 mm		260	°C
Total power dissipation		P_{tot}	700	mW

Notes

- (1) $T_{amb} = 25$ °C, unless otherwise specified.
 Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.
- (2) Refer to reflow profile for soldering conditions for surface mounted devices.

ELECTRICAL CHARACTERISTICS (1)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT (2)						
Forward voltage	$I_F = 5$ mA	V_F		1.15	1.4	V
Reverse current	$V_R = 6.0$ V	I_R		0.01	10	μ A
Capacitance	C_O	C_O		14		pF
Thermal resistance		R_{thja}		1000		K/W
OUTPUT						
Collector emitter leakage current	$V_{CE} = 20$ V	I_{CEO}			100	nA
Collector emitter capacitance	$V_{CE} = 5.0$ V, $f = 1.0$ MHz	C_{CE}		2.8		pF
Thermal resistance		R_{thja}		500		K/W
COUPLER						
Collector emitter saturation voltage	$I_F = 20$ mA, $I_C = 1.0$ mA	V_{CEsat}		0.1	0.4	V
Coupling capacitance	$f = 1.0$ MHz	C_C		1.0		pF

Notes

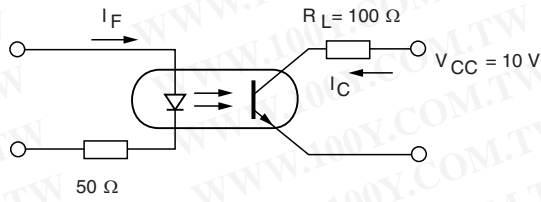
- (1) $T_{amb} = 25$ °C, unless otherwise specified.
 Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.
- (2) $T_{amb} = 25$ °C (except where noted).

CURRENT TRANSFER RATIO						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Current transfer ratio	$I_F = 5.0$ mA, $V_{CC} = 5.0$ V	CTR	50		300	%

SWITCHING CHARACTERISTICS						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
NON-SATURATED						
Rise time	$I_C = 2.0$ mA, $V_{CC} = 10$ V, $R_L = 100$ Ω	t_r		4.0		μ s
Fall time	$I_C = 2.0$ mA, $V_{CC} = 10$ V, $R_L = 100$ Ω	t_f		3.0		μ s
Turn-on time	$I_C = 2.0$ mA, $V_{CC} = 10$ V, $R_L = 100$ Ω	t_{on}		5.0		μ s
Turn-off time	$I_C = 2.0$ mA, $V_{CC} = 10$ V, $R_L = 100$ Ω	t_{off}		4.0		μ s
SATURATED						
Rise time	$I_F = 16.0$ mA, $V_{CC} = 5.0$ V, $R_L = 1.9$ k Ω	t_r		15		μ s
Fall time	$I_F = 16.0$ mA, $V_{CC} = 5.0$ V, $R_L = 1.9$ k Ω	t_f		0.5		μ s
Turn-on time	$I_F = 16.0$ mA, $V_{CC} = 5.0$ V, $R_L = 1.9$ k Ω	t_{on}		1.0		μ s
Turn-off time	$I_F = 16.0$ mA, $V_{CC} = 5.0$ V, $R_L = 1.9$ k Ω	t_{off}		30		μ s

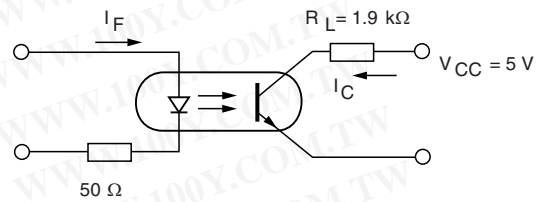
Optocoupler, Phototransistor Output,
Quad Channel, SOP-16, Half Pitch Mini-Flat Package

Vishay Semiconductors



isfh6916_01

Fig. 1 - Switching Operation (Without Saturation)



isfh6916_02

Fig. 2 - Switching Operation (With Saturation)

SAFETY AND INSULATION RATINGS

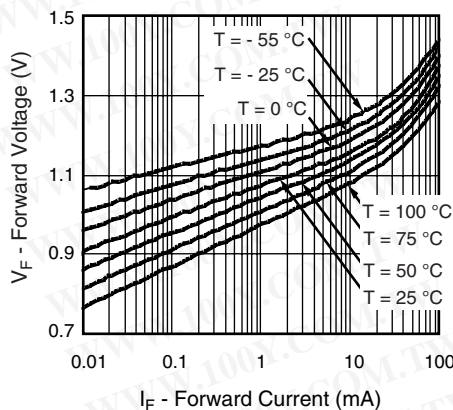
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Climatic classification (according to IEC 68 part 1)				55/100/21		
Comparative tracking index		CTI	175		399	
Peak transient overvoltage		V_{IOTM}	6000			V
Peak insulation voltage		V_{IORM}	707			V
Safety rating - power output		P_{SO}			350	mW
Safety rating - input current		I_{SI}			150	mA
Safety rating - temperature		T_{SI}			175	°C
Creepage distance			5			mm
Clearance distance			5			mm

Note

As per IEC 60747-5-2, § 7.4.3.8.1, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.

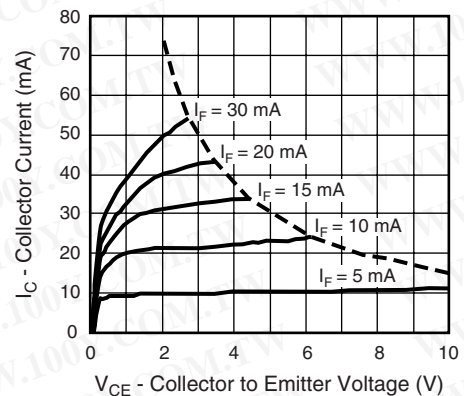
TYPICAL CHARACTERISTICS

$T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified



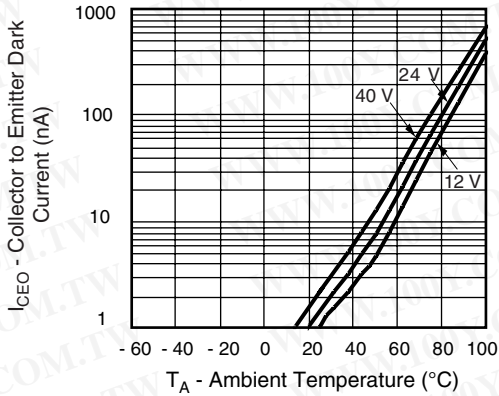
isfh6916_03

Fig. 3 - Diode Forward Voltage vs. Forward Current



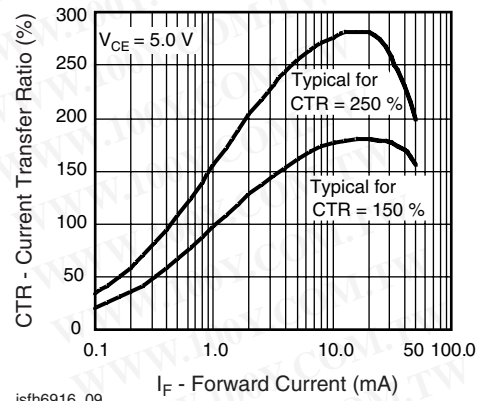
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Fig. 4 - Collector Current vs. Collector Emitter Voltage



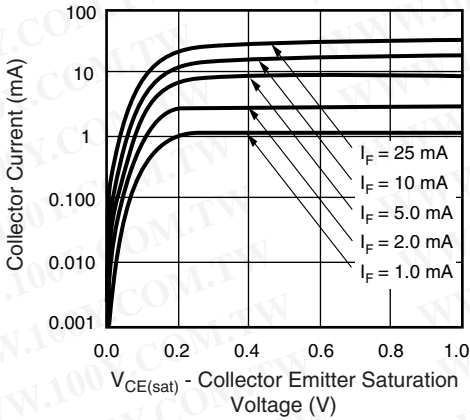
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Fig. 5 - Collector to Emitter Dark Current vs. Ambient Temperature



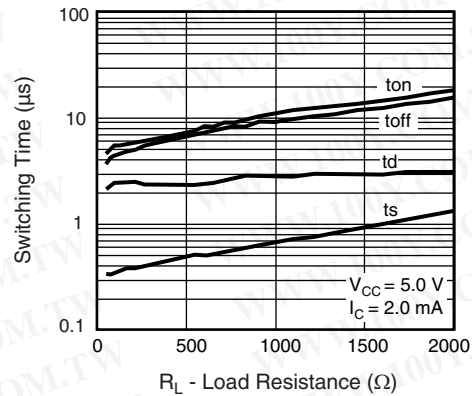
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Fig. 8 - Current Transfer Ratio vs. Forward Current



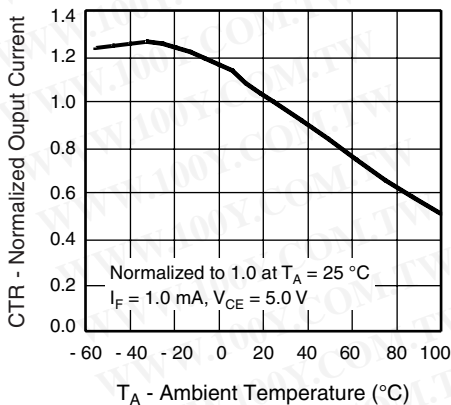
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Fig. 6 - Collector Current vs. Collector Emitter Saturation Voltage



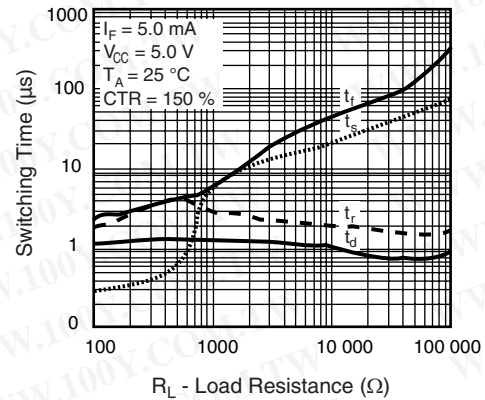
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Fig. 9 - Switching Time vs. Load Resistance



isfh6916_07

Fig. 7 - Normalized Output Current vs. Ambient Temperature



isfh6916_11

Fig. 10 - Switching Time vs. Load Resistance



Optocoupler, Phototransistor Output, Vishay Semiconductors
Quad Channel, SOP-16, Half Pitch Mini-Flat Package

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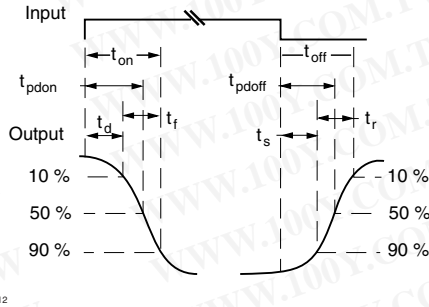
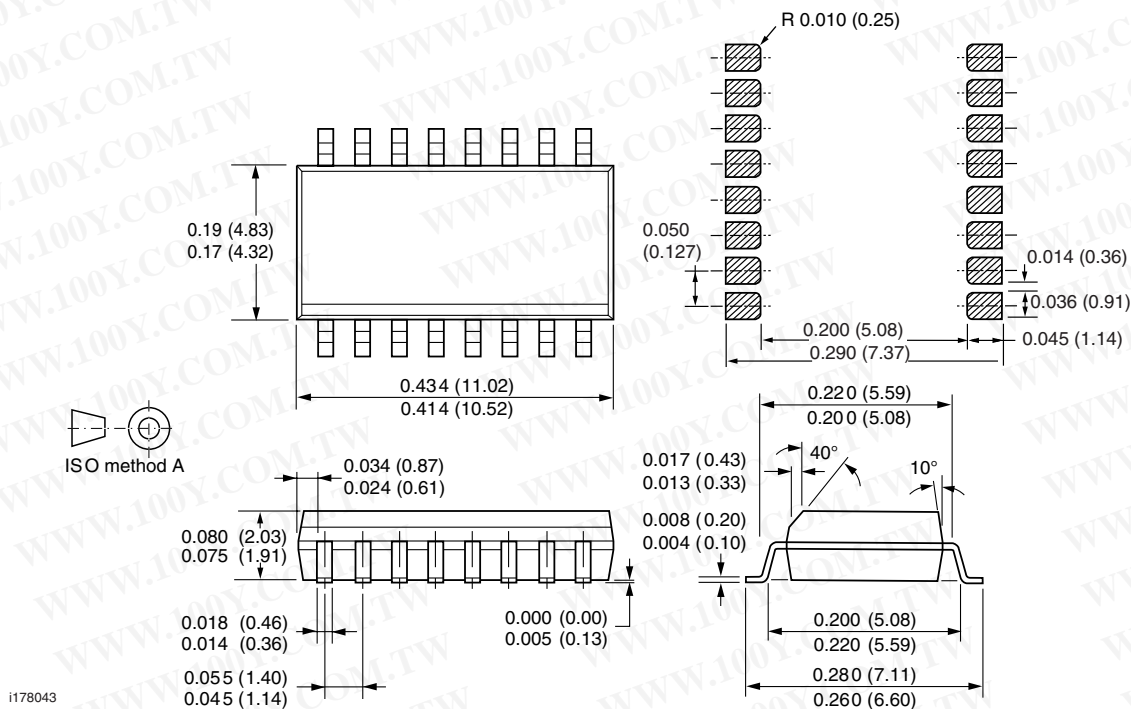


Fig. 11 - Switching Time Measurement

PACKAGE DIMENSIONS in inches (millimeters)



OZONE DEPLETING SUBSTANCES POLICY STATEMENT

It is the policy of Vishay Semiconductor GmbH to

1. Meet all present and future national and international statutory requirements.
2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively.
2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA.
3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

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