

# S102S12 Series S202S12 Series

\*Non-zero cross type is also available. (S102S11 Series/S202S11 Series)

I<sub>T</sub>(rms)≤8A, Built-in snubber circuit Zero Cross type SIP 4pin Triac output SSR

# ■ Description

S102S12 Series and S202S12 Series Solid State Relays (SSR) are an integration of an infrared emitting diode (IRED), a Phototriac Detector and a main output Triac. These devices are ideally suited for controlling high voltage AC loads with solid state reliability while providing 4.0kV isolation ( $V_{iso}(rms)$ ) from input to output.

#### ■ Features

- 1. Output current, I<sub>T</sub>(rms)≤8.0A
- 2. Zero crossing functionary (Vox: MAX. 35V)
- 3. 4 pin SIP package
- 4. High repetitive peak off-state voltage

(V<sub>DRM</sub>: 600V, **S202S12 Series**)

(V<sub>DRM</sub>: 400V, **S102S12 Series**)

- 5. Built-in snubber circuit
- High isolation voltage between input and output (V<sub>iso</sub>(rms): 4.0kV)
- 7. Lead-free terminal components are also available (see Model Line-up section in this datasheet)
- 8. Screw hole for heat sink

# ■ Agency approvals/Compliance

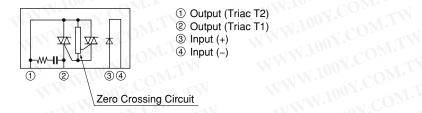
- 1. Recognized by UL508, file No. E94758 (as models No. **\$102\$12**/**\$202\$12**)
- Approved by CSA 22.2 No.14, file No. LR63705 (as models No. S102S12/S202S12)
- 3. Package resin: UL flammability grade (94V-0)

## **■** Applications

- Isolated interface between high voltage AC devices and lower voltage DC control circuitry.
- 2. Switching motors, fans, heaters, solenoids, and valves.
- 3. Power control in applications such as lighting and temperature control equipment.

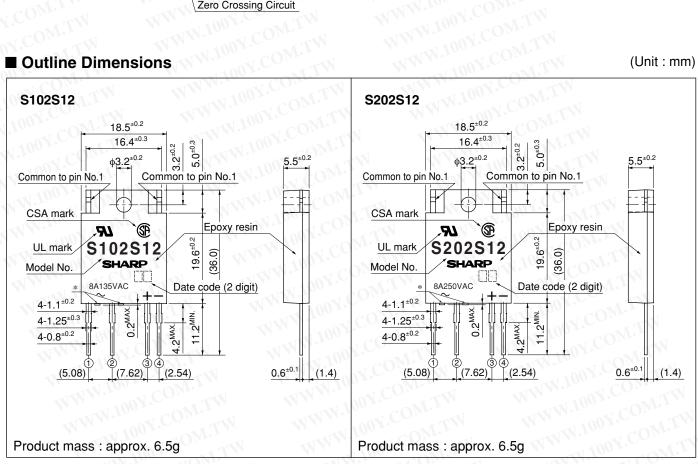


## ■ Internal Connection Diagram



#### Outline Dimensions

(Unit: mm)



- \*: Do not allow external connection.
- ( ): Typical dimensions WWW.100Y.COM.TW

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## Date code (2 digit)

	Year of p	roduction	001.	Month of	f production
A.D.	Mark	A.D	Mark	Month	Mark
1990	A	2002	P	January	WWW 1 . OOY.C
1991	В	2003	R	February	WW2 COM
1992	C	2004	S	March	3N.100
1993	D	2005	T	April	4 100
1994	E	2006	Ú	May	W 5
1995	F	2007	V	June	6 N CO
1996	Н	2008	W	July	7 N. N. O.
1997	J	2009	X	August	8
1998	K	2010	A	September	9 1 100 7 . 4
1999	OL	2011	В	October	O WWW
2000	M	2012	C	November	N N
2001	N		Ni .	December	D 100
repe	eats in a 2	0 year cyc	ele	VW.100Y.CON	LTW WWW.10

# Country of origin Japan

#### Rank mark

WW.100Y.COM.TW WW.100Y.COM.TW There is no rank mark indicator and currently there are no rank offered for this device. WWW.100Y.COM.TW WWW.100Y.COM.TW

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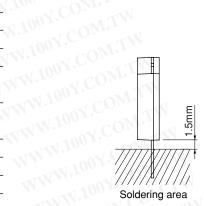
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#### ■ Absolute Maximum Ratings

	Parameter	1 CO	Symbol	Rating	Unit	
T.,4	Forward current	1001.	$I_{\mathrm{F}}$	50 *3	mA	
Input	Reverse voltage	$V_R$	6	V		
OM	RMS ON-state curren	t C	$I_{T}(rms)$	8 *3	A	
COM.	Peak one cycle surge	current	I <sub>surge</sub>	80 *4	A	
Mos	Repetitive	S102S12 S202S12	VDRM	400	77	
CO	peak OFF-state voltage			600	V	
Output	Non-Repetitive	S102S12	V <sub>DSM</sub>	400		
-1 CC	peak OFF-state voltage	S202S12		600	V	
001	Critical rate of rise of ON	-state current	dI <sub>T</sub> /dt	50	A/μs	
OOY.	Operating frequency	1111	f	45 to 65	Hz	
*1 Isolatic	on voltage	V <sub>iso</sub> (rms)	4.0	kV		
Operati	ing temperature	WWI	$T_{opr}$	-20 to +80	°C	
Storage	e temperature	- TAT V	$T_{stg}$	-30 to +100	°C	
*2Solderi	ng temperature	M.	$T_{sol}$	260	°C	10.
*2 For 10s *3 Refer to	0 to 60%RH, AC for 1minute, f=60Hz or 10s efer to Fig.1, Fig.2 50Hz sine wave, T <sub>j</sub> =25°C start			oy.coM	I.TW M.TW	M.

<sup>\*1 40</sup> to 60%RH, AC for 1minute, f=60Hz



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# WWW.100Y.COM.TW **■** Electro-optical Characteristics

	Parameter	+	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
	Forward voltage		$V_{\rm F}$	I <sub>F</sub> =20mA	W-10	1.2	1.4	V
Input	Reverse current	N	$I_R$	V <sub>R</sub> =3V	- <del></del>	10 X	100	μΑ
Output	ON-state voltage		V <sub>T</sub> (rms)	I <sub>T</sub> (rms)=2A, Resistance load, I <sub>F</sub> =20mA	NI	1.Y	1.5	V
	Misi M. O. Li COM.	S102S12	(rmc)	V <sub>OUT</sub> (rms)=120V	11/2// 100x		50	
	Minimum Operating current	S202S12		V <sub>OUT</sub> (rms)=240V			50	mA
	Open circuit leak current	S102S12	I <sub>leak</sub> (rms)	V <sub>OUT</sub> (rms)=120V	5 10		5	mA
		S202S12		$V_{OUT}(rms)=240V$			10	
	Critical rate of rise of OFF-st	tate voltage	dV/dt	V <sub>D</sub> =2/3•V <sub>DRM</sub>	30	M.	M-C	V/µs
	Critical rate of rise of OFF-state voltage	e at commutaion	(dV/dt)c	$T_j=125^{\circ}C$ , $V_D=2/3 \cdot V_{DRM}$ , $dI_T/dt=-4.0A/ms$	5	W (4)	- <del>-</del>	V/µs
Fransfer charac- eristics	Minimum trigger current	TIM	$I_{FT}$	$V_D=6V, R_L=30\Omega$	-//	- <del>-</del> N	8	mA
	Zero cross voltage	COM	$V_{OX}$	$I_F=8mA$	- 1	111	35	V
	Isolation resistance	$CO_{Mr}$	R <sub>ISO</sub>	DC500V, 40 to 60%RH	$10^{10}$	NAN	- oo	Ω
	Turn-on time	S102S12	t <sub>on</sub>	V <sub>D</sub> (rms)=100V, AC60Hz I <sub>T</sub> (rms)=2A, Resistance load, I <sub>F</sub> =20mA	_	WW	9.3	N.CC
		S202S12		$V_D(rms)$ =200V, AC60Hz $I_T(rms)$ =2A, Resistance load, $I_F$ =20mA	- 9.3		ms	
	MMM.	S102S12	MITW	V <sub>D</sub> (rms)=100V, AC60Hz I <sub>T</sub> (rms)=2A, Resistance load, I <sub>F</sub> =20mA	W-	- <	9.3	1007
	Turn-off time	S202S12	t <sub>off</sub>	$V_D(rms)$ =200V, AC60Hz $I_T(rms)$ =2A, Resistance load, $I_F$ =20mA	TW	9.3 ms		ms
"la aa c	1 magistanaa	11.100	R <sub>th</sub> (j-c)	Between junction and case	-	4.0	-NV	°CMI
nermal resistance		R <sub>th</sub> (j-a)	Between junction and ambient	$M_{T_{L}}$	40		°C/W	

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<sup>\*2</sup> For 10s

<sup>\*3</sup> Refer to Fig.1, Fig.2

<sup>\*4</sup> f=50Hz sine wave, T<sub>i</sub>=25°C start



# W.100Y.COM.TW ■ Model Line-up (1) (Lead-free terminal components)

nipping Package	200pcs/case	V <sub>DRM</sub> [V]	$I_{FT}[mA]$ ( $V_D=6V$ ,
M.T.V	\$102\$12F	400	$R_L=30\Omega$ )  MAX.8
Model No.	S202S12F	600	MAX.8

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iviogei Lii	ne-up (2) (Lead so	older platin	g components)	
ON.COM.	Case	$V_{ m DRM}$	I <sub>FT</sub> [mA]	
Shipping Package	200pcs/case	[V]	$(V_D=6V, R_L=30\Omega)$	
Madal Na	S102S12	400	MAX.8	
Model No.	S202S12	600	MAX.8	

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Fig.1 Forward Current vs. Ambient Temperature

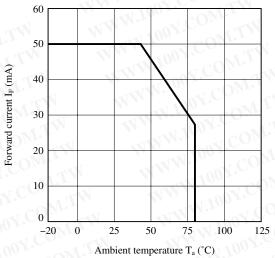


Fig.3 RMS ON-state Current vs. Case Temperature

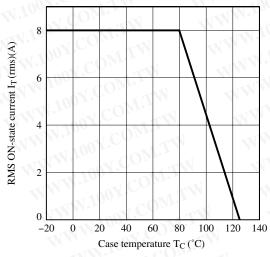


Fig.5 Surge Current vs. Power-on Cycle

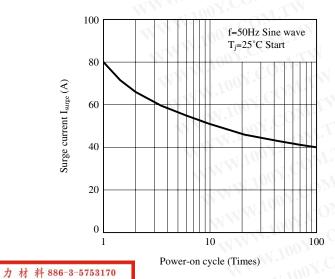


Fig.2 RMS ON-state Current vs. Ambient Temperature

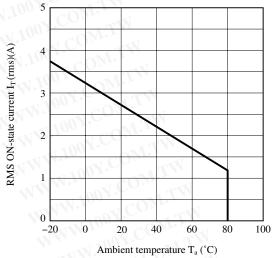


Fig.4 Forward Current vs. Forward Voltage

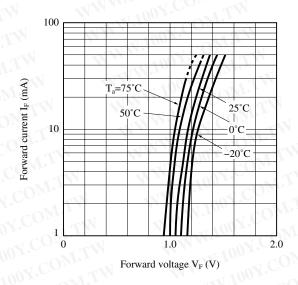


Fig.6 Maximum ON-state Power Dissipation vs. RMS ON-state Current

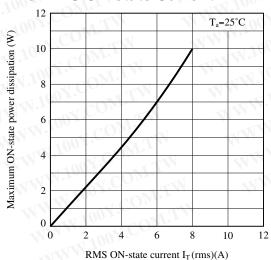




Fig.7 Minimum Trigger Current vs. **Ambient Temperature** 

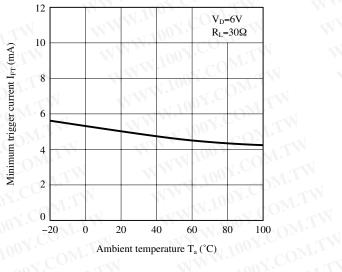


Fig.8-b Open Circuit Leak Current vs. Supply Voltage (S202S12)

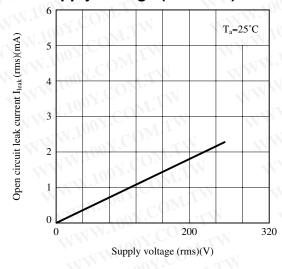
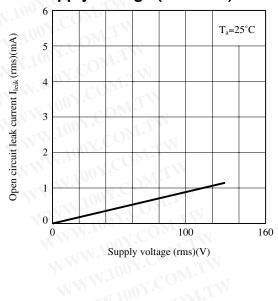


Fig.8-a Open Circuit Leak Current vs. Supply Voltage (S102S12)



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## **■** Design Considerations

# Recommended Operating Conditions

	Parameter	1001.	Symbol	Conditions	MIN.	MAX.	Unit
- CT	Input signal current a	t ON state	$I_F(ON)$	W 1 - 1100 Y.	16	24	mA
Input	Input signal current at	OFF state	I <sub>F</sub> (OFF)	MAIN TOOX CO	0	0.1	mA
COM.	Load supply voltage	S102S12	17 (************************************	W WYW.100Y.C	80	120	V
		S202S12			80	240	
Output	Load supply current	100	I <sub>OUT</sub> (rms)	$\overline{\omega}_{M} = \overline{\omega}_{M} 100x$	0.1	$I_T(rms) \times 80\%(*)$	mA
V.CO	Frequency	MM	Cf	TW TOOL	47	63	Hz
Operation	Operating temperature		$T_{opr}$	-WWW.	C -20	80	°C

<sup>(\*)</sup> See Fig.2 about derating curve (I<sub>T</sub>(rms) vs. ambient temperature).

# Design guide

In order for the SSR to turn off, the triggering current (I<sub>F</sub>) must be 0.1mA or less.

For over voltage protection, a Varistor may be used.

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A varistor used for the above mentioned scenarios should be located as close to the main output triac as possible.

Particular attention needs to be paid when utilizing SSRs that incorporate zero crossing circuitry.

If the phase difference between the voltage and the current at the output pins is large enough, zero crossing type SSRs cannot be used. The result, if zero crossing SSRs are used under this condition, is that the SSR may not turn on and off irregardless of the input current. In this case, only a non zero cross type SSR should be used in combination with the above mentioned snubber circuit selection process.

The load current should be within the bounds of derating curve. (Refer to Fig.2) Also, please use the optional heat sink when necessary.

In case the optional heat sink is used and the isolation voltage between the device and the optional heat sink is needed, please locate the insulation sheet between the device and the heat sink.

When the optional heat sink is equipped, please set up the M3 screw-fastening torque at 0.3 to 0.5N· m. In order to dissipate the heat generated from the inside of device effectively, please follow the below suggestions.

- (a) Make sure there are no warps or bumps on the heat sink, insulation sheet and device surface.
- (b) Make sure there are no metal dusts or burrs attached onto the heat sink, insulation sheet and device surface.
- (c) Make sure silicone grease is evenly spread out on the heat sink, insulation sheet and device surface.

Silicone grease to be used is as follows;

- 1) There is no aged deterioration within the operating temperature ranges.
- 2) Base oil of grease is hardly separated and is hardly permeated in the device.
- 3) Even if base oil is separated and permeated in the device, it should not degrade the function of a device.

Recommended grease: G-746 (Shin-Etsu Chemical Co., Ltd.)

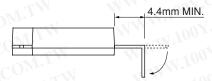
: G-747 (Shin-Etsu Chemical Co., Ltd.)

: SC102 (Dow Corning Toray Silicone Co., Ltd.)



In case the optional heat sink is screwed up, please solder after screwed.

In case of the lead frame bending, please keep the following minimum distance and avoid any mechanical stress between the base of terminals and the molding resin.



Some of AC electromagnetic counters or solenoids have built-in rectifier such as the diode.

In this case, please use the device carefully since the load current waveform becomes similar with rectangular waveform and this results may not make a device turn off.

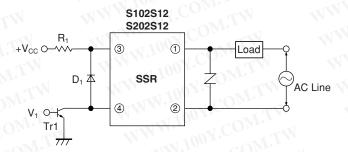
# Degradation

In general, the emission of the IRED used in SSR will degrade over time.

In the case where long term operation and / or constant extreme temperature fluctuations will be applied to the devices, please allow for a worst case scenario of 50% degradation over 5years.

Therefore in order to maintain proper operation, a design implementing these SSRs should provide at least twice the minimum required triggering current from initial operation.

#### Standard Circuit



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<sup>☆</sup> For additional design assistance, please review our corresponding Optoelectronic Application Notes.



# ■ Manufacturing Guidelines

# Soldering Method

Flow Soldering (No solder bathing)

Preheating is within the bounds of 100 to 150°C and 30 to 80s.

Please solder within one time

# W.100Y.COM Other notices

Please test the soldering method in actual condition and make sure the soldering works fine, since the impact on the junction between the device and PCB varies depending on the tooling and soldering conditions.

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# Cleaning instructions

## Solvent cleaning:

Solvent temperature should be 45°C or below. Immersion time should be 3minutes or less.

## Ultrasonic cleaning:

The impact on the device varies depending on the size of the cleaning bath, ultrasonic output, cleaning time, size of PCB and mounting method of the device.

Therefore, please make sure the device withstands the ultrasonic cleaning in actual conditions in advance of mass production.

#### Recommended solvent materials:

Ethyl alcohol, Methyl alcohol and Isopropyl alcohol.

In case the other type of solvent materials are intended to be used, please make sure they work fine in actual using conditions since some materials may erode the packaging resin.

#### Presence of ODC

This product shall not contain the following materials.

And they are not used in the production process for this device.

Regulation substances: CFCs, Halon, Carbon tetrachloride, 1.1.1-Trichloroethane (Methylchloroform)

Specific brominated flame retardants such as the PBBOs and PBBs are not used in this product at all.

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# ■ Package specification

Package materials

Packing case: Corrugated cardboard Partition: Corrugated cardboard Pad: Corrugated cardboard Cushioning material: Polyethylene

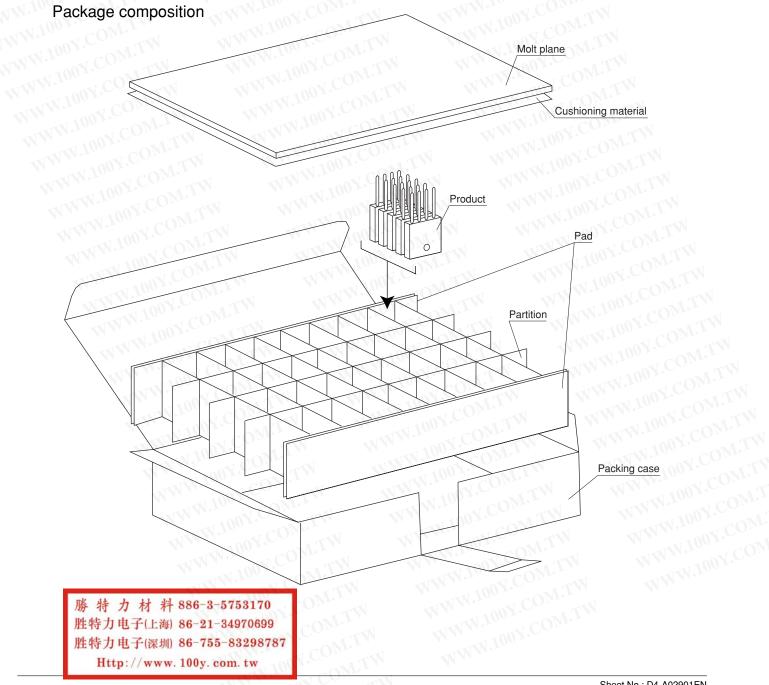
Molt plane: Urethane

# Package method

The product should be located after the packing case is partitioned and protected inside by 4 pads.

Each partition should have 5 products with the lead upward.

Cushioning material and molt plane should be located after all products are settled (1 packing contains 200 pcs).





## ■ Important Notices

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  - --- Office automation equipment
  - --- Telecommunication equipment [terminal]
  - --- Test and measurement equipment
  - --- Industrial control
  - --- Audio visual equipment
  - --- Consumer electronics
- (ii) Measures such as fail-safe function and redundant design should be taken to ensure reliability and safety when SHARP devices are used for or in connection

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- --- Traffic signals
- --- Gas leakage sensor breakers
- --- Alarm equipment
- --- Various safety devices, etc.

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- --- Medical and other life support equipment (e.g., scuba).
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