

### **THYRISTORS**

# 2P4M, 2P5M, 2P6M

## 2 A(4 Ar.m.s.) PLASTIC MOLDED THYRISTOR

### DESCRIPTION

The 2P4M to 2P6M are P-gate all diffused plastic molded type SCR granted average on-state current 2 Amps ( $T_c = 77$  °C), with rated voltages up to 600 volts.

#### **FEATURES**

- Easy installation by its miniature size and thin electrode leads
- Less holding current distribution provides free application design.
- Low cost because of mass-production.

#### **QUALITY GRADE**

Standard

Please refer to "Quality grade on NEC Semiconductor Devices" (Document number IEI-1209) published by NEC Corporation to know the specification of quality grade on the devices and its recommended applications.

#### **APPLICATIONS**

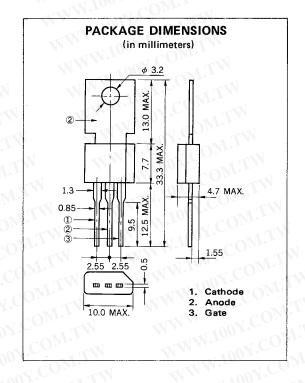
Electric blanket, Electronic jar, Various temperature control.

Electric sewing machine, Speed control of miniature type motor.

Light display equipment, Lamp dimmer such as a display for entertainment.

Automatic gas lighter, Battery charger.

Solid state static switches etc.



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### ABSOLUTE MAXIMUM RATINGS ( $T_a = 25$ °C)

CHARACTERISTIC	SYMBOL	2P4M	2P5M	2P6M	UNIT	NOTE
Non-Repetitive Peak Reverse Voltage*	VRSM	500	600	700	V	RGK = 1 kΩ
Non-Repetitive Peak Off-state Voltage*	V <sub>DSM</sub>	500	600	700	V	R <sub>GK</sub> = 1 kΩ
Repetitive Peak Reverse Voltage*	VRRM	400	500	600	V	R <sub>GK</sub> = 1 kΩ
Repetitive Peak Off-state Voltage*	VDRM	400	500	600	V	R <sub>GK</sub> = 1 kΩ
On-state Current	I <sub>T(AV)</sub>	2 (T <sub>c</sub> = 77 °C,	Α	See Fig.3, Fig.4		
Surge Non-Repetitive On-state Current	ITSM	COSTI	20	1007	Α	See Fig. 10
Peak Gate Power Dissipation	РСМ	0.5 (f ≥ 50 Hz, Duty ≤ 10 %)			W	WT
Average Gate Power Dissipation	PG(AV)	T COMP.	0.1	MW.IO	W	)Mr.
Peak Gate Forward Current	l <sub>FGM</sub>	0.2 (f	≥ 50 Hz, Duty ≤	10 %)	Α	OM
Peak Gate Reverse Voltage	V <sub>RGM</sub>	001.	6	111	V.	
Junction Temperature	Tj	LOON.CO	-40 to + 125	MM	°C	
Storage Temperature	T <sub>stg</sub>	CO	-55 to + 150	WWW.	°C	COM
Weight		N.100	1.4	Wixe	g	COM

T<sub>c</sub>: Case Temperature is measured at 1.5 mm from the neck of Tablet.

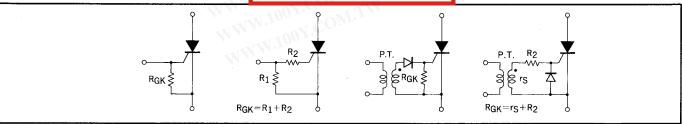
### ELECTRICAL CHARACTERISTICS ( $T_a = 25$ °C)

CHARACTERISTIC	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	NOTE
Repetitive Peak Reverse Current*	IRRM	V <sub>RM</sub> = V <sub>RRM</sub> , T <sub>j</sub> = 125 °C R <sub>GK</sub> = 1 kΩ		_	100	μΑ	OOX.CON
Repetitive Peak Off-state Current*	IDRM	$V_{DM} = V_{DRM}, T_j = 125 ^{\circ}C$ $R_{GK} = 1 k\Omega$	TI	N –	100	μΑ	100 X . CO
On-state Voltage	V <sub>TM</sub>	I <sub>TM</sub> = 4 A	7× (	W.	2.2	V	See Fig. 1
Gate-Trigger Current*	l <sub>GT</sub>	$V_{DM} = 6 \text{ V}, R_L = 100 \Omega$ $R_{GK} = 1 \text{ k}\Omega$	$\overline{O}_{N_P}$	TĦ	200	μА	See Fig. 5 Fig. 7
Gate-Trigger Voltage*	V <sub>GT</sub>	$V_{DM} = 6 \text{ V}, R_{L} = 100 \Omega$ $R_{GK} = 1 \text{ k}\Omega$	$C\overline{O}_{k}$	LTW	8.0	V	See Fig. 6, Fig. 8
Gate Non-Trigger Voltage*	V <sub>GD</sub>	$V_{DM} = 1/2 V_{DRM}, T_j = 125 °C$ $R_{GK} = 1 k\Omega$	0.2	M-II)	- XI	٧	MM.100
Critical Rate-of-Rise of Off-state Voltage	dv/dt	$V_{DM} = 2/3 V_{DRM}, T_j = 125 ^{\circ}C$ $R_{GK} = 1 k\Omega$	10	10**		V/μS	** 2P5M, 2P6M
Holding Current*	IHO)	V <sub>D</sub> = 24 V, R <sub>GK</sub> = 1 kΩ I <sub>TM</sub> = 4 A	001.	COM	3	mA	See Fig. 9
Thermal Resistance	Rth (J-c)	Junction to Case	-01	CDE	10	°C/W	See Fig. 11
Thermal Hesistance	R <sub>th</sub> (j-a)	Junction to Ambient	$7n_{o}$ ,	-7 CO	75	C/VV	See Fig. 11

<sup>\*\*</sup> Note: Insert a resistance less than 1 k $\Omega$  between gate and cathode, because the items indicated are guaranteed by connecting short resistance between gate and cathode (R<sub>GK</sub> = 1 k $\Omega$ ).

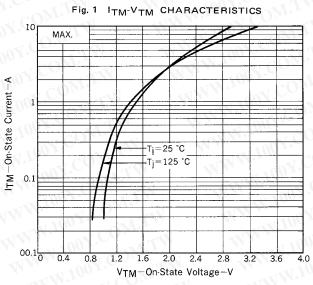


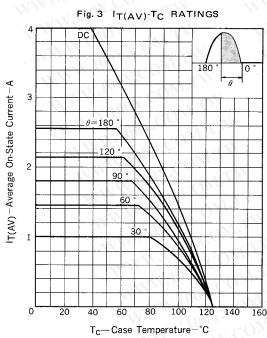
### EXAMPLE OF $R_{G\,K}$ INSERTION

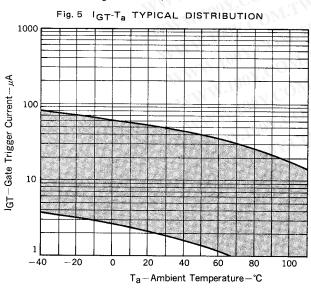


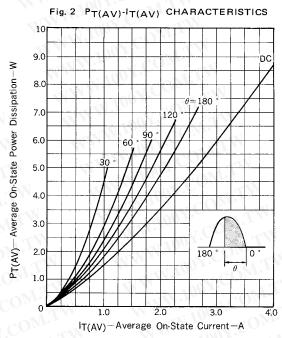
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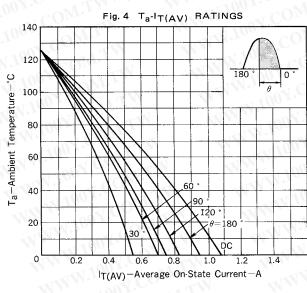
TYPICAL CHARACTERISTICS (Ta = 25 °C)

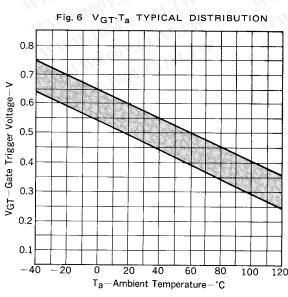


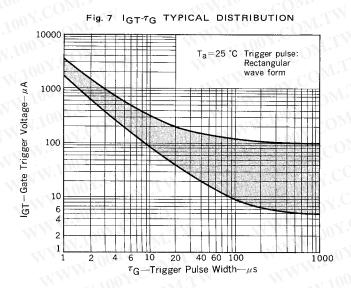


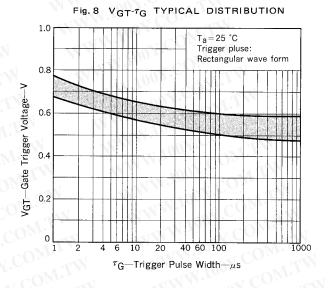


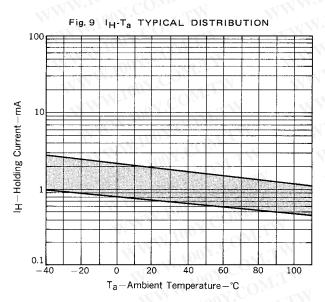


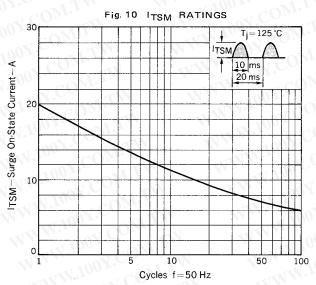


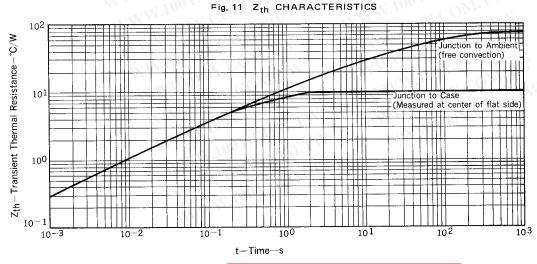












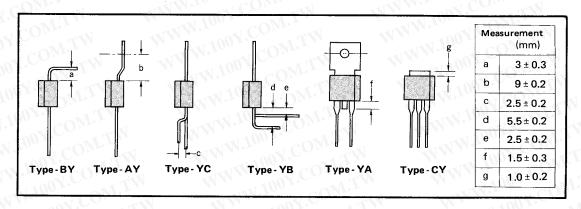
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#### **NOTICE FOR INSTALLATION**

- 1. Electrode leads (especially heat sink tablet) are not granted to be bent because of wet-proof. However in case it is required inevitably, a mechanical stress should not be put on mold. Fix tightly between the mold case and the area to be formed or bent.
- 2. Electrode leads should not to be bent more than twice over 90°. Avoid the bending within 1.5 mm from the neck of mold case.
- 3. Special lead and heat tab formings as indicated below are available at an additional cost.



- 4. The surface of heat sink for thermal radiator is to be smooth without any foreign matter.
- 5. Suitable torque value is 4 to 5 kg.cm.
- 6. Soldering
  - O Recommended solder: PbSu (4:6)

Melting point 180 °C

O Dimension from the neck of leads to dipping points

Soldering temperature and period

250°C 230°C

#### REFERENCE

Document name Docume	ument No
uality control guide of semiconductor devices MEI-1	IEI-1202
ssembly manual of semiconductor devices IEI-1	EI-1207

### NEC

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