

MOS FIELD EFFECT TRANSISTOR
2SK2488

SWITCHING
N-CHANNEL POWER MOS FET
INDUSTRIAL USE

勝特力材料 886-3-5753170
勝特力电子(上海) 86-21-54151736
勝特力电子(深圳) 86-755-83298787
[Http://www.100y.com.tw](http://www.100y.com.tw)

DESCRIPTION

The 2SK2488 is N-Channel MOS Field Effect Transistor designed for high voltage switching applications.

FEATURES

- Low On-Resistance
 $R_{DS(on)} = 1.2 \Omega$ ($V_{GS} = 10 V, I_D = 5.0 A$)
- Low C_{iss} $C_{iss} = 2900 pF$ TYP.
- High Avalanche Capability Ratings

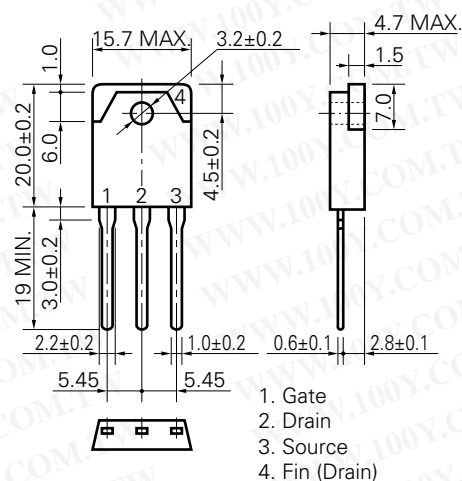
ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ C$)

Drain to Source Voltage	V_{DSS}	900	V
Gate to Source Voltage	V_{GSS}	± 30	V
Drain Current (DC)	$I_D(DC)$	± 10	A
Drain Current (pulse)*	$I_D(pulse)$	± 20	A
Total Power Dissipation ($T_c = 25^\circ C$)	P_{T1}	150	W
Total Power Dissipation ($T_A = 25^\circ C$)	P_{T2}	3.0	W
Channel Temperature	T_{ch}	150	$^\circ C$
Storage Temperature	T_{stg}	-55 to +150	$^\circ C$
Single Avalanche Current**	I_{AS}	10	A
Single Avalanche Energy**	E_{AS}	294	mJ

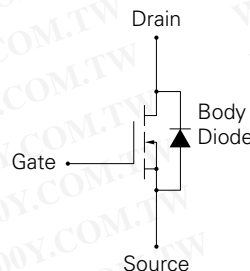
* $PW \leq 10 \mu s, Duty Cycle \leq 1\%$

** Starting $T_{ch} = 25^\circ C, R_G = 25 \Omega, V_{GS} = 20 V \rightarrow 0$

PACKAGE DIMENSIONS
(in millimeter)



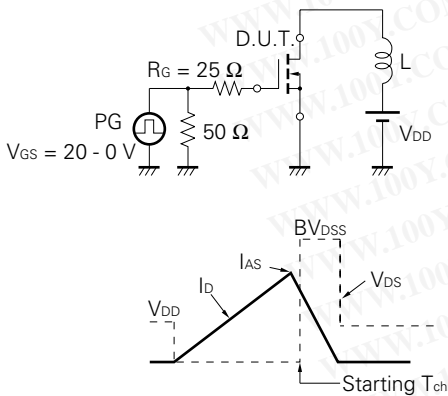
MP-88



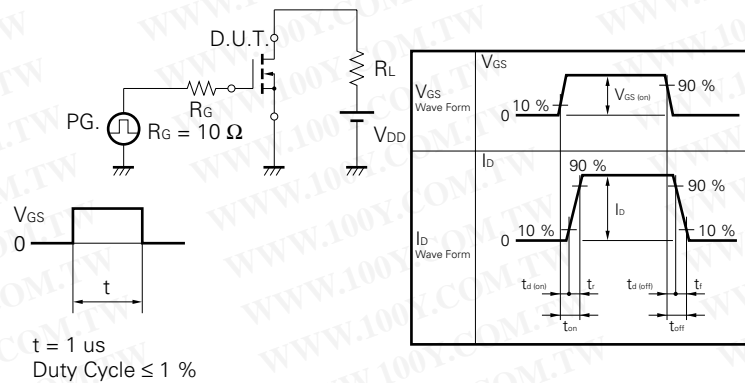
ELECTRICAL CHARACTERISTICS (T_A = 25 °C)

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Drain to Source On-Resistance	R _{DS(on)}		1.0	1.2	Ω	V _{GS} = 10 V, I _D = 5.0 A
Gate to Source Cutoff Voltage	V _{GS(off)}	2.5		3.5	V	V _{DS} = 10 V, I _D = 1 mA
Forward Transfer Admittance	y _{fs}	3.5			S	V _{DS} = 20 V, I _D = 5.0 A
Drain Leakage Current	I _{DSS}			100	μA	V _{DS} = V _{DSS} , V _{GS} = 0
Gate to Source Leakage Current	I _{GSS}			±100	nA	V _{GS} = ±30 V, V _{DS} = 0
Input Capacitance	C _{iss}		2 900		pF	V _{DS} = 10 V
Output Capacitance	C _{oss}		400		pF	V _{GS} = 0
Reverse Transfer Capacitance	C _{rss}		70		pF	f = 1 MHz
Turn-On Delay Time	t _{d(on)}		35		ns	I _D = 5.0 A
Rise Time	t _r		30		ns	V _{GS} = 10 V
Turn-Off Delay Time	t _{d(off)}		160		ns	V _{DD} = 150 V
Fall Time	t _f		32		ns	R _G = 10 Ω
Total Gate Charge	Q _G		90		nC	I _D = 10 A
Gate to Source Charge	Q _{GS}		16		nC	V _{DD} = 450 V
Gate to Drain Charge	Q _{GD}		40		nC	V _{GS} = 10 V
Body Diode Forward Voltage	V _{F(S-D)}		1.0		V	I _F = 10 A, V _{GS} = 0
Reverse Recovery Time	t _{rr}		990		ns	I _F = 10 A, V _{GS} = 0
Reverse Recovery Charge	Q _{rr}		7.0		μC	di/dt = 50 A/μs

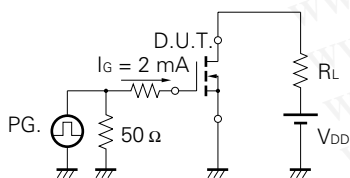
Test Circuit 1 Avalanche Capability



Test Circuit 2 Switching Time

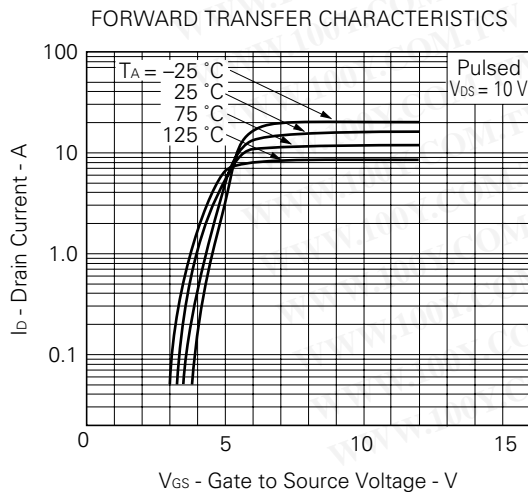
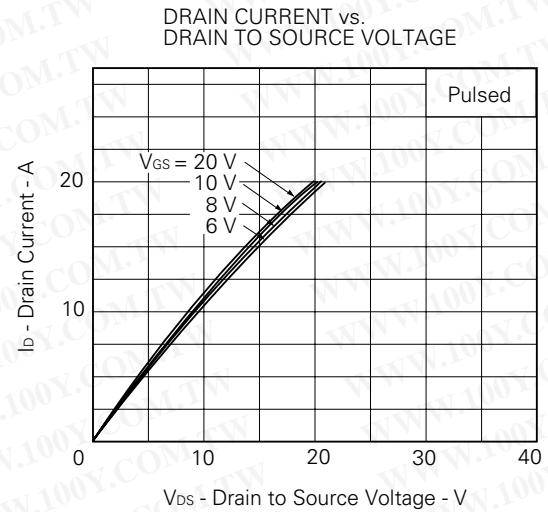
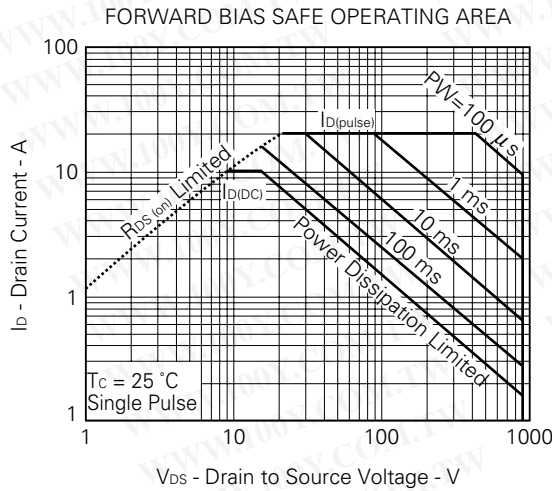
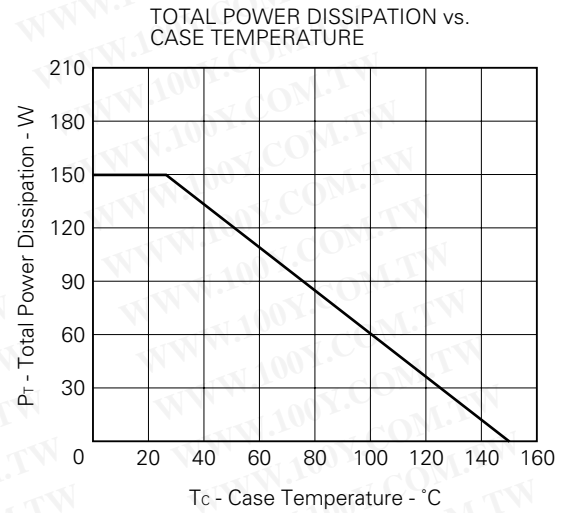
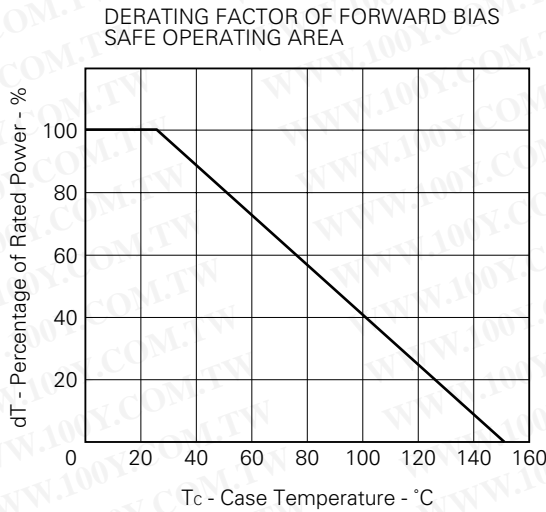


Test Circuit 3 Gate Charge

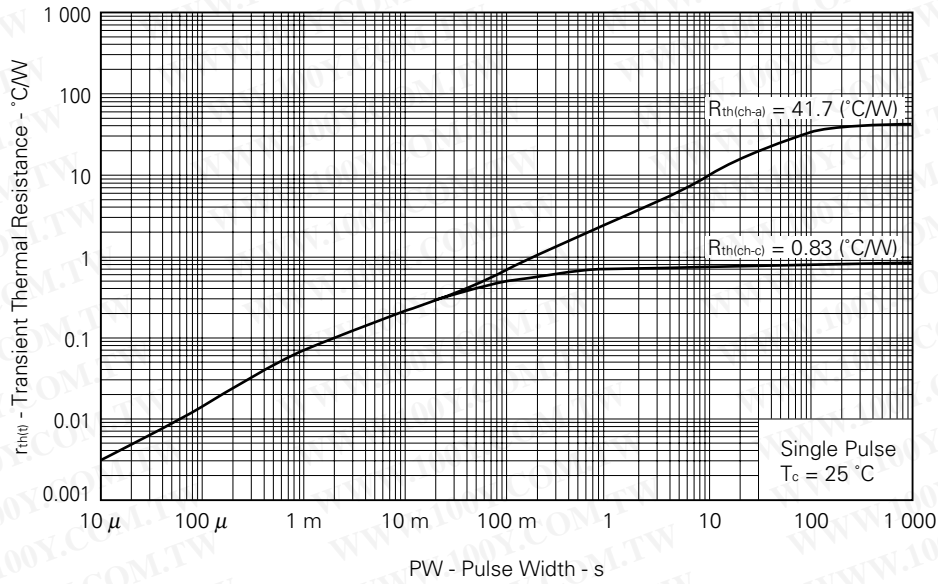


The application circuits and their parameters are for references only and are not intended for use in actual design-in's.

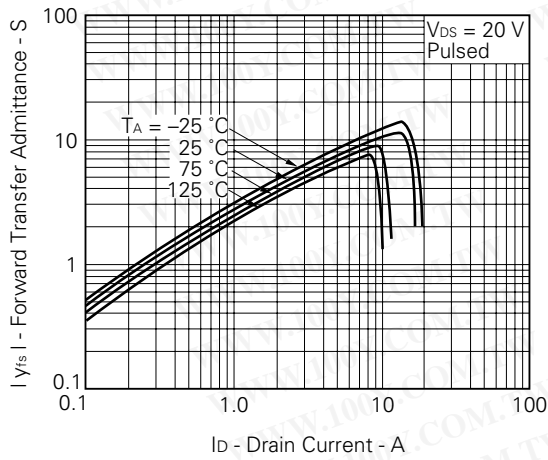
TYPICAL CHARACTERISTICS (T_A = 25 °C)



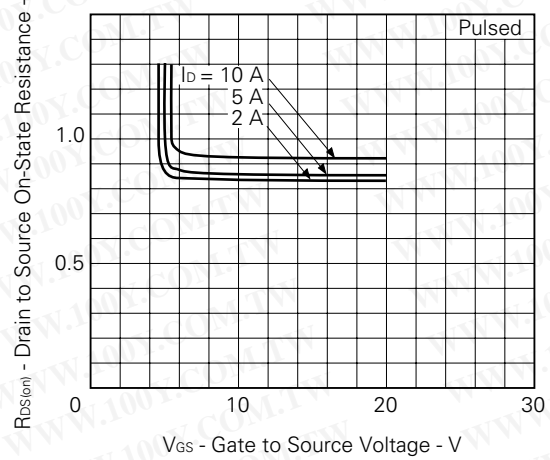
TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH



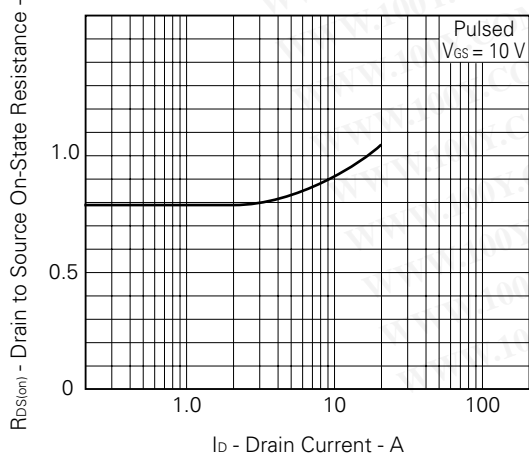
FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



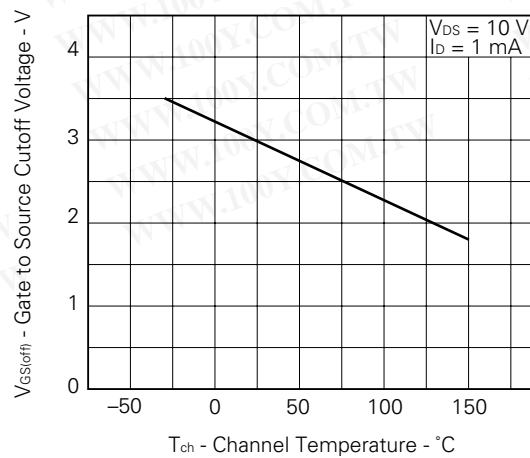
DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT

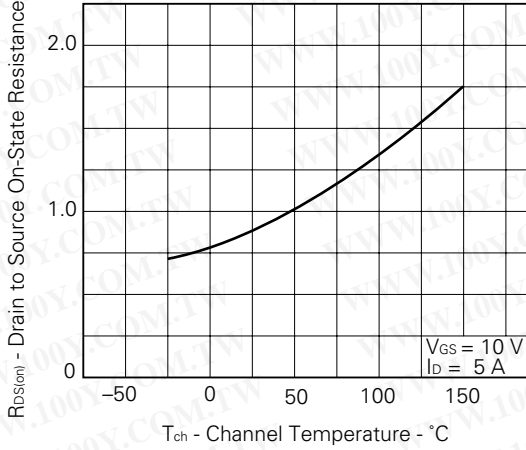


GATE TO SOURCE CUTOFF VOLTAGE vs. CHANNEL TEMPERATURE

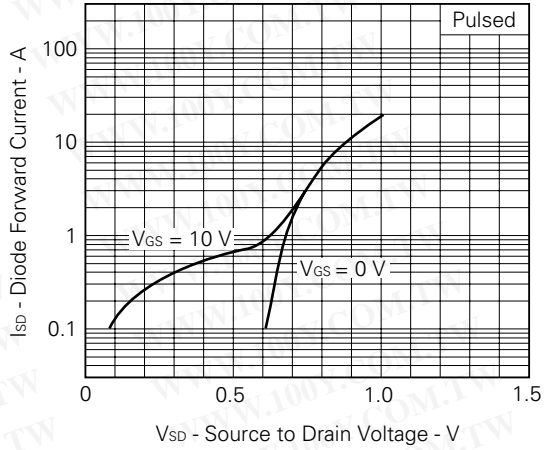


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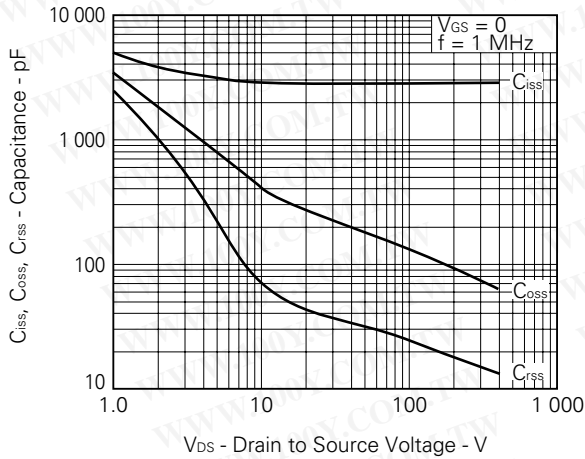
DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE



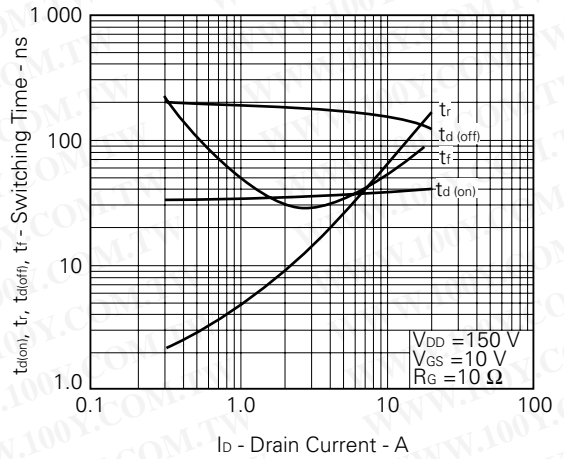
SOURCE TO DRAIN DIODE FORWARD VOLTAGE



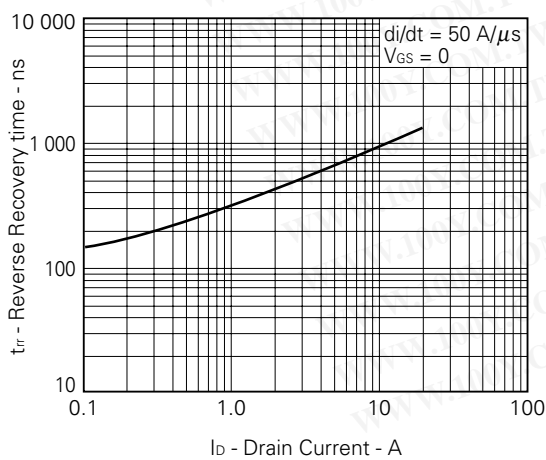
CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



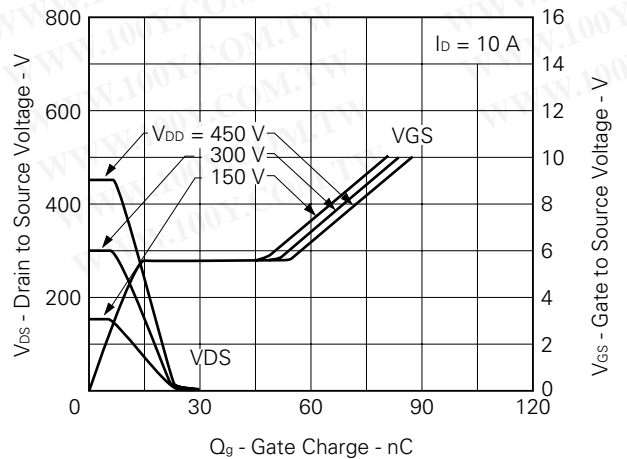
SWITCHING CHARACTERISTICS



REVERSE RECOVERY TIME vs. DRAIN CURRENT

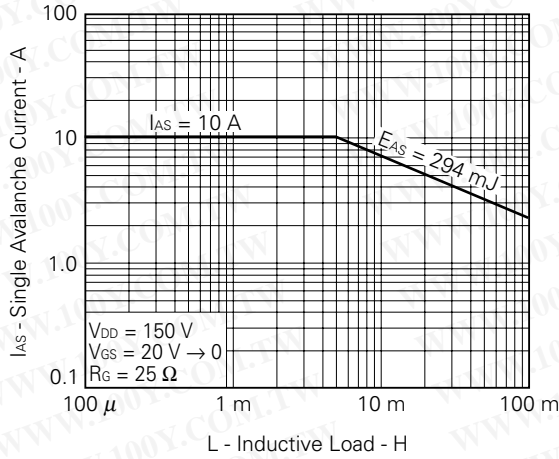


DYNAMIC INPUT/OUTPUT CHARACTERISTICS

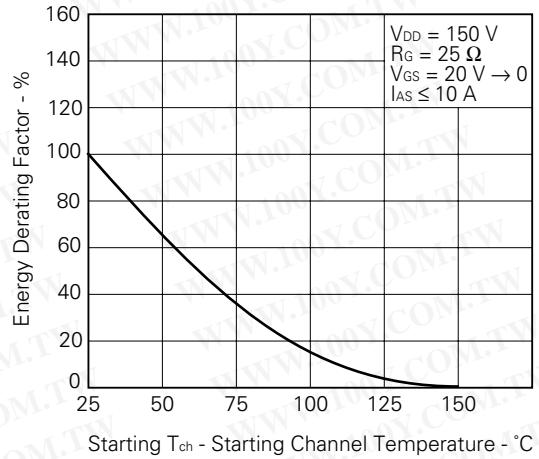


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SINGLE AVALANCHE CURRENT vs. INDUCTIVE LOAD



SINGLE AVALANCHE ENERGY DERATING FACTOR



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REFERENCE

Document Name	Document No.
NEC semiconductor device reliability/quality control system.	TEI-1202
Quality grade on NEC semiconductor devices.	IEI-1209
Semiconductor device mounting technology manual.	IEI-1207
Semiconductor device package manual.	IEI-1213
Guide to quality assurance for semiconductor devices.	MEI-1202
Semiconductor selection guide.	MF-1134
Power MOS FET features and application switching power supply.	TEA-1034
Application circuits using Power MOS FET.	TEA-1035
Safe operating area of Power MOS FET.	TEA-1037

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