



2N7002T

N-CHANNEL ENHANCEMENT MODE FIELD EFFECT TRANSISTOR

Features

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Ultra-Small Surface Mount Package
- Lead Free/RoHS Compliant (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability
- "Green" Device (Notes 3 and 4)

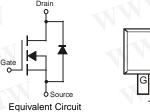
特力材料886-3-5753170 胜特力电子(上海) 86-21-34970699 胜特力电子(深圳) 86-755-83298787 Http://www.100y.com.tw

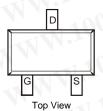


Mechanical Data

- Case: SOT523
- Case Material: Molded Plastic. "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals: Solderable per MIL-STD-202, Method 208
- Lead Free Plating (Matte Tin Finish annealed over Alloy 42 leadframe).
- Terminal Connections: See Diagram
- Marking Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.002 grams (approximate)

SOT523





Maximum Ratings @TA = 25°C unless otherwise specified

Cha	racteristic	Symbol	Value	Units
Drain-Source Voltage	1111	V _{DSS}	60	V
Drain-Gate Voltage R _{GS} ≤ 1.0N	ΜΩ	V_{DGR}	60	V
Gate-Source Voltage	Continuous Pulsed	V _{GSS}	±20 ±40	v 00 x •
Drain Current (Note 1)	Continuous Continuous @ 100°C Pulsed	Y.CO _{ID}	115 73 800	mA

Thermal Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 1)	P _d	150	mW
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	833	°C/W
Operating and Storage Temperature Range	T _j , T _{STG}	-55 to +150	°C

Notes:

- 1. Device mounted on FR-4 PCB 1.0 x 0.75 x 0.062 inch pad layout as shown on Diodes, Inc. suggested pad layout AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.
- No purposefully added lead.
- Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.
- Product manufactured with Date Code UO (week 40, 2007) and newer are built with Green Molding Compound. Product manufactured prior to Date Code UO are built with Non-Green Molding Compound and may contain Halogens or Sb2O3 Fire Retardants.

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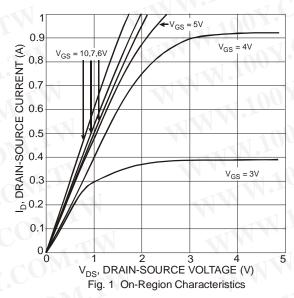
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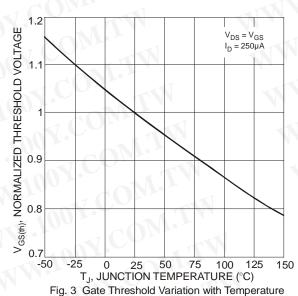
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Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristi	C	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 5)	CONT.					1.5	
Drain-Source Breakdown Voltage		BV _{DSS}	60		AT.	V	$V_{GS} = 0V, I_{D} = 10\mu A$
Zero Gate Voltage Drain Current	@ T _C = 25°C @ T _C = 125°C	I _{DSS}	_	_	1.0 500	μA	V _{DS} = 60V, V _{GS} = 0V
Gate-Body Leakage		I _{GSS}	_		±10	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 5)						- 131	
Gate Threshold Voltage	41 C.U.	V _{GS(th)}	1.0		2.0	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$
Static Drain-Source On-Resistance	@ T _i = 25°C		_	2.0	_	Ω	$V_{GS} = 5.0V, I_D = 0.05A$
17.1	@ T _j = 125°C	R _{DS} (ON)		4.4			$V_{GS} = 10V, I_D = 0.5A$
On-State Drain Current		I _{D(ON)}	0.5	1.0	4	Α	$V_{GS} = 10V, V_{DS} = 7.5V$
Forward Transconductance	THU - A	g _{FS}	80	_		mS	$V_{DS} = 10V, I_D = 0.2A$
DYNAMIC CHARACTERISTICS	-7 (1)	_ <	1			-11	11 (U)
Input Capacitance		C _{iss}	_	22	50	pF	10017.
Output Capacitance Reverse Transfer Capacitance		Coss	_	11	25	pF	$V_{DS} = 25V, V_{GS} = 0V, f = 1.0MHz$
		C _{rss}	1	2.0	5.0	pF	
SWITCHING CHARACTERISTICS	40117.		44				21 1 1 1 1 2
Turn-On Delay Time		t _{D(ON)}		7.0	20	ns	$V_{DD} = 30V, I_D = 0.2A,$
Turn-Off Delay Time		t _{D(OFF)}		11	20	ns	$R_L = 150\Omega$, $V_{GEN} = 10V$, $R_{GEN} = 250$

Notes: 5. Short duration pulse test used to minimize self-heating effect.





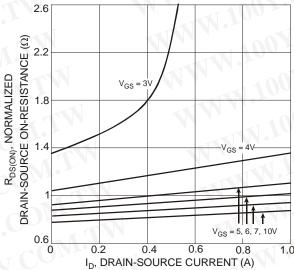


Fig. 2 On-Resistance Variation with Gate Voltage and Drain-Source Current

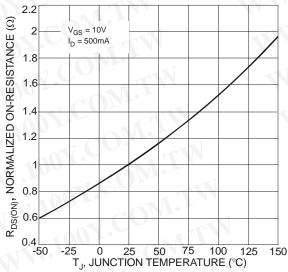
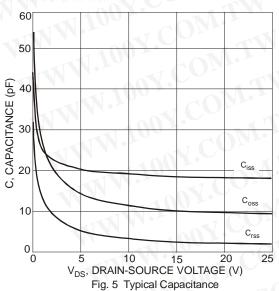


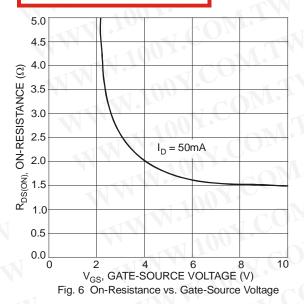
Fig. 4 On-Resistance Variation with Temperature





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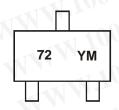


Ordering Information (Note 6)

Part Number	Qualification	Case	Packaging
2N7002T-7-F	Commercial	SOT523	3000/Tape & Reel
2N7002TQ-7-F	Automotive	SOT523	3000/Tape & Reel

Notes: 6. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information

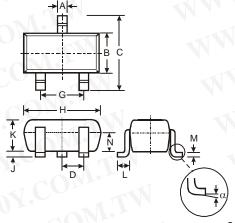


72 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: T = 2006) M = Month (ex: 9 = September)

Date Code Key

Year	2005		2006	2007		2008	2009	1.	2010	2011		2012
Code	S		T	U		V	W		X	Υ		Z
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

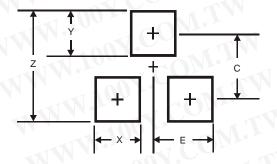
Package Outline Dimensions



SOT523						
Dim	Min	Max	Тур			
Α	0.15	0.30	0.22			
В	0.75	0.85	0.80			
C	1.45	1.75	1.60			
D	1		0.50			
G	0.90	1.10	1.00			
Н	1.50	1.70	1.60			
J	0.05					
K 0.60 0.80 0.7						
L	0.10	0.30	0.22			
М	0.10	0.20	0.12			
N	0.45	0.65	0.50			
α	0°	8°				
All Dimensions in mm						



Suggested Pad Layout



Dimensions	Value (in mm)
Z	1.8
Х	0.4
Y	0.51
С	1.3
E	0.7

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