



**BSS138** 

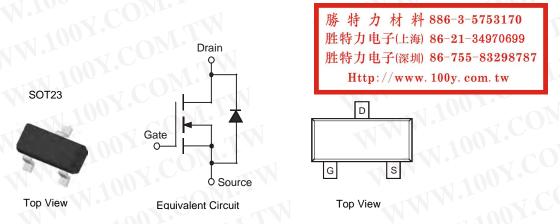
#### N-CHANNEL ENHANCEMENT MODE MOSFET

### **Features**

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Lead, Halogen and Antimony Free, RoHS Compliant (Note 1)
- "Green" Device (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability

### **Mechanical Data**

- Case: SOT23
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish annealed over Alloy 42 leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Weight: 0.008 grams (approximate)



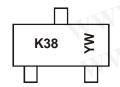
## Ordering Information (Note 3)

Part Num	ber	Qualification	Case	Packaging
BSS138-7	7-F	Commercial	SOT23	3000/Tape & Reel
BSS138Q-	·7-F	Automotive	SOT23	3000/Tape & Reel

Notes:

- 1. No purposefully added lead. Halogen and Antimony Free.
- Product manufactured with Data Code V9 (week 33, 2008) and newer are built with Green Molding Compound. Product manufactured prior to Date Code V9 are built with Non-Green Molding Compound and may contain Halogens or Sb<sub>2</sub>O<sub>3</sub> Fire Retardants.
- 3. For packaging details, go to our website at http://www.diodes.com.

## **Marking Information**



K38 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: N = 2002) M = Month (ex: 9 = September)

Date Code Key

Year	1998	1999	2000	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012
Code	J	K	L	М	N	Р	R	S	\T	U	V	W	Х	Υ	Z
Month	Jan	Fel	b I	Mar	Apr	May	Ju	n	Jul	Aug	Sep	Oc	t I	VoV	Dec
Code	V 1	2		3	4	5	6		7	8	9	0		N	D



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### Maximum Ratings @TA = 25°C unless otherwise specified

Characteristic	Symbol	Value	Units
Drain-Source Voltage	$V_{DSS}$	50	V
Drain-Gate Voltage $R_{GS} \le 20K\Omega$	$V_{DGR}$	50	V
Gate-Source Voltage Continuous	V	±20	V
Gate-Source Voltage Non repetitive, Pulse width<50µs	V <sub>GSS</sub>	±40	V
Drain Current Continuous	ID	200	mA
Pulsed Drain Current (10µs pulse duty cycle = 1%)	I <sub>DM</sub>	1	A

## Thermal Characteristics @TA = 25°C unless otherwise specified

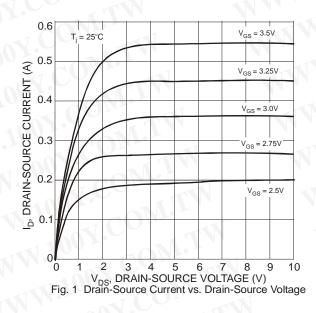
Characteristic	Symbol	Value	Units
Power Dissipation (Note 4)	P <sub>D</sub>	300	mW
Thermal Resistance, Junction to Ambient (Note 4)	$R_{\theta JA}$	417	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

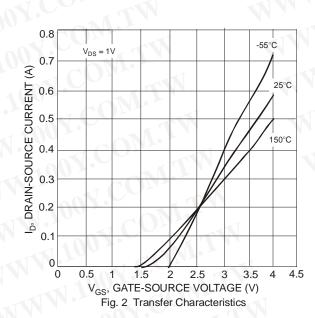
# Electrical Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 5)			N			
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	50	75	_	V	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	À	0.5	μΑ	$V_{DS} = 50V, V_{GS} = 0V$
Gate-Body Leakage	I <sub>GSS</sub>	-		±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 5)		V.				
Gate Threshold Voltage	V <sub>GS(th)</sub>	0.5	1.2	1.5	V	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$
Static Drain-Source On-Resistance	R <sub>DS</sub> (ON)		1.4	3.5	Ω	$V_{GS} = 10V, I_D = 0.22A$
Forward Transconductance	g <sub>FS</sub>	100	_		mS	$V_{DS} = 25V$ , $I_D = 0.2A$ , $f = 1.0KHz$
DYNAMIC CHARACTERISTICS					_	
Input Capacitance	C <sub>iss</sub>	-	1	50	pF	
Output Capacitance	Coss	Op.	_	25	pF	$V_{DS} = 10V, V_{GS} = 0V, f = 1.0MHz$
Reverse Transfer Capacitance	C <sub>rss</sub>			8.0	pF	1
SWITCHING CHARACTERISTICS			10		•	- 1111
Turn-On Delay Time	t <sub>D(ON)</sub>			20	ns	V 30V I 0 34 B 500
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	-	20	ns	$V_{DD} = 30V$ , $I_D = 0.2A$ , $R_{GEN} = 50\Omega$

Notes:

<sup>5.</sup> Short duration pulse test used to minimize self-heating effect





<sup>4.</sup> Device mounted on FR-5 PCB 1.0 x 0.75 x 0.062 inch pad layout as shown on Diodes, Inc. suggested pad layout AP02001, which can be found on our website at http://www.diodes.com.

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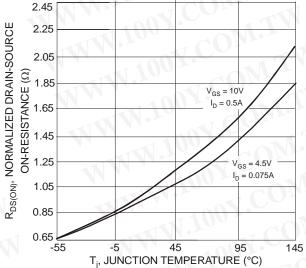


Fig. 3 Drain-Source On-Resistance vs. Junction Temperature

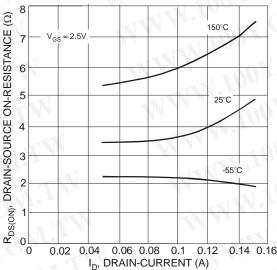


Fig. 5 Drain-Source On-Resistance vs. Drain-Current

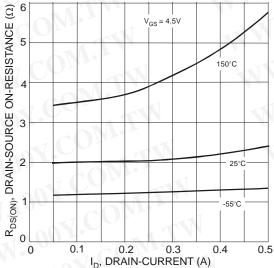


Fig. 7 Drain-Source On-Resistance vs. Drain-Current

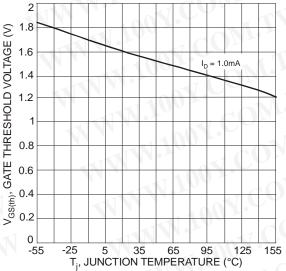


Fig. 4 Gate Threshold Voltage vs. Junction Temperature

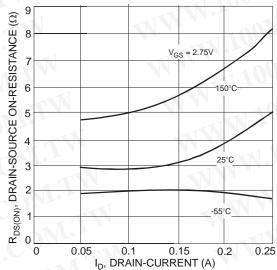


Fig. 6 Drain-Source On-Resistance vs. Drain-Current

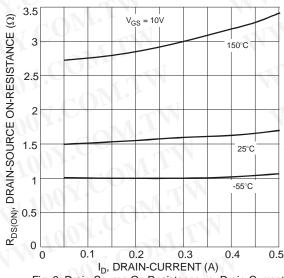


Fig. 8 Drain-Source On Resistance vs. Drain-Current



0.001 0.001 0.001 0.001 0.002 0.04 0.6 0.8 1.2 V<sub>SD</sub>, DIODE FORWARD VOLTAGE (V)

Fig. 9 Body Diode Current vs. Body Diode Voltage

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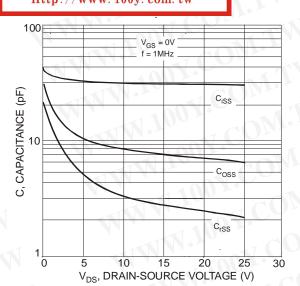
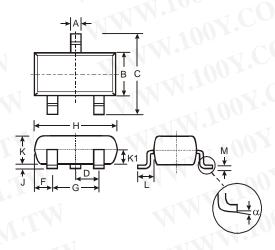


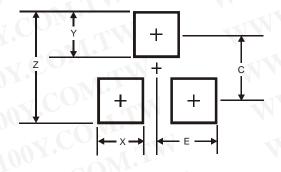
Fig. 10 Capacitance vs. Drain-Source Voltage

## **Package Outline Dimensions**



SOT23								
Dim	Min	Max	Тур					
Α	0.37	0.51	0.40					
В	1.20	1.40	1.30					
C	2.30	2.50	2.40					
D	0.89	1.03	0.915					
E	0.45	0.60	0.535					
G	1.78	2.05	1.83					
Ŧ	2.80	3.00	2.90					
7	0.013	0.10	0.05					
K	0.903	1.10	1.00					
K1	-	< 1=	0.400					
_	0.45	0.61	0.55					
M	0.085	0.18	0.11					
α	0°	8°	-					
All Dimensions in mm								

# **Suggested Pad Layout**



Dimensions	Value (in mm)				
Z	2.9				
X	0.8				
Υ	0.9				
С	2.0				
E	1.35				



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