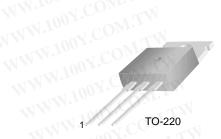


BDX33/A/B/C

Power Linear and Switching Applications

- High Gain General Purpose
- Power Darlington TR
- Complement to BDX34/34A/34B/34C respectively



1.Base 2.Collector 3.Emitter WWW.100Y.COM.T

NPN Epitaxial Silicon Transistor

Absolute Maximum Ratings T_C=25°C unless otherwise noted

45 60 80 100	V V V
60 80	COV
80	V
	V
100	V
	V
MW. 100	1.00
45	V
60	V
80	V
100	00 x A
10	Α
15	Α
0.25	(10 A)
70	W
150	°C
- 65 ~ 150	°C
	60 80 100 10 15 0.25

WWW.100Y.COM.TW 特力材料886-3-5753170 胜特力电子(上海) 86-21-54151736 胜特力电子(深圳) 86-755-83298787 WWW.100Y.COM.TW Http://www. 100y. com. tw

W.100Y.COM.TW

WWW.100Y.COM

WWW.100Y.COM.TW

W.100Y.COM.TW

©2000 Fairchild Semiconductor International

Electrical Characteristics T_C=25°C unless otherwise noted

noy.COM.TW

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
V _{CEO} (sus)	*Collector-Emitter Sustaining Voltage : BDX33 : BDX33A : BDX33B : BDX33C	I _C = 100mA I _B = 0	45 60 80 100	N TW		>
V _{CER} (sus)	* Collector-Emitter Sustaining Voltage : BDX33 : BDX33A : BDX33B : BDX33C	$I_{C} = 100 \text{mA}, I_{B} = 0$ $R_{BE} = 100 \Omega$	45 60 80 100	NT. M.T.	M 1	V V V
V _{CEV} (sus)	* Collector-Emitter Sustaining Voltage : BDX33 : BDX33A : BDX33B : BDX33C	I _C = 100mA, I _B = 0 V _{BE} = 1.5V	45 60 80 100	COM	TW TW	V V V
I _{CBO}	Collector Cut-off Current : BDX33 : BDX33A : BDX33B : BDX33C	$V_{CB} = 45V, I_{E} = 0$ $V_{CB} = 60V, I_{E} = 0$ $V_{CB} = 80V, I_{E} = 0$ $V_{CB} = 100V, I_{E} = 0$	M.100	07.CO	0.2 0.2 0.2 0.2	mA mA mA mA
ICEO	Collector Cut-off Current : BDX33 : BDX33A : BDX33B : BDX33C	$V_{CE} = 22V, I_B = 0$ $V_{CE} = 30V, I_B = 0$ $V_{CE} = 40V, I_B = 0$ $V_{CE} = 50V, I_B = 0$		1.100, 100,X	0.5 0.5 0.5 0.5	mA mA mA
I _{EBO}	Emitter Cut-off Current	$V_{EB} = 5V, I_{C} = 0$	-111	M.In.	5	mA
h _{FE} 100 Y	* DC Current Gain : BDX33/34 : BDX33B/33C	$V_{CE} = 3V, I_{C} = 4A$ $V_{CE} = 3V, I_{C} = 3A$	750 750	NW.1	⁷⁰⁰ 1.	MOS
V _{CE} (sat)	* Collector-Emitter Saturation Voltage : BDX33/33A : BDX33B/33C	I _C = 4A, I _B = 8mA I _C = 3A, I _B = 6mA		NWV	2.5 2.5	V
V _{BE} (on)	* Base-Emitter ON Voltage : BDX33/33A : BDX33B/33C	$V_{CE} = 3V, I_{C} = 4A$ $V_{CE} = 3V, I_{C} = 3A$	J	WW	2.5 2.5	V V
V _F	* Parallel Diode Forward Voltage	I _F = 8A	- T	-1	4	V

WWW.100Y.COM.TW

WWW.100Y.COM.TW

WWW.100Y.C

特力材料886-3-5753170 胜特力电子(上海) 86-21-54151736 胜特力电子(深圳) 86-755-83298787 Http://www. 100y. com. tw

WWW.100Y.COM.TW

<u>rw.</u>1007.COM.TW W.100Y.COM.TW WWW.100Y.COM. ©2000 Fairchild Semiconductor International Rev. A, February 2000

Pulse Test: PW=300μs, duty Cycle =1.5% Pulse

勝 特 力 材 料 886-3-5753170 胜特力电子(上海) 86-21-54151736 胜特力电子(深圳) 86-755-83298787

Http://www. 100y. com. tw

Typical Characteristics

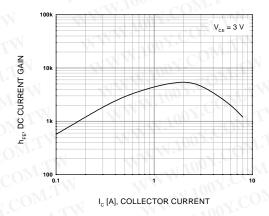


Figure 1. DC Current Gain

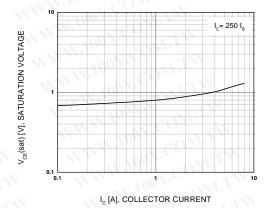


Figure 2. Collector-Emitter Saturation Voltage

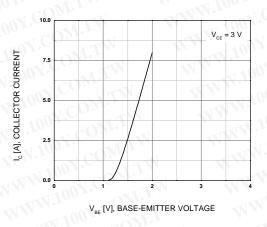


Figure 3. Base-Emitter On Voltage

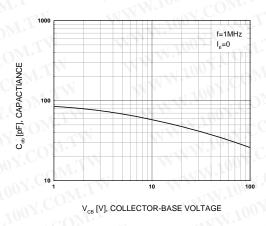


Figure 4. Output Capacitance

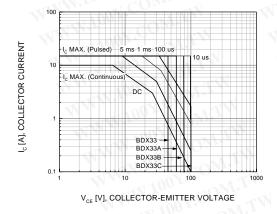


Figure 5. Safe Operating Area

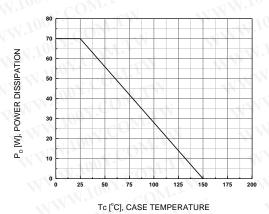


Figure 6. Power Derating

©2000 Fairchild Semiconductor International Rev. A, February 2000

Package Demensions WWW.100Y.COM.TW

Y.COM.TW

W.COM.TW box.COM.TW

100Y.COM.TW

W.100Y.COM.TW

WW.100Y.COM.T

OOY.COM.TW

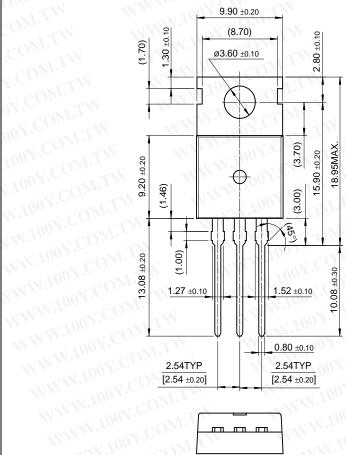
WWW.100Y.COM.TW WWW.100Y.COM.TW

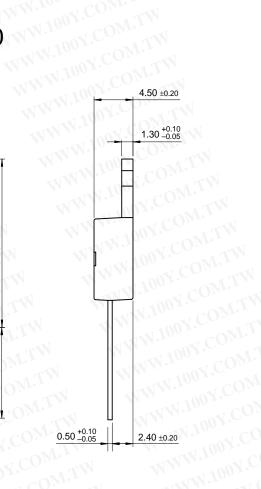
WWW.100Y.COM.TW

WWW.100X

100Y.COM.TW

WWW.100Y.COM.T





W.100Y.COM.TW

WWW. Too



WWW.100Y.COM. 特力材料886-3-5753170 胜特力电子(上海) 86-21-54151736 胜特力电子(深圳) 86-755-83298787 WWW.100Y.COM.TW Http://www. 100y. com. tw

W.100Y.COM.TW

WWW.100Y.COM

WWW.100Y

WWW.100Y.COM.T

WWW.100Y.COM.TW **Dimensions in Millimeters**

TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

FACT™ QFET¹ FACT Quiet Series™ QS™

FAST[®] Quiet Series[™] SuperSOT[™]-3 GTO[™] SuperSOT[™]-6

ISOPLANAR™ SyncFET™
MICROWIRE™ TinyLogic™
POP™ UHC™
PowerTrench® VCX™
QFET™

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR INTERNATIONAL.

As used herein:

- 1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
- 2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

SuperSOT™-8

PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition			
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.			
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.			
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.			
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.			

©2000 Fairchild Semiconductor International Rev. E