

FGB40N60SM

600 V, 40 A Field Stop IGBT

Features

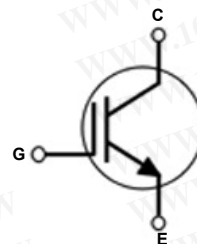
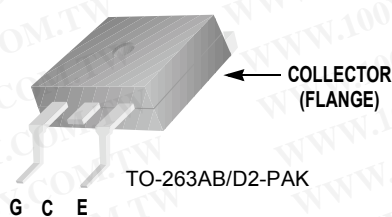
- Maximum Junction Temperature : $T_J = 175^{\circ}\text{C}$
- Positive Temperature Co-efficient for Easy Parallel Operating
- High Current Capability
- Low Saturation Voltage: $V_{CE(sat)} = 1.9 \text{ V(Typ.) @ } I_C = 40 \text{ A}$
- High Input Impedance
- Fast Switching: $E_{OFF} = 6.5 \text{ uJ/A/A}$
- Tighten Parameter Distribution
- RoHS Compliant
- IR Reflow Only

Applications

- Welder,, PFC

General Description

Using novel field stop IGBT technology, Fairchild®'s new series of Field Stop 2nd generation IGBTs offer the optimum performance for welder and PFC applications where low conduction and switching losses are essential. ®



Absolute Maximum Ratings

Symbol	Description	Ratings	Unit
V_{CES}	Collector to Emitter Voltage	600	V
V_{GES}	Gate to Emitter Voltage	± 20	V
I_C	Collector Current @ $T_C = 25^{\circ}\text{C}$	80	A
	Collector Current @ $T_C = 100^{\circ}\text{C}$	40	A
$I_{CM} (1)$	Pulsed Collector Current	120	A
P_D	Maximum Power Dissipation @ $T_C = 25^{\circ}\text{C}$	349	W
	Maximum Power Dissipation @ $T_C = 100^{\circ}\text{C}$	174	W
T_J	Operating Junction Temperature	-55 to +175	$^{\circ}\text{C}$
T_{stg}	Storage Temperature Range	-55 to +175	$^{\circ}\text{C}$
T_L	Maximum Lead Temp. for soldering Purposes, 1/8" from case for 5 seconds	300	$^{\circ}\text{C}$

Notes:

1: Repetitive rating: Pulse width limited by max. junction temperature

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JC}$ (IGBT)	Thermal Resistance, Junction to Case	-	0.43	$^{\circ}\text{C/W}$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	-	62.5	$^{\circ}\text{C/W}$

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FGB40N60SM	FGB40N60SM	TO-263AB/D2-PAK	-	-	50

Electrical Characteristics of the IGBT $T_C = 25^{\circ}\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
BV _{CES}	Collector to Emitter Breakdown Voltage	V _{GE} = 0V, I _C = 250μA	600	-	-	V
$\frac{\Delta BV_{CES}}{\Delta T_J}$	Temperature Coefficient of Breakdown Voltage	V _{GE} = 0V, I _C = 250μA	-	0.6	-	V/°C
I _{CES}	Collector Cut-Off Current	V _{CE} = V _{CES} , V _{GE} = 0V	-	-	250	μA
I _{GES}	G-E Leakage Current	V _{GE} = V _{GES} , V _{CE} = 0V	-	-	±400	nA
On Characteristics						
V _{GE(th)}	G-E Threshold Voltage	I _C = 250μA, V _{CE} = V _{GE}	3.5	4.5	6.0	V
V _{CE(sat)}	Collector to Emitter Saturation Voltage	I _C = 40A, V _{GE} = 15V	-	1.9	2.3	V
		I _C = 40A, V _{GE} = 15V, T _C = 175°C	-	2.1	-	V
Dynamic Characteristics						
C _{ies}	Input Capacitance	V _{CE} = 30V, V _{GE} = 0V, f = 1MHz	-	1880	-	pF
C _{oes}	Output Capacitance		-	180	-	pF
C _{res}	Reverse Transfer Capacitance		-	50	-	pF
Switching Characteristics						
t _{d(on)}	Turn-On Delay Time	V _{CC} = 400V, I _C = 40A, R _G = 6Ω, V _{GE} = 15V, Inductive Load, T _C = 25°C	-	12	16	ns
t _r	Rise Time		-	20	28	ns
t _{d(off)}	Turn-Off Delay Time		-	92	120	ns
t _f	Fall Time		-	13	17	ns
E _{on}	Turn-On Switching Loss		-	0.87	1.30	mJ
E _{off}	Turn-Off Switching Loss		-	0.26	0.34	mJ
E _{ts}	Total Switching Loss		-	1.13	1.64	mJ
t _{d(on)}	Turn-On Delay Time	V _{CC} = 400V, I _C = 40A, R _G = 6Ω, V _{GE} = 15V, Inductive Load, T _C = 175°C	-	15	-	ns
t _r	Rise Time		-	22	-	ns
t _{d(off)}	Turn-Off Delay Time		-	116	-	ns
t _f	Fall Time		-	16	-	ns
E _{on}	Turn-On Switching Loss		-	0.97	-	mJ
E _{off}	Turn-Off Switching Loss		-	0.60	-	mJ
E _{ts}	Total Switching Loss		-	1.57	-	mJ

Electrical Characteristics of the IGBT (Continued)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max	Unit
Q_g	Total Gate Charge	$V_{CE} = 400V, I_C = 40A,$ $V_{GE} = 15V$	-	119	180	nC
Q_{ge}	Gate to Emitter Charge		-	13	20	nC
Q_{gc}	Gate to Collector Charge		-	58	90	nC

Typical Performance Characteristics

Figure 1. Typical Output Characteristics

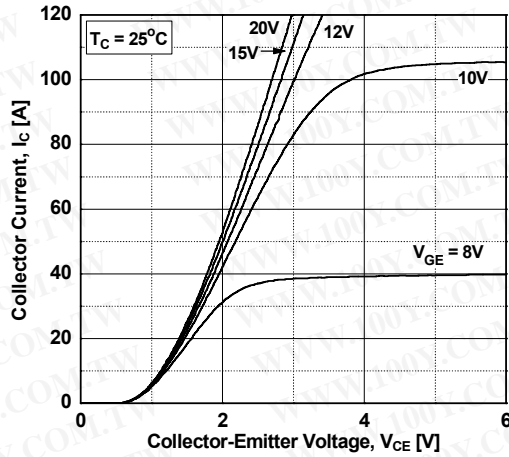


Figure 2. Typical Output Characteristics

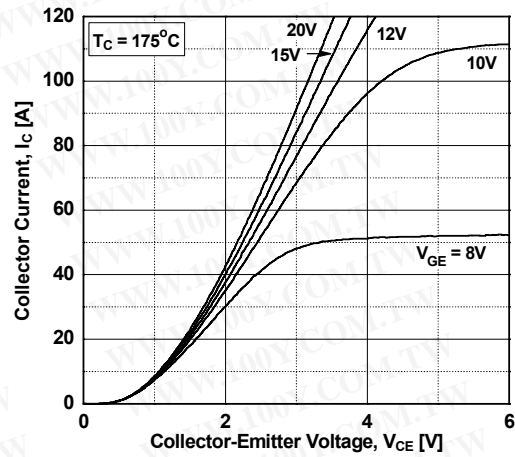


Figure 3. Typical Saturation Voltage Characteristics

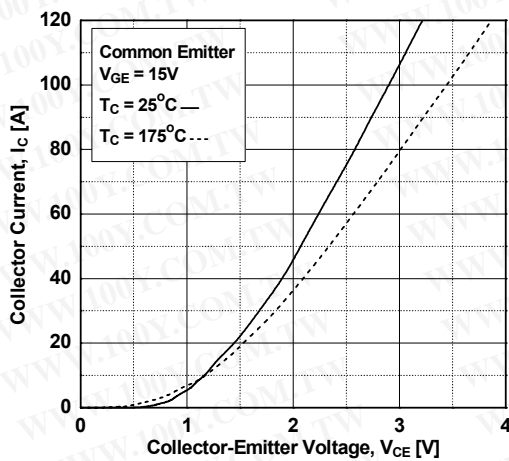


Figure 4. Transfer Characteristics

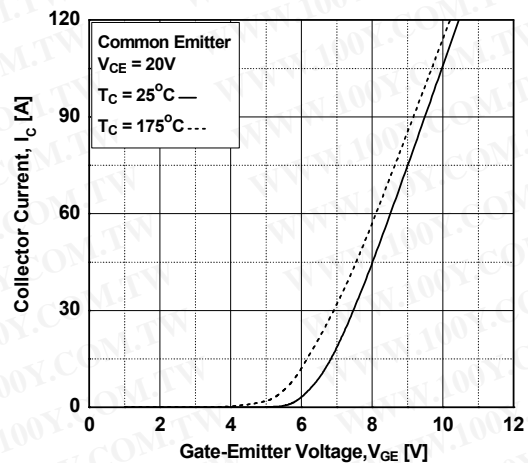


Figure 5. Saturation Voltage vs. Case Temperature at Variant Current Level

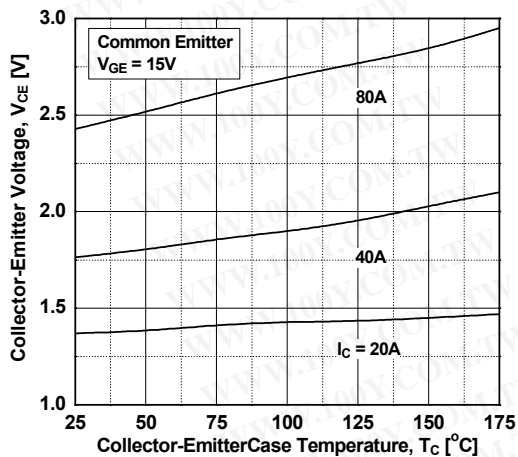
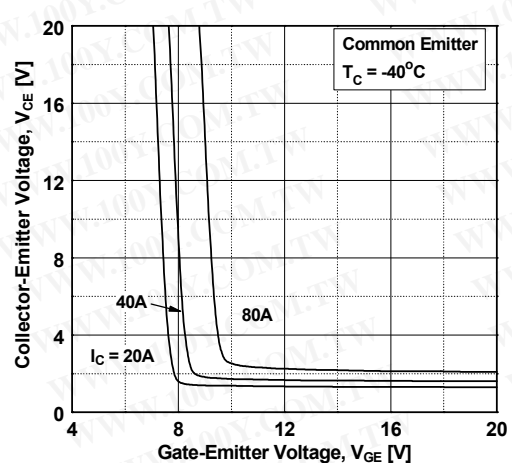


Figure 6. Saturation Voltage vs. Vge



Typical Performance Characteristics

Figure 7. Saturation Voltage vs. V_{GE}

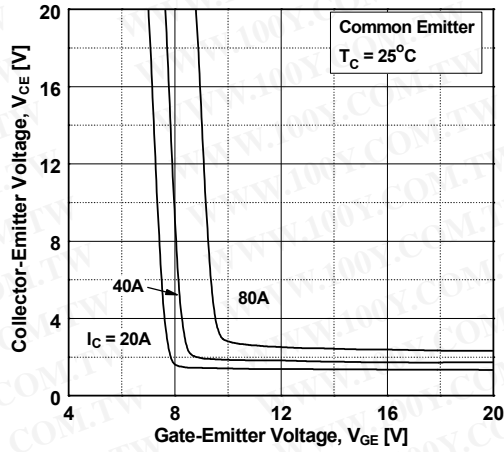


Figure 8. Saturation Voltage vs. V_{GE}

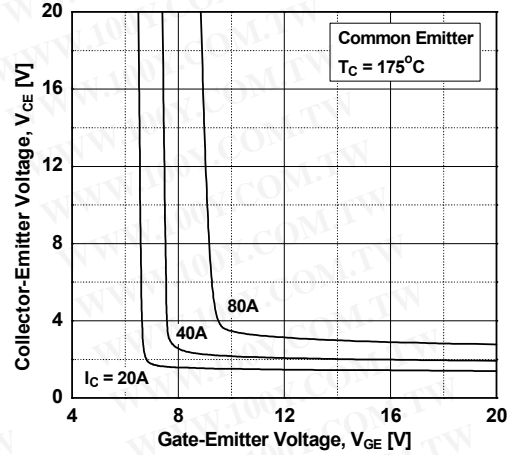


Figure 9. Capacitance Characteristics

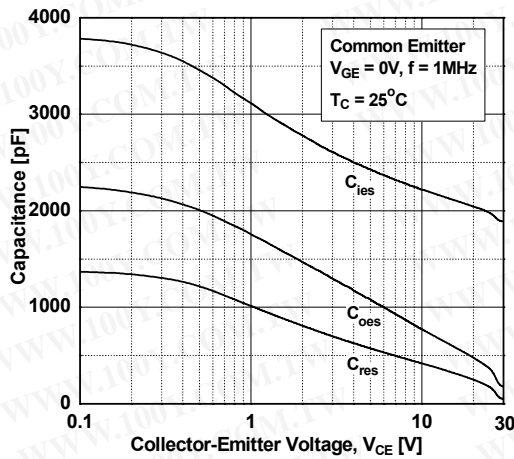


Figure 10. Gate charge Characteristics

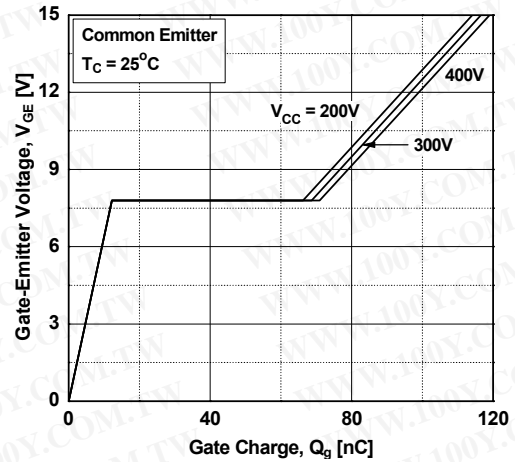


Figure 11. SOA Characteristics

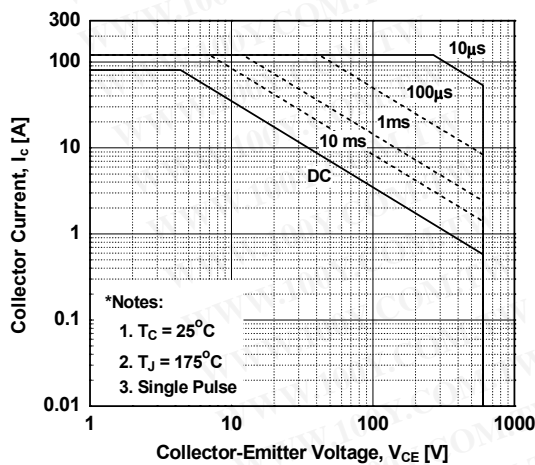
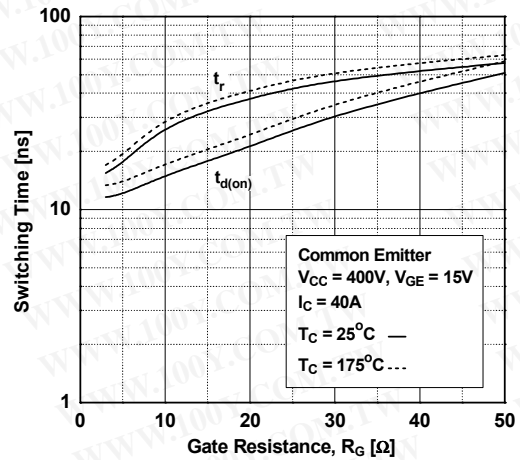


Figure 12. Turn-on Characteristics vs. Gate Resistance



Typical Performance Characteristics

Figure 13. Turn-off Characteristics vs. Gate Resistance

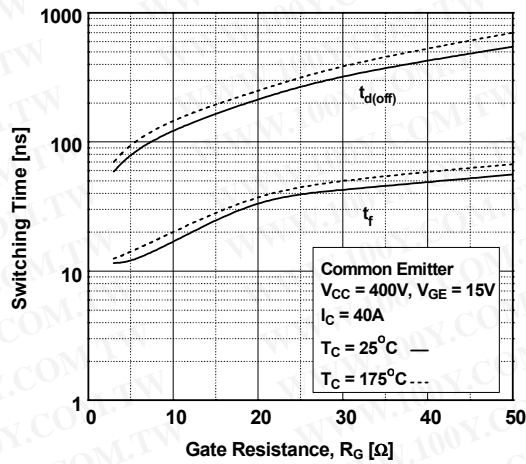


Figure 14. Turn-on Characteristics vs. Collector Current

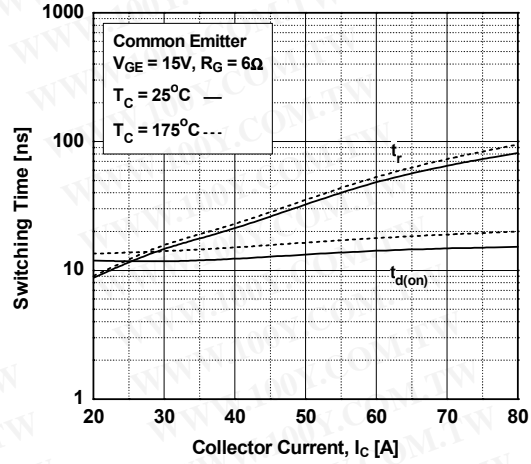


Figure 15. Turn-off Characteristics vs. Collector Current

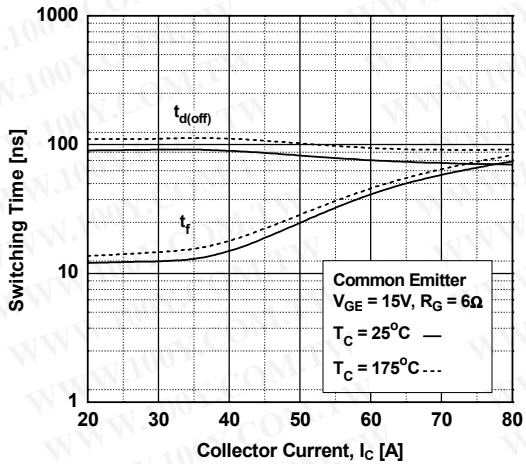


Figure 16. Switching Loss vs. Gate Resistance

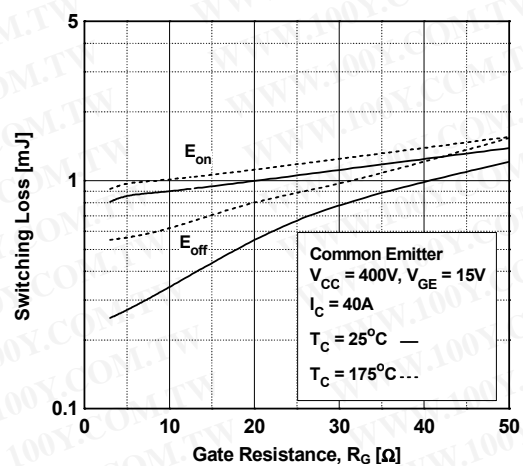


Figure 17. Switching Loss vs. Collector Current

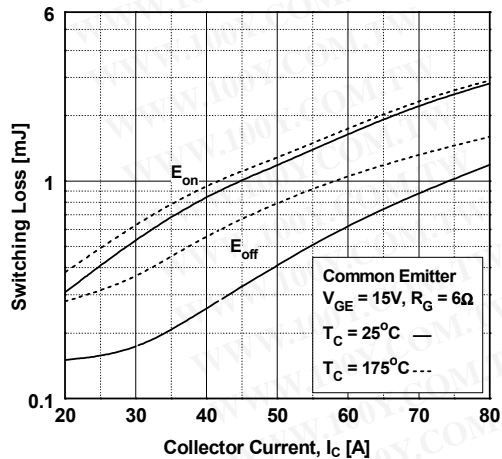
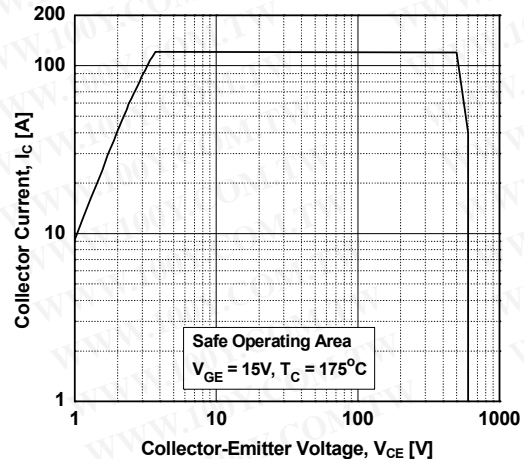


Figure 18. Turn off Switching SOA Characteristics



Typical Performance Characteristics

Figure 19. Current Derating

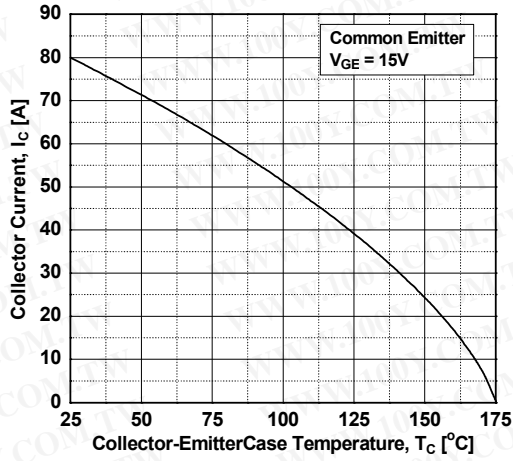


Figure 20. Load Current Vs. Frequency

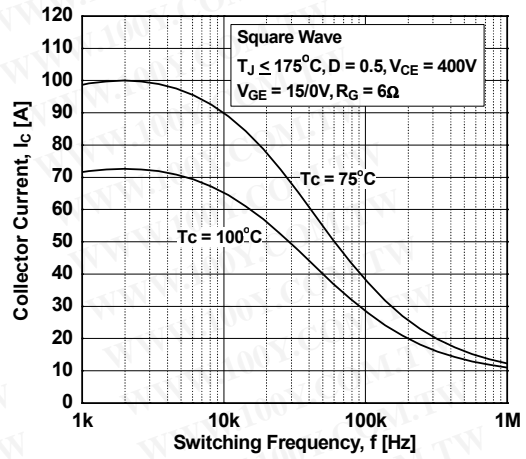
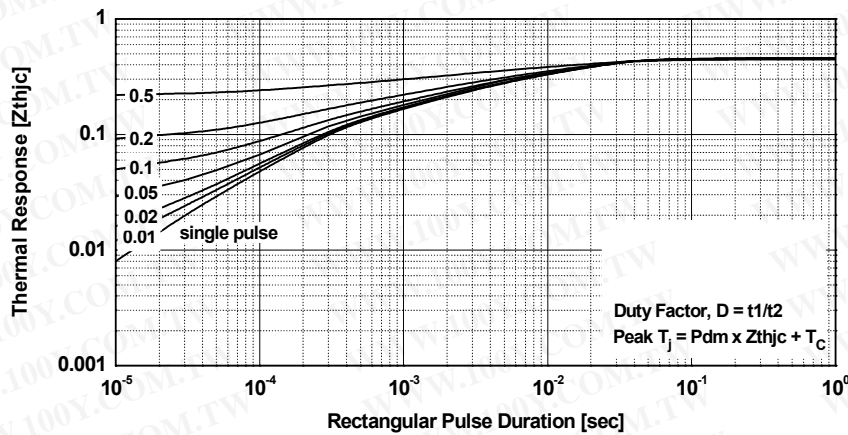
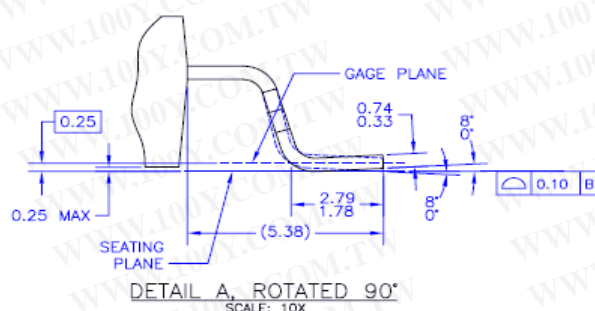
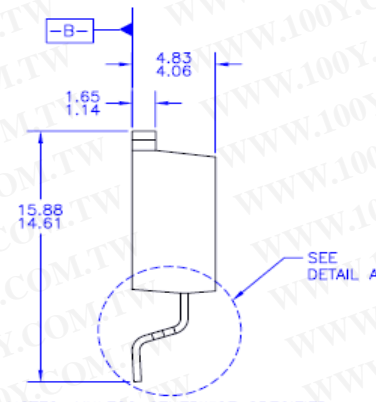
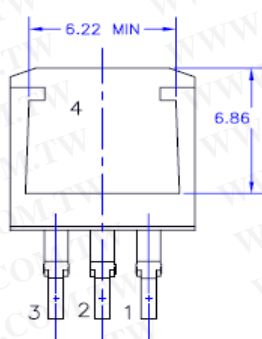
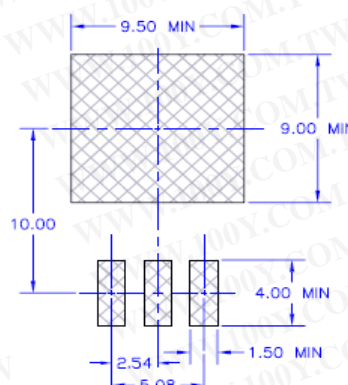
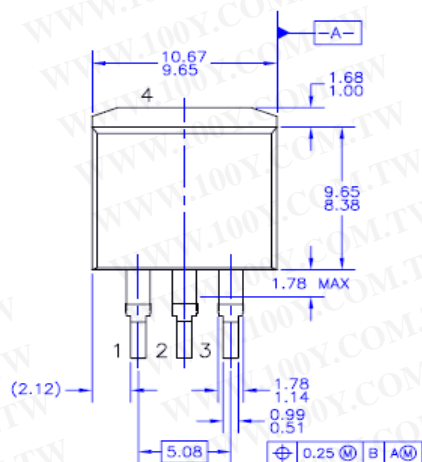


Figure 21. Transient Thermal Impedance of IGBT



Mechanical Dimensions

D²PAK





- NOTES: UNLESS OTHERWISE SPECIFIED
- A) PACKAGE OUTLINE CONFORMS TO JEDEC REGISTRATION TO-263, VARIATION AB.
 - B) ALL DIMENSIONS ARE IN MILLIMETERS.
 - C) DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
 - D) DIMENSIONS AND TOLERANCES PER ASME Y14.5M - 1994.
 - E) LOCATION OF THE PIN HOLE MAY VARY (LOWER LEFT CORNER, LOWER CENTER AND CENTER OF THE PACKAGE).
 - F) DRAWING FILENAME: TO263B03REV2

Dimensions in Millimeters

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Datasheet Identification	Product Status	Definition
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
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