FAIRCHILD SEMICONDUCTOR®

July 2008

Power-SPM™

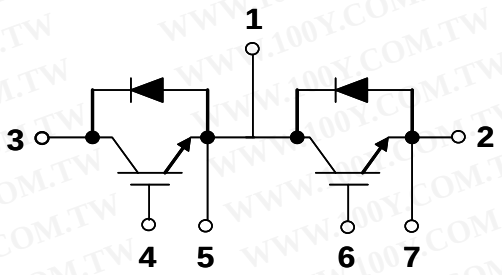
FP7G50US60

Transfer Molded Type IGBT Module

General Description

Fairchild's New IGBT Modules (Transfer Molded Type) provide low conduction and switching losses as well as short circuit ruggedness. They are designed for applications such as Motor control, Uninterrupted Power Supplies (UPS) and general Inverters where short circuit ruggedness is a required feature.

Package Code : EPM7



Internal Circuit Diagram

Features

- Short Circuit rated 10us @Tc=100°C, Vge=15V
- High Speed Switching
- Low Saturation Voltage : Vce(sat) =2.2V @Ic=50A
- High Input Impedance
- Fast & Soft Anti-Parallel FWD

Application

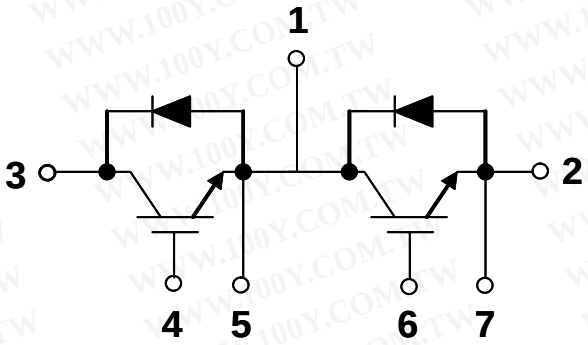
- Welders
- AC & DC Motor Controls
- General Purpose Inverters
- Robotics
- Servo Controls
- UPS

Absolute Maximum Ratings

Symbol	Description		Rating	Units
V_{CES}	Collector-Emitter Voltage		600	V
V_{GES}	Gate-Emitter Voltage		± 20	V
I_C	Collector Current	@ T_C = 25°C	50	A
$I_{CM\ (1)}$	Pulsed Collector Current		100	A
I_F	Diode Continuous Forward Current	@ T_C = 100°C	50	A
I_{FM}	Diode Maximum Forward Current		100	A
T_{SC}	Short Circuit Withstand Time	@ T_C = 100°C	10	us
P_D	Maximum Power Dissipation	@ T_C = 25°C	250	W
T_J	Operating Junction Temperature		-40 to +125	°C
T_{stg}	Storage Temperature Range		-40 to +125	°C
V_{iso}	Isolation Voltage	@ AC 1minute	2500	V
Mounting Torque	Power Terminals Screw : M5		2.0	N.m
	Mounting Screw : M5		2.0	N.m

Pin Configuration and Pin Description

Top View



Internal Circuit Diagram

Pin Description

Pin Number	Pin Description
1	Emitter of Q1, IGBT, Collector of Q2, IGBT
2	Emitter of Q2, IGBT
3	Collector of Q1, IGBT
4	Gate of Q1, IGBT
5	Emitter of Q1, IGBT
6	Gate of Q2, IGBT
7	Emitter of Q2, IGBT

Electrical Characteristics (T_J = 25°C, Unless Otherwise Specified)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
BV_{CES}	Collector-Emitter Breakdown Voltage	V_{GE}= 0V, I_C = 250μA	600	-	-	V
$\Delta BV_{CES}/\Delta T_J$	Temperature Coeff. of Breakdown Voltage	V_{GE}= 0V, I_C = 1mA	-	0.6	-	V
I_{CES}	Collector Cut-off Current	V_{CE}= V_{CES}, V_{GE}= 0V	-	-	250	uA
I_{GES}	Gate-Emitter Leakage Current	V_{GE}= V_{GES}, V_{CE}= 0V	-	-	±100	nA
On Characteristics						
$V_{GE(th)}$	G-E Threshold Voltage	V_{GE} = 0V, I_C=50mA	5.0	6.0	8.5	V
$V_{CE(sat)}$	Collector to Emitter Saturation Voltage	I_C = 50A, V_{GE} = 15V	-	2.2	2.8	V
Dynamic Characteristics						
C_{ies}	Input Capacitance	V_{CE} = 30V, V_{GE} = 0V, f = 1MHz		2920		pF
C_{oes}	Output Capacitance			400		pF
C_{res}	Reverse Capacitance			75		pF
Switching Characteristics						
$t_{d(on)}$	Turn-On Delay Time	V_{CC} = 300 V, I_C = 50A, R_G = 5.9Ω, V_{GE} = 15V Inductive Load, T_C = 25°C	-	58	-	ns
t_r	Rise Time		-	40	-	ns
$t_{d(off)}$	Turn-Off Delay Time		-	107	-	ns
t_f	Fall Time		-	140	-	ns
E_{on}	Turn-On Switching Loss		-	0.75	-	mJ
E_{off}	Turn-Off Switching Loss		-	0.54	-	mJ
E_{ts}	Total Switching Loss		-	1.29	-	mJ
$t_{d(on)}$	Turn-On Delay Time	V_{CC} = 300 V, I_C = 50A, R_G = 5.9Ω, V_{GE} = 15V Inductive Load, T_C = 125°C	-	53	-	ns
t_r	Rise Time		-	40	-	ns
$t_{d(off)}$	Turn-Off Delay Time		-	106	-	ns
t_f	Fall Time		-	274	-	ns
E_{on}	Turn-On Switching Loss		-	1.09	-	mJ
E_{off}	Turn-Off Switching Loss		-	1.68	-	mJ
E_{ts}	Total Switching Loss		-	2.77	-	mJ
T_{sc}	Short Circuit Withstand Time	V_{CC} = 300 V, V_{GE} = 15V @ T_C = 100°C	10	-	-	us
Q_g	Total Gate Charge	V_{CE} = 300 V, I_C = 50A, V_{GE} = 15V	-	136	-	nC
Q_{ge}	Gate-Emitter Charge		-	26	-	nC
Q_{gc}	Gate-Collector Charge		-	76	-	nC

Electrical Characteristics of DIODE (T_J = 25°C, Unless Otherwise Specified)

Symbol	Parameter	Conditions		Min	Typ	Max	Units
V_{FM}	Diode Forward Voltage	I_F = 50A	T_C = 25°C	-	1.9	2.8	V
			T_C = 100°C	-	1.8	-	
t_{rr}	Diode Reverse Recovery Time	I_F = 50A di / dt = 100 A/us	T_C = 25°C	-	76	100	ns
			T_C = 100°C	-	138		
I_{rr}	Diode Peak Reverse Recovery Current		T_C = 25°C	-	4	5.2	A
			T_C = 100°C	-	6		
Q_{rr}	Diode Reverse Recovery Charge		T_C = 25°C	-	152	260	nC
			T_C = 100°C	-	404		

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Units
$R_{\theta JC}$	Junction-to-Case (IGBT Part, per 1/2 Module)	-	0.4	°C/W
$R_{\theta JC}$	Junction-to-Case (DIODE Part, per 1/2 Module)	-	1.0	°C/W
$R_{\theta CS}$	Case-to-Sink (Conductive grease applied)	0.05	-	°C/W
Weight	Weight of Module	-	90	g

Typical Performance Characteristics

Fig 1. Typical Output Characteristics

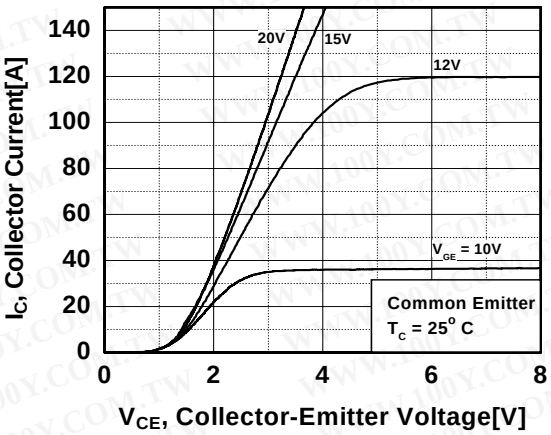


Fig 2. Typical Saturation Voltage Characteristics

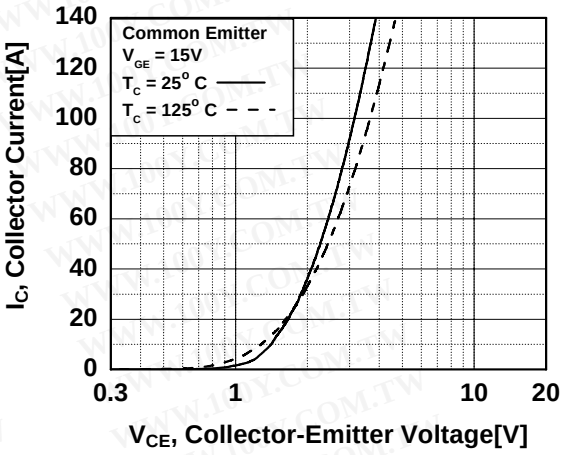


Fig 3. Saturation Voltage vs. Case Temperature at Variant Current Level

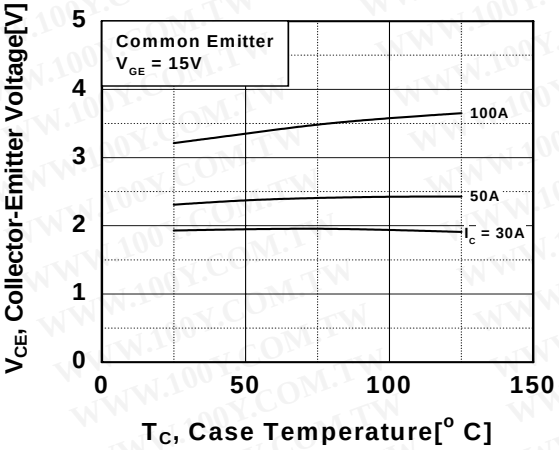


Fig 4. Load Current vs. Frequency

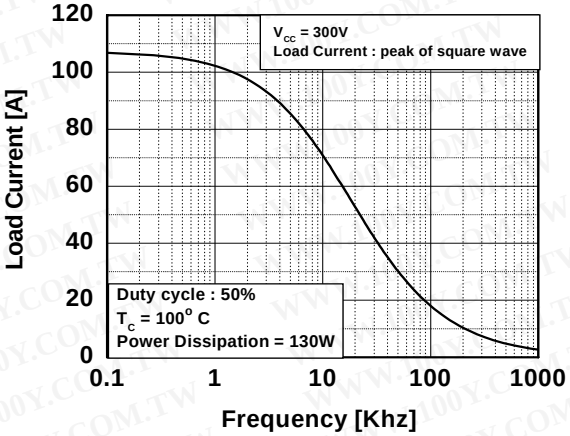


Fig 5. Saturation Voltage vs. V_{GE}

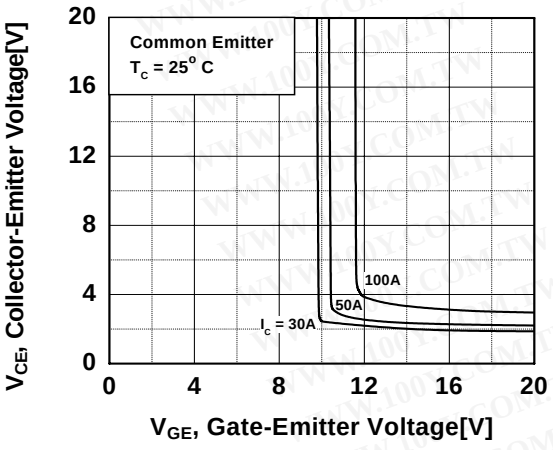


Fig 6. Saturation Voltage vs. V_{GE}

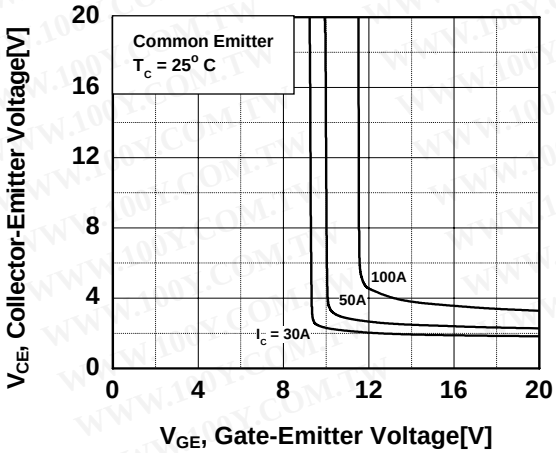


Fig 7. Capacitance Characteristics

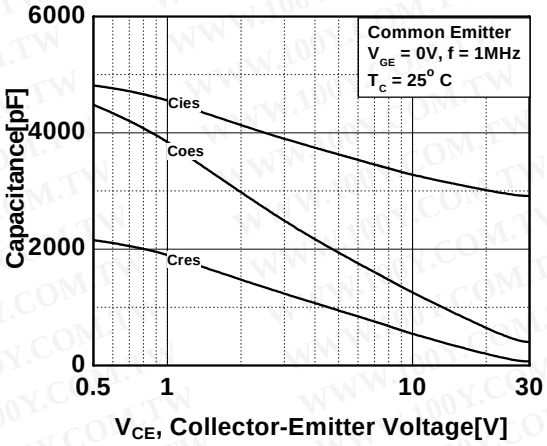


Fig 8. Turn-On Characteristics vs. Gate Resistance

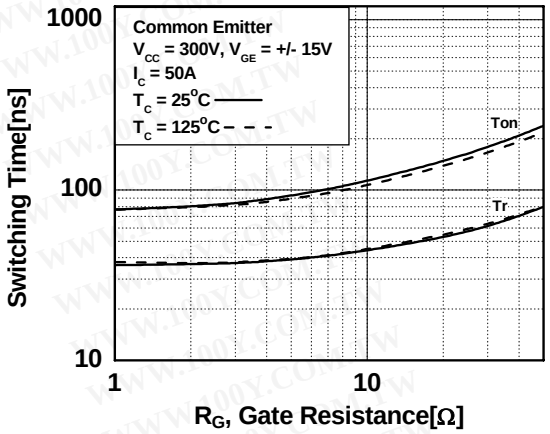


Fig 9. Turn-Off Characteristics vs. Gate Resistance

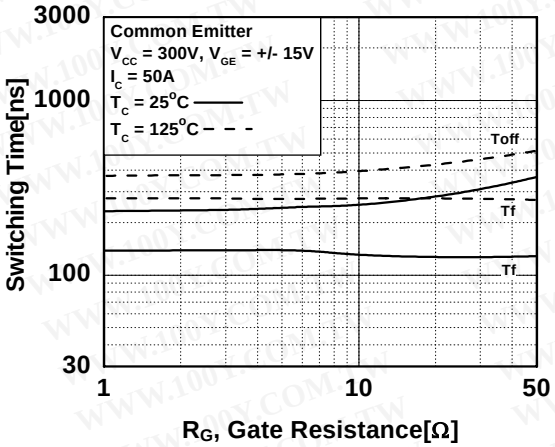


Fig 10. Switching Loss vs. Gate Resistance

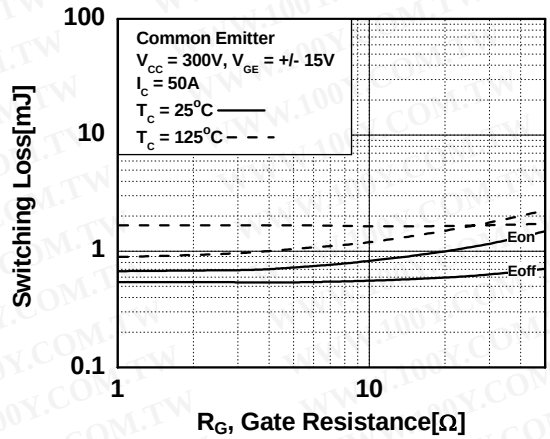


Fig 11. Turn-On Characteristics vs. Collector Current

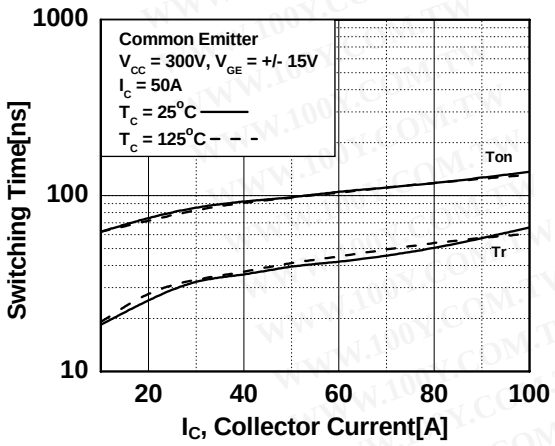


Fig 12. Turn-Off Characteristics vs. Collector Current

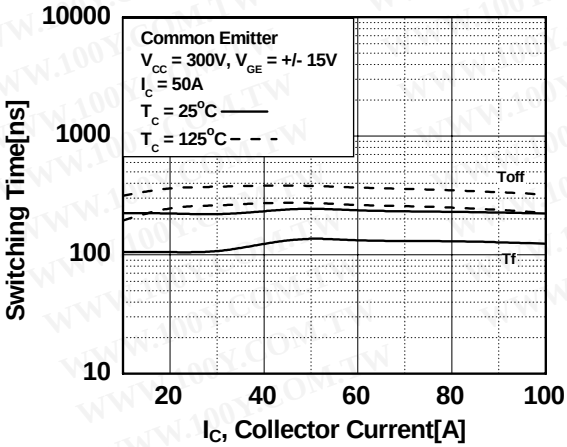


Fig 13. Switching Loss vs. Collector

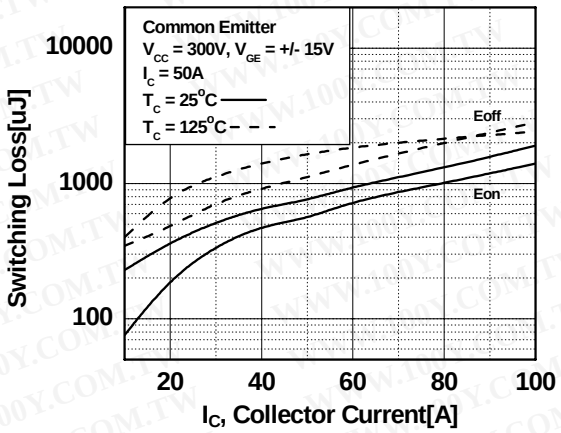


Fig 14. Gate Charge Characteristics

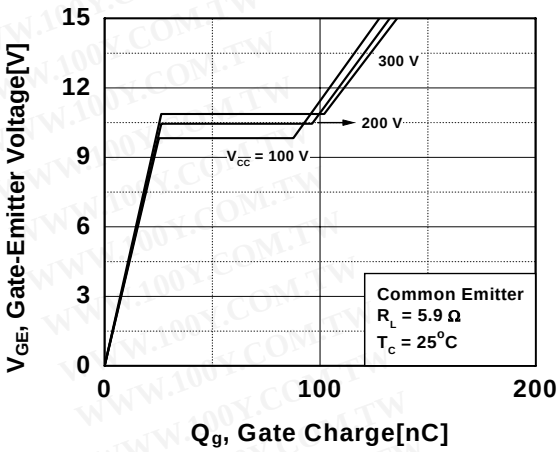


Fig 15. SOA Characteristics

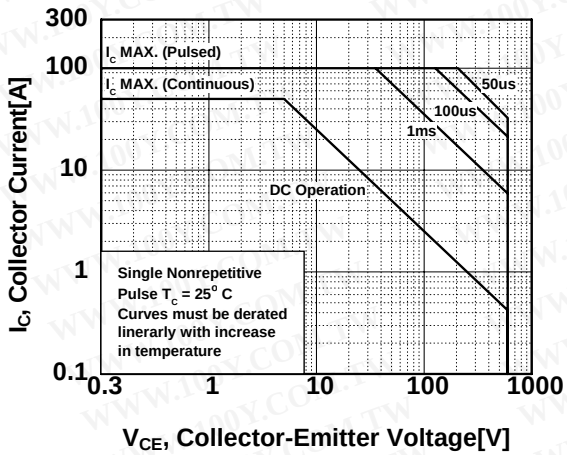


Fig 16. Turn-Off SOA Characteristics

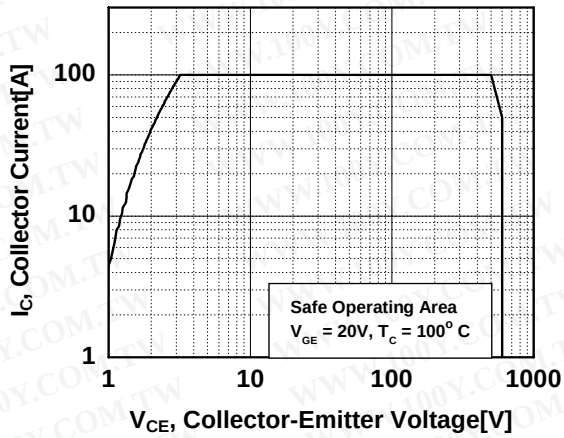


Fig 17. RBSOA Characteristics

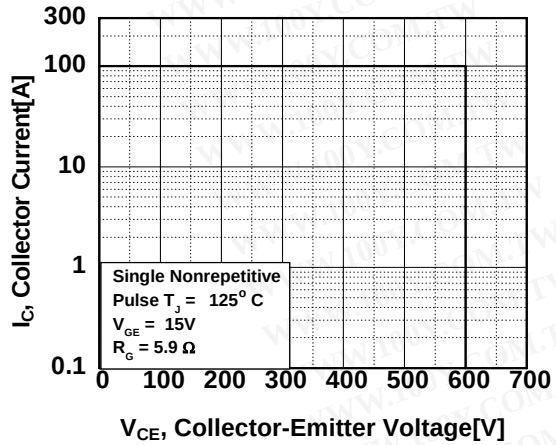


Fig 18. Transient Thermal Impedance

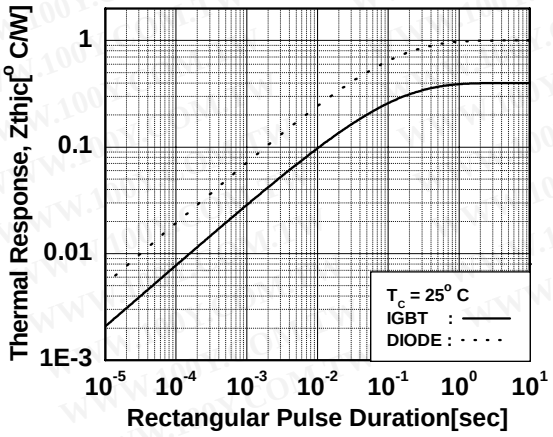


Fig 19. Forward Characteristics

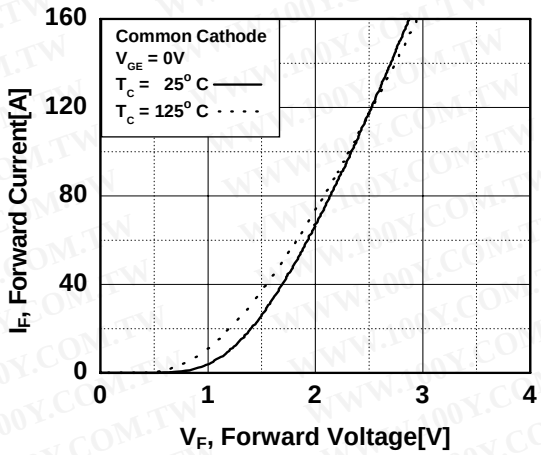
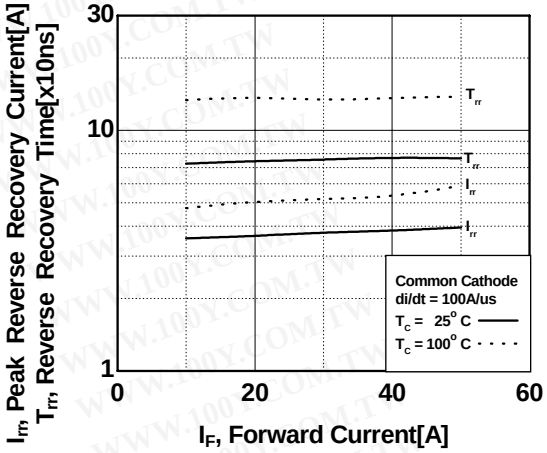
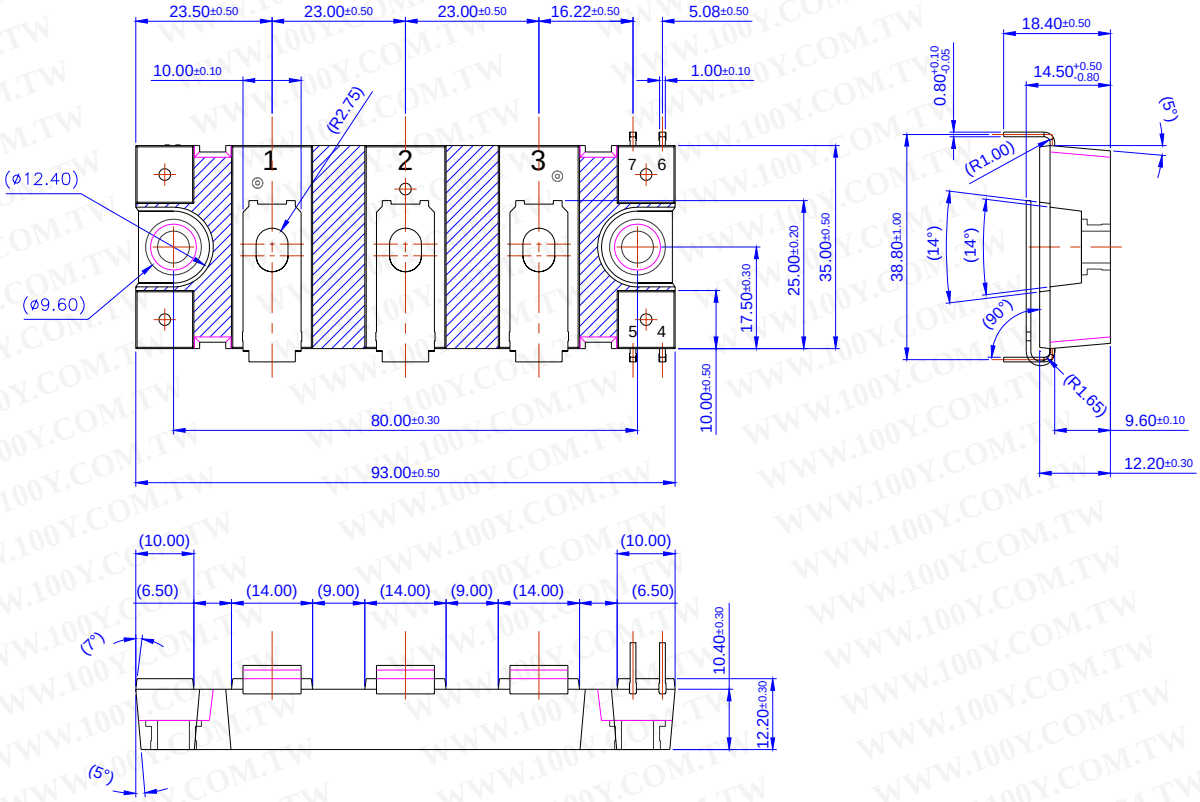


Fig 20. Reverse Recovery Characteristics




23.50±0.50
23.00±0.50
23.00±0.50
16.22±0.50
5.08±0.50
10.00±0.10
1.00±0.10
(R2.75)
(ø12.40)
(ø9.60)
1
2
3
7
6
5
4
25.00±0.20
35.00±0.50
17.50±0.30
10.00±0.50
80.00±0.30
93.00±0.50
18.40±0.50
0.80+0.10 -0.05
14.50+0.50 -0.80
(5°)
(R1.00)
38.80±1.00
(14°)
(14°)
(90°)
(R1.65)
9.60±0.10
12.20±0.30
(10.00)
(10.00)
(6.50)
(14.00)
(9.00)
(14.00)
(9.00)
(14.00)
(6.50)
(7°)
(5°)
10.40±0.30
12.20±0.30

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No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
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