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TIP30 Series(TIP30/30A/30B/30C)

Medium Power Linear Switching Applications

· Complementary to TIP29/29A/29B/29C



PNP Epitaxial Silicon Transistor

1.Base 2.Collector 3.Emitter

Absolute Maximum Ratings T_C=25°C unless otherwise noted

Symbol	M MM.	Parameter	Value	Units
V_{CBO}	Collector-Base Voltage	: TIP30	- 40	V
	LA A	: TIP30A	- 60	V
¹ CO _M	TIN TIN	: TIP30B	- 80	V
Mo	11.	: TIP30C	- 100	V
V _{CEO}	Collector-Emitter Voltage	: TIP30	- 40	V
00X.CO	1.1	: TIP30A	- 60	$CO_{D}A$
	TW	: TIP30B	- 80	V
	NI.	: TIP30C	- 100	COV
V_{EBO}	Emitter-Base Voltage		- 5	V
I _C	Collector Current (DC)		W-1 10	Α
I _{CP}	Collector Current (Pulse)		- 3	A
I _B	Base Current		- 0.4	A
P _C	Collector Dissipation (T _C =25°C)		30	W
P _C	Collector Dissipation (T _a =25°C)		2	w
T _{J-1} 100	Junction Temperature	W. 100 2	150	°C
T _{STG}	Storage Temperature	MM. TOOX.CO	- 65 ~ 150	°C

Electrical Characteristics T_C=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Max.	Units
V _{CEO} (sus)	* Collector-Emitter Sustaining Voltage : TIP30 : TIP30A : TIP30B : TIP30C	$I_{C} = -30 \text{mA}, I_{B} = 0$	-40 -60 -80 -100		V V V
I _{CEO}	Collector Cut-off Current : TIP30/30A : TIP30B/30C	$V_{CE} = -30V, I_{B} = 0$ $V_{CE} = -60V, I_{B} = 0$	1	-0.3 -0.3	mA mA
I _{CES}	Collector Cut-off Current : TIP30 : TIP30A : TIP30B : TIP30C	$V_{CE} = -40V, V_{EB} = 0$ $V_{CE} = -60V, V_{EB} = 0$ $V_{CE} = -80V, V_{EB} = 0$ $V_{CE} = -100V, V_{EB} = 0$		-200 -200 -200 -200	μΑ μΑ μΑ μΑ
I _{EBO}	Emitter Cut-off Current	$V_{EB} = -5V, I_{C} = 0$	WT	-1.0	mA
h _{FE}	* DC Current Gain	$V_{CE} = -4V, I_{C} = -0.2A$ $V_{CE} = -4V, I_{C} = -1A$	40 15	75	W
V _{CE} (sat)	* Collector-Emitter Saturation Voltage	I _C = -1A, I _B = -125mA		-0.7	V
V _{BE} (sat)	* Base-Emitter Saturation Voltage	$V_{CE} = -4V, I_{C} = -1A$	G_{Mr}	-1.3	V
f _T	Current Gain Bandwidth Product	$V_{CE} = -10V, I_{C} = -200mA$	3.0	M.	MHz

Typical Characteristics

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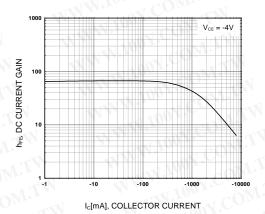


Figure 1. DC current Gain

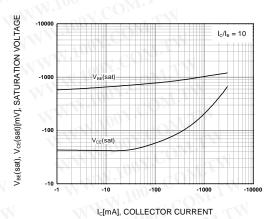


Figure 2. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

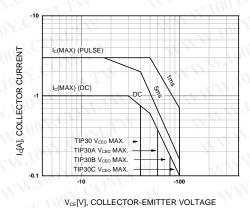


Figure 3. Safe Operating Area

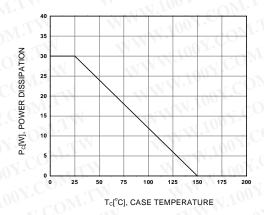


Figure 4. Power Derating

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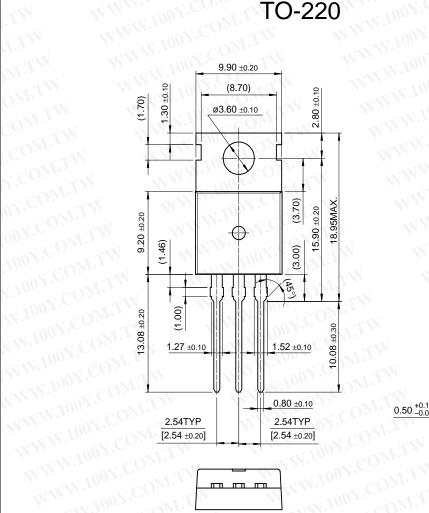
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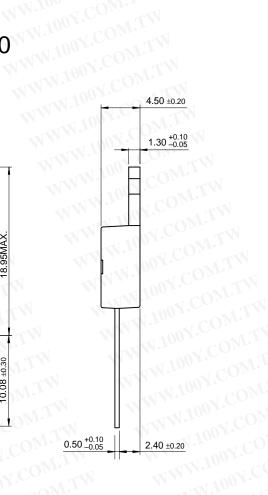
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