

IRGB30B60KPbF
IRGS30B60KPbF
IRGSL30B60KPbF

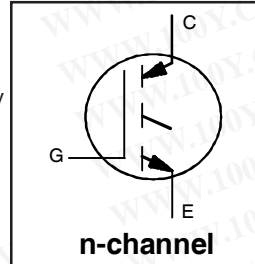
INSULATED GATE BIPOLAR TRANSISTOR

Features

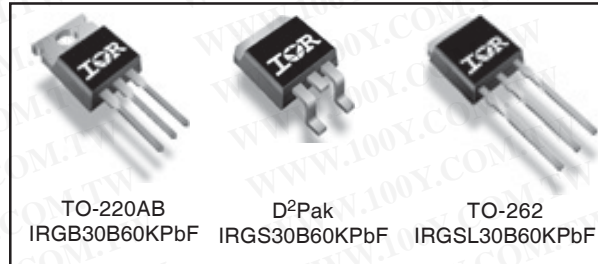
- Low VCE (on) Non Punch Through IGBT Technology
- 10µs Short Circuit Capability
- Square RBSOA
- Positive VCE (on) Temperature Coefficient
- Maximum Junction Temperature rated at 175°C
- Lead-Free

Benefits

- Benchmark Efficiency for Motor Control
- Rugged Transient Performance
- Low EMI
- Excellent Current Sharing in Parallel Operation



$V_{CES} = 600V$
 $I_C = 50A, T_C = 100^\circ C$
at $T_J = 175^\circ C$
 $t_{sc} > 10\mu s, T_J = 150^\circ C$
 $V_{CE(on)} \text{ typ.} = 1.95V$



Absolute Maximum Ratings

	Parameter	Max.	Units
V_{CES}	Collector-to-Emitter Voltage	600	V
$I_C @ T_C = 25^\circ C$	Continuous Collector Current	78 ^⑤	A
$I_C @ T_C = 100^\circ C$	Continuous Collector Current	50	
I_{CM}	Pulse Collector Current (Ref.Fig.C.T.5)	120	
I_{LM}	Clamped Inductive Load current ①	120	
V_{ISOL}	RMS Isolation Voltage, Terminal to Case, t=1 min.	2500	V
V_{GE}	Gate-to-Emitter Voltage	±20	
$P_D @ T_C = 25^\circ C$	Maximum Power Dissipation	370	W
$P_D @ T_C = 100^\circ C$	Maximum Power Dissipation	180	
T_J	Operating Junction and	-55 to +175	°C
T_{STG}	Storage Temperature Range		
	Soldering Temperature, for 10 sec.	300 (0.063 in. (1.6mm) from case)	
	Mounting Torque, 6-32 or M3 Screw	10 lbf·in (1.1 N·m)	

Thermal / Mechanical Characteristics

	Parameter	Min.	Typ.	Max.	Units
$R_{\theta JC}$	Junction-to-Case- IGBT	—	—	0.41*	°C/W
$R_{\theta CS}$	Case-to-Sink, flat, greased surface	—	0.50	—	
$R_{\theta JA}$	Junction-to-Ambient, typical socket mount ②	—	—	62	
$R_{\theta JA}$	Junction-to-Ambient (PCB Mount, Steady State)③	—	—	40	
Wt	Weight	—	1.44	—	g

* $R_{\theta JC}$ (end of life) = 0.65°C/W. This is the maximum measured value after 1000 temperature cycles from -55 to 150°C and is accounted for by the physical wearout of the die attach medium.

IRGB/S/SL30B60KPbF

International
IR Rectifier

Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions	Ref.Fig.
$V_{(BR)CES}$	Collector-to-Emitter Breakdown Voltage	600	—	—	V	$V_{GE} = 0V, I_C = 500\mu A$	
$\Delta V_{(BR)CES}/\Delta T_J$	Temperature Coeff. of Breakdown Voltage	—	0.40	—	V/°C	$V_{GE} = 0V, I_C = 1mA (25^\circ\text{C}-150^\circ\text{C})$	
$V_{CE(on)}$	Collector-to-Emitter Voltage	—	1.95	2.35	V	$I_C = 30A, V_{GE} = 15V, T_J = 25^\circ\text{C}$	5,6,7
		—	2.40	2.75		$I_C = 30A, V_{GE} = 15V, T_J = 150^\circ\text{C}$	8,9,10
		—	2.6	2.95		$I_C = 30A, V_{GE} = 15V, T_J = 175^\circ\text{C}$	
$V_{GE(th)}$	Gate Threshold Voltage	3.5	4.5	5.5	V	$V_{CE} = V_{GE}, I_C = 250\mu A$	8,9,10
$\Delta V_{GE(th)}/\Delta T_J$	Threshold Voltage temp. coefficient	—	-10	—	mV/°C	$V_{CE} = V_{GE}, I_C = 1.0mA (25^\circ\text{C}-150^\circ\text{C})$	11
gfe	Forward Transconductance	—	18	—	S	$V_{CE} = 50V, I_C = 50A, PW = 80\mu s$	
I_{CES}	Zero Gate Voltage Collector Current	—	5.0	250	μA	$V_{GE} = 0V, V_{CE} = 600V$	
		—	1000	2000		$V_{GE} = 0V, V_{CE} = 600V, T_J = 150^\circ\text{C}$	
		—	1830	3000		$V_{GE} = 0V, V_{CE} = 600V, T_J = 175^\circ\text{C}$	
I_{GES}	Gate-to-Emitter Leakage Current	—	—	± 100	nA	$V_{GE} = \pm 20V, V_{CE} = 0V$	

Switching Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions	Ref.Fig.
Q_g	Total Gate Charge (turn-on)	—	102	153	nC	$I_C = 30A$ $V_{CC} = 400V$ $V_{GE} = 15V$	17
Q_{ge}	Gate-to-Emitter Charge (turn-on)	—	14	21			CT1
Q_{gc}	Gate-to-Collector Charge (turn-on)	—	44	66			
E_{on}	Turn-On Switching Loss	—	350	620	μJ	$I_C = 30A, V_{CC} = 400V$ $V_{GE} = 15V, R_G = 10\Omega, L = 200\mu H$ $T_J = 25^\circ\text{C} \text{ ④}$	CT4
E_{off}	Turn-Off Switching Loss	—	825	955			
E_{tot}	Total Switching Loss	—	1175	1575			
$t_{d(on)}$	Turn-On delay time	—	46	60	ns	$I_C = 30A, V_{CC} = 400V$ $V_{GE} = 15V, R_G = 10\Omega, L = 200\mu H$ $T_J = 25^\circ\text{C}$	CT4
t_r	Rise time	—	28	39			
$t_{d(off)}$	Turn-Off delay time	—	185	200			
t_f	Fall time	—	31	40			
E_{on}	Turn-On Switching Loss	—	635	1085			
E_{off}	Turn-Off Switching Loss	—	1150	1350	12,14		
E_{tot}	Total Switching Loss	—	1785	2435	WF1,WF2		
$t_{d(on)}$	Turn-On delay time	—	46	60	ns	$I_C = 30A, V_{CC} = 400V$ $V_{GE} = 15V, R_G = 10\Omega, L = 200\mu H$ $T_J = 150^\circ\text{C}$	13,15
t_r	Rise time	—	28	39			CT4
$t_{d(off)}$	Turn-Off delay time	—	205	235			WF1
t_f	Fall time	—	32	42			WF2
L_E	Internal Emitter Inductance	—	7.5	—	nH	Measured 5mm from package	
C_{ies}	Input Capacitance	—	1750	2500	pF	$V_{GE} = 0V$ $V_{CC} = 30V$ $f = 1.0MHz$	16
C_{oes}	Output Capacitance	—	160	255			
C_{res}	Reverse Transfer Capacitance	—	60	90			
RBSOA	Reverse Bias Safe Operating Area	FULL SQUARE				$T_J = 150^\circ\text{C}, I_C = 120A, V_p = 600V$ $V_{CC}=500V, V_{GE} = +15V \text{ to } 0V, R_G = 10\Omega$	4 CT2
SCSOA	Short Circuit Safe Operating Area	10	—	—	μs	$T_J = 150^\circ\text{C}, V_p = 600V, R_G = 10\Omega$ $V_{CC}=360V, V_{GE} = +15V \text{ to } 0V$	CT3 WF3
$I_{SC}(\text{Peak})$	Peak Short Circuit Collector Current	—	200	—	A		WF3

Note ① to ⑤ are on page 13

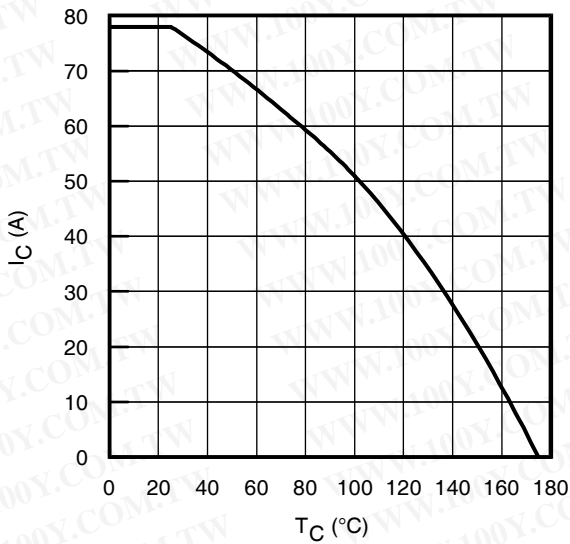


Fig. 1 - Maximum DC Collector Current vs. Case Temperature

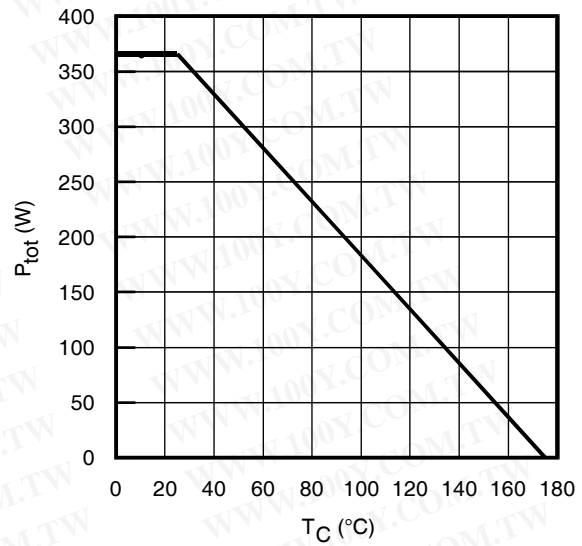


Fig. 2 - Power Dissipation vs. Case Temperature

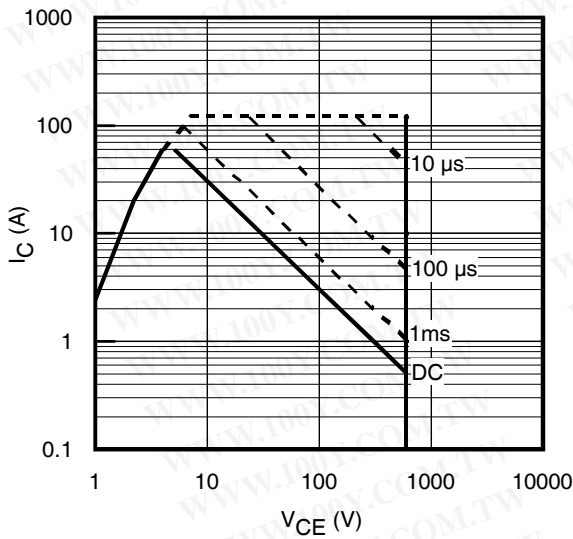


Fig. 3 - Forward SOA
 $T_C = 25^{\circ}C$; $T_J \leq 150^{\circ}C$

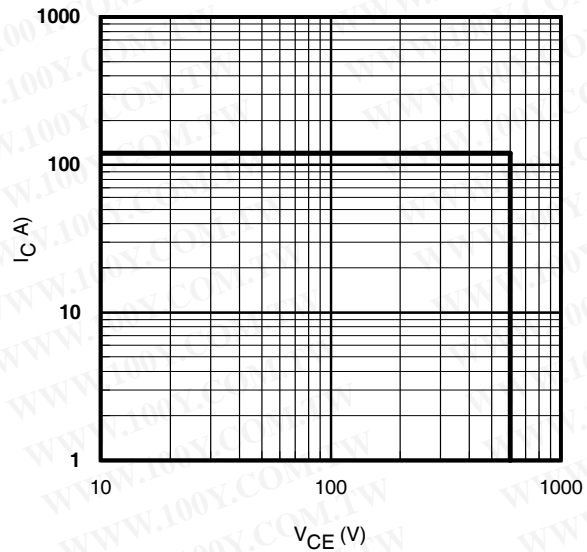


Fig. 4 - Reverse Bias SOA
 $T_J = 150^{\circ}C$; $V_{GE} = 15V$

IRGB/S/SL30B60KPbF

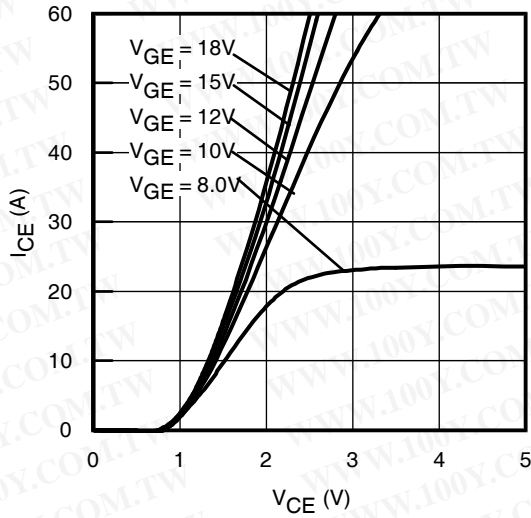


Fig. 5 - Typ. IGBT Output Characteristics
 $T_J = -40^\circ\text{C}$; $t_p = 80\mu\text{s}$

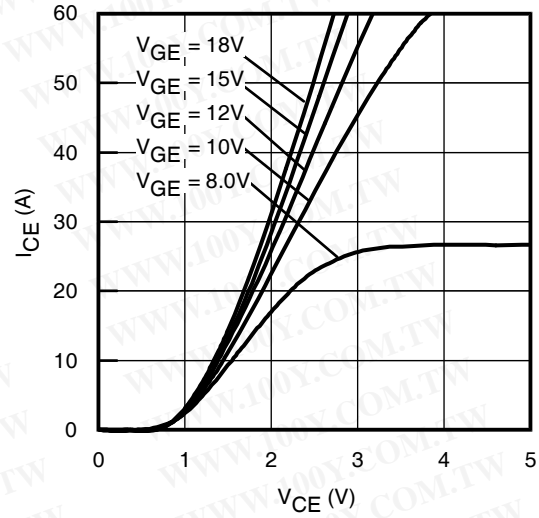


Fig. 6 - Typ. IGBT Output Characteristics
 $T_J = 25^\circ\text{C}$; $t_p = 80\mu\text{s}$

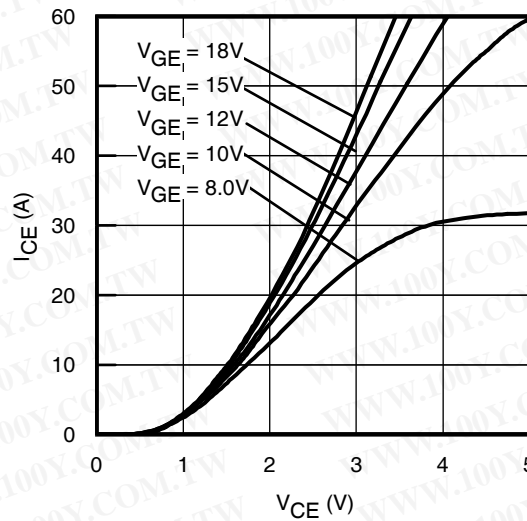


Fig. 7 - Typ. IGBT Output Characteristics
 $T_J = 150^\circ\text{C}$; $t_p = 80\mu\text{s}$

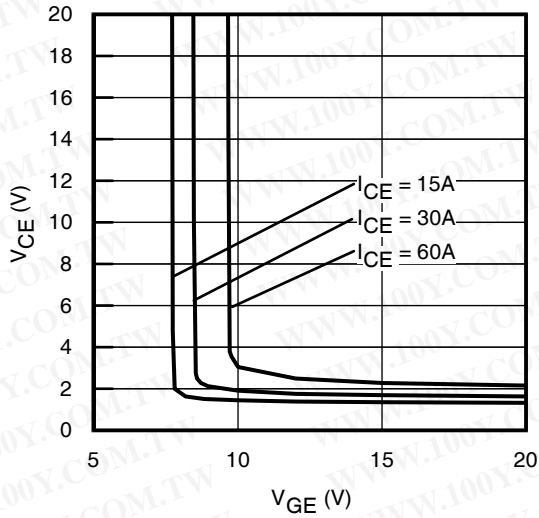


Fig. 8 - Typical V_{CE} vs. V_{GE}
 $T_J = -40^\circ\text{C}$

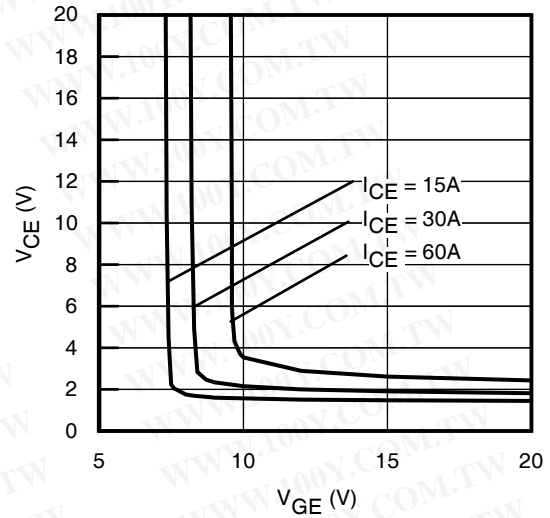


Fig. 9 - Typical V_{CE} vs. V_{GE}
 $T_J = 25^\circ\text{C}$

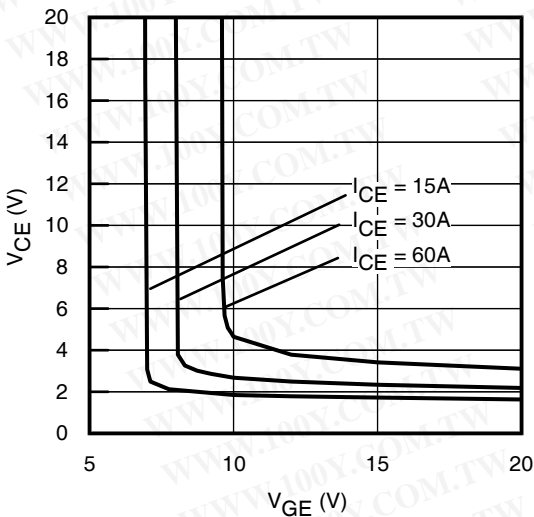


Fig. 10 - Typical V_{CE} vs. V_{GE}
 $T_J = 150^\circ\text{C}$

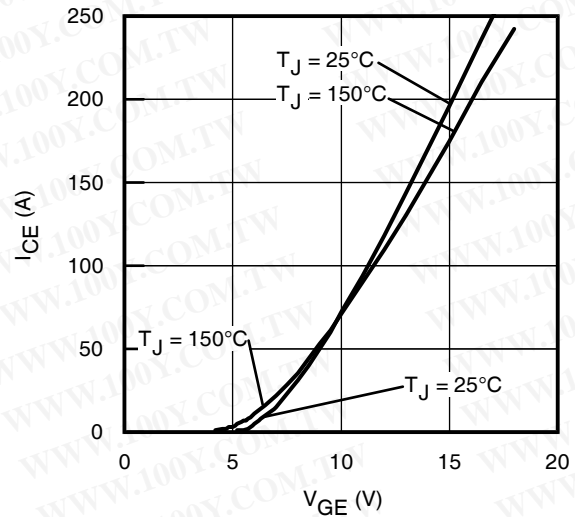


Fig. 11 - Typ. Transfer Characteristics
 $V_{CE} = 50\text{V}$; $t_p = 10\mu\text{s}$

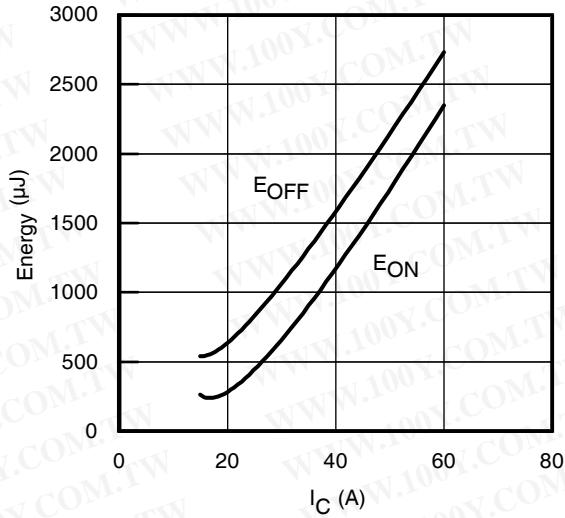


Fig. 12 - Typ. Energy Loss vs. I_C
 $T_J = 150^\circ\text{C}$; $L=200\mu\text{H}$; $V_{CE}=400\text{V}$,
 $R_G = 10\Omega$; $V_{GE}=15\text{V}$

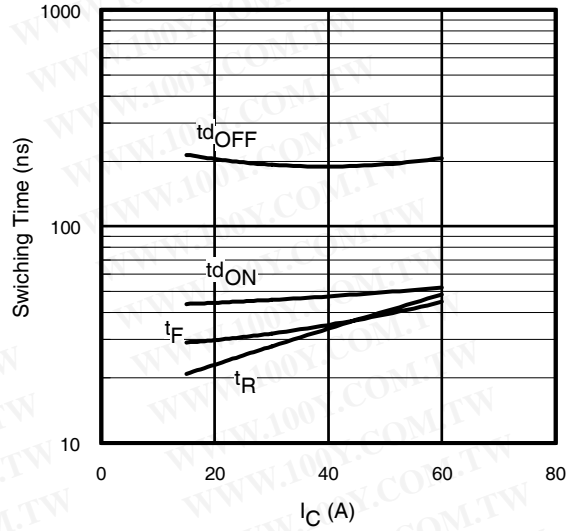


Fig. 13 - Typ. Switching Time vs. I_C
 $T_J = 150^\circ\text{C}$; $L=200\mu\text{H}$; $V_{CE}=400\text{V}$
 $R_G = 10\Omega$; $V_{GE}=15\text{V}$

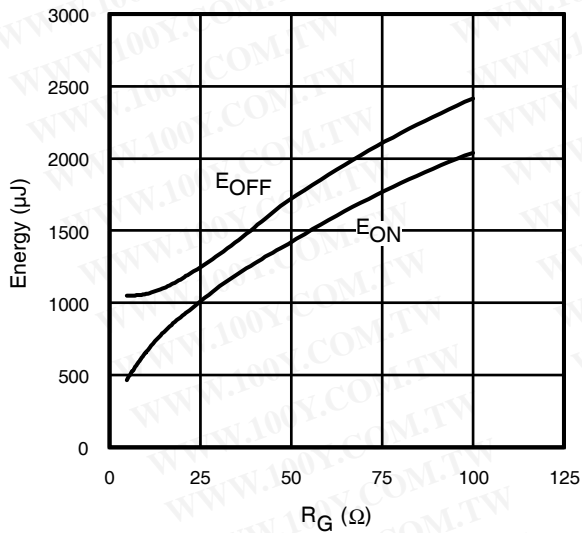


Fig. 14 - Typ. Energy Loss vs. R_G
 $T_J = 150^\circ\text{C}$; $L=200\mu\text{H}$; $V_{CE}=400\text{V}$
 $I_{CE}=30\text{A}$; $V_{GE}=15\text{V}$

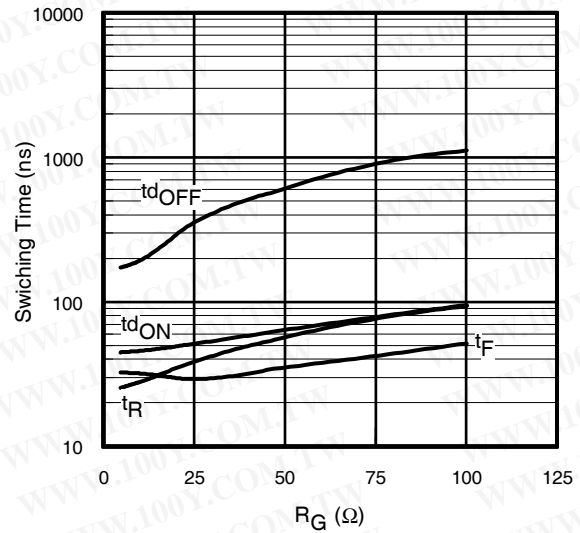


Fig. 15 - Typ. Switching Time vs. R_G
 $T_J = 150^\circ\text{C}$; $L=200\mu\text{H}$; $V_{CE}=400\text{V}$
 $I_{CE}=30\text{A}$; $V_{GE}=15\text{V}$

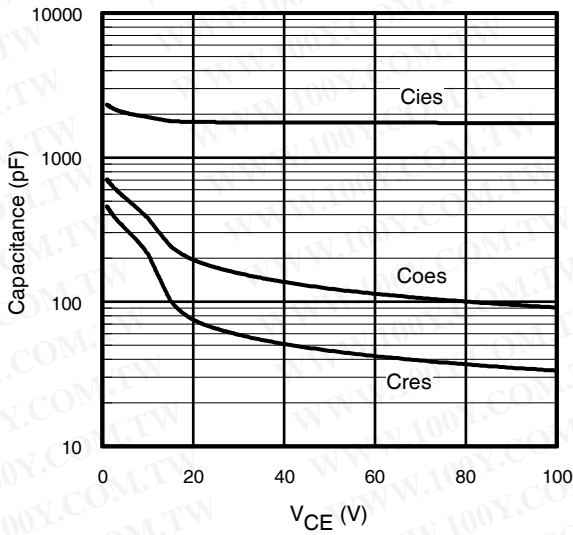


Fig. 16- Typ. Capacitance vs. V_{CE}
 $V_{GE} = 0V$; $f = 1MHz$

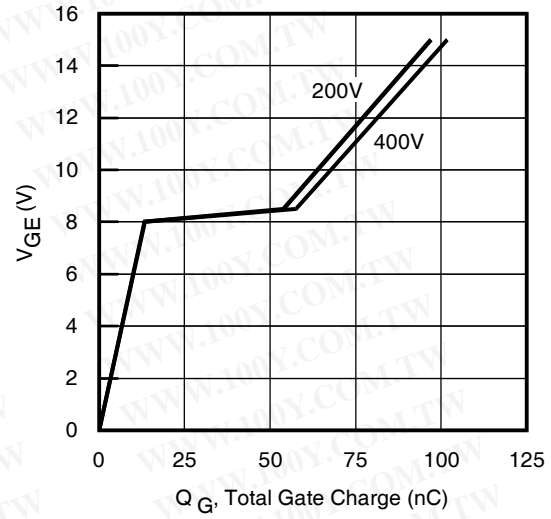


Fig. 17 - Typical Gate Charge vs. V_{GE}
 $I_{CE} = 30A$; $L = 600\mu H$

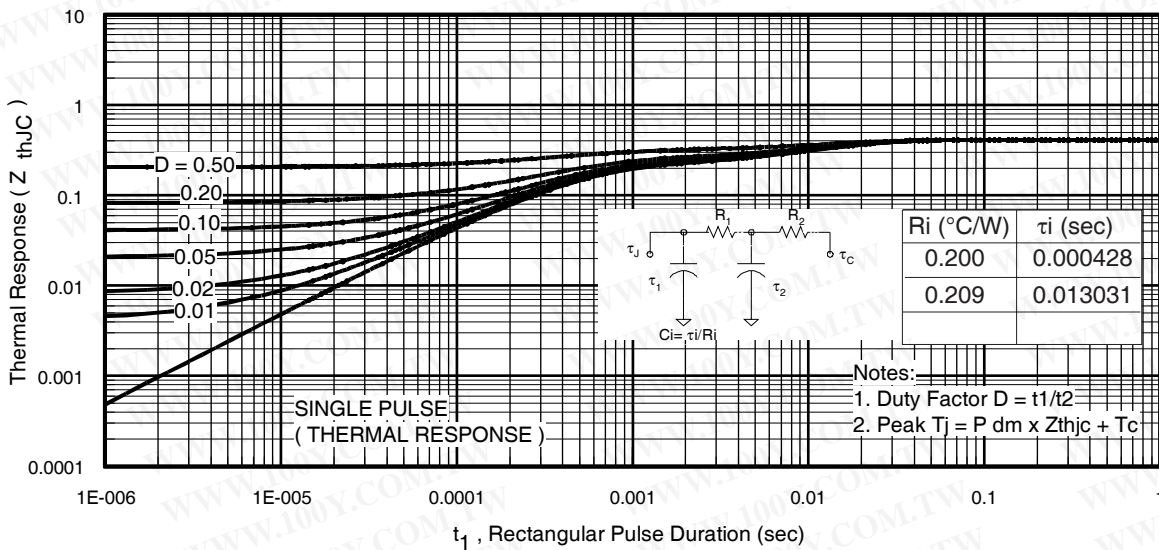


Fig 18. Maximum Transient Thermal Impedance, Junction-to-Case (IGBT)

IRGB/S/SL30B60KPbF

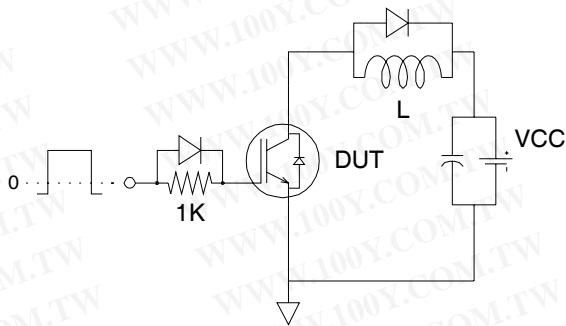


Fig.C.T.1 - Gate Charge Circuit (turn-off)

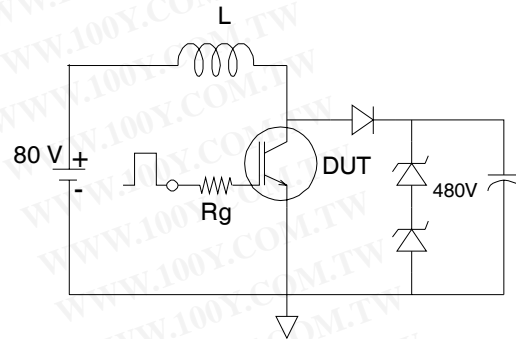


Fig.C.T.2 - RBSOA Circuit

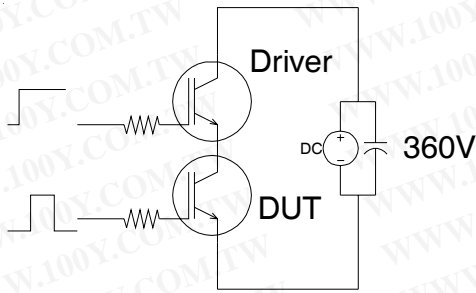


Fig.C.T.3 - S.C.SOA Circuit

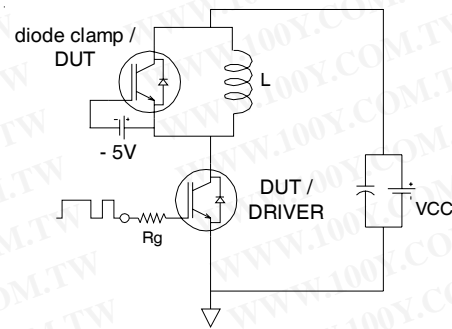


Fig.C.T.4 - Switching Loss Circuit

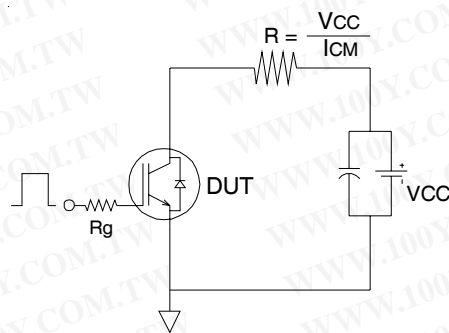


Fig.C.T.5 - Resistive Load Circuit

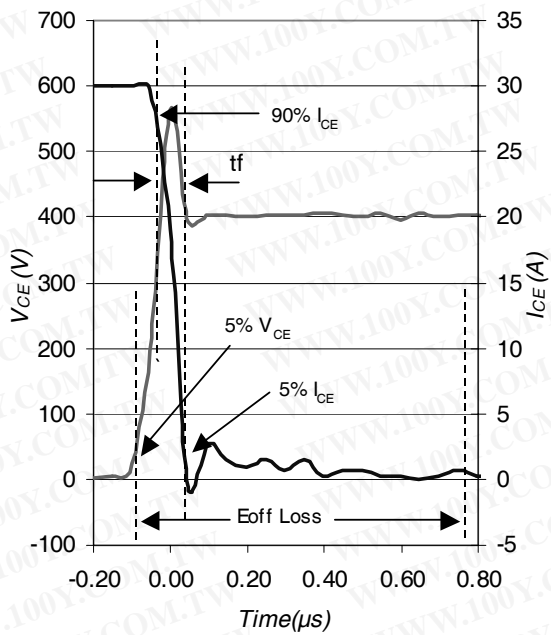


Fig. WF1- Typ. Turn-off Loss Waveform
@ $T_J = 150^\circ C$ using Fig. CT.4

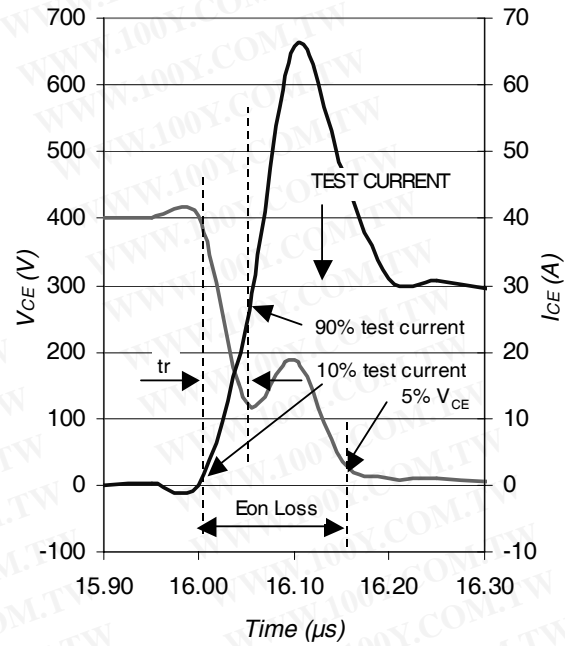


Fig. WF2- Typ. Turn-on Loss Waveform
@ $T_J = 150^\circ C$ using Fig. CT.4

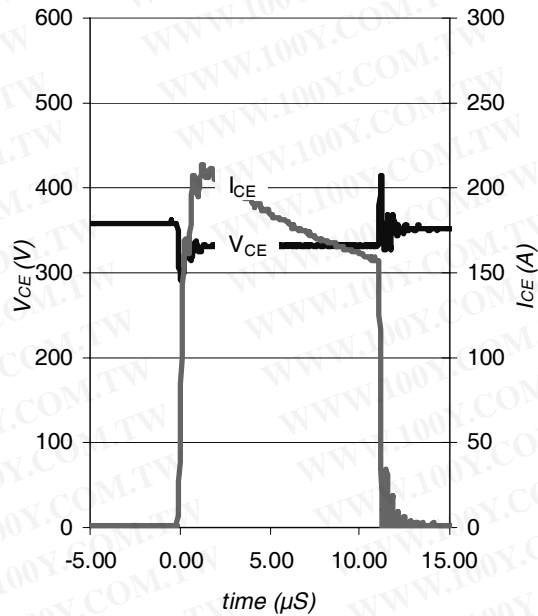
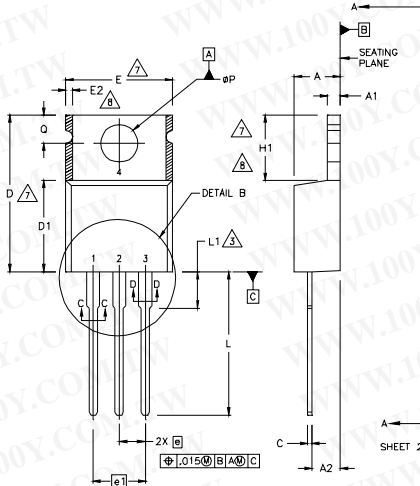


Fig. WF3- Typ. S.C Waveform
@ $T_C = 150^\circ C$ using Fig. CT.3

IRGB/S/SL30B60KPbF

TO-220AB Package Outline

Dimensions are shown in millimeters (inches)



NOTES:

- 1 DIMENSIONING AND TOLERANCING PER ASME Y14.5 M- 1994.
- 2 DIMENSIONS ARE SHOWN IN INCHES [MILLIMETERS].
- 3 LEAD DIMENSION AND FINISH UNCONTROLLED IN L1.
- 4 DIMENSION D & E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED .005" (0.127) PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY.
- 5 DIMENSION b1 & c1 APPLY TO BASE METAL ONLY.
- 6 CONTROLLING DIMENSION : INCHES.
- 7 THERMAL PAD CONTOUR OPTIONAL WITHIN DIMENSIONS E,H1,D2 & E1
- 8 DIMENSION E2 X H1 DEFINE A ZONE WHERE STAMPING AND SINGULATION IRREGULARITIES ARE ALLOWED.

LEAD ASSIGNMENTS

HEXFET

- 1- GATE
- 2- DRAIN
- 3- SOURCE

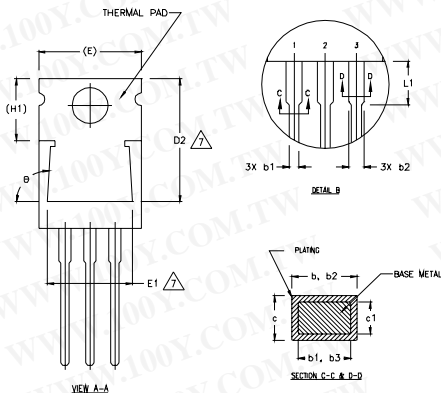
IGBTs, CoPACK

- 1- GATE
- 2- COLLECTOR
- 3- EMITTER

DIODES

- 1- ANODE/OPEN
- 2- CATHODE
- 3- ANODE

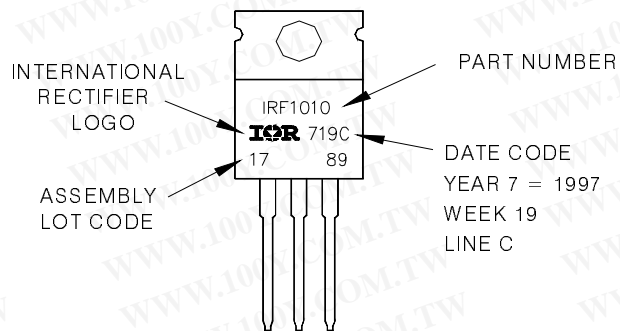
SYMBOL	DIMENSIONS				NOTES
	MILLIMETERS		INCHES		
	MIN.	MAX.	MIN.	MAX.	
A	3.56	4.82	.140	.190	
A1	0.51	1.40	.020	.055	
A2	2.04	2.92	.080	.115	
b	0.38	1.01	.015	.040	
b1	0.38	0.96	.015	.038	5
b2	1.15	1.77	.045	.070	
b3	1.15	1.73	.045	.068	
c	0.36	0.61	.014	.024	
c1	0.36	0.56	.014	.022	5
D	14.22	16.51	.560	.650	4
D1	8.38	9.02	.330	.355	
D2	12.19	12.88	.480	.507	7
E	9.66	10.66	.380	.420	4,7
E1	8.38	8.89	.330	.350	7
e	2.54 BSC		.100 BSC		
e1	5.08		.200 BSC		
H1	5.85	6.55	.230	.270	7,8
L	12.70	14.73	.500	.580	
L1	-	6.35	-	.250	3
ØP	3.54	4.08	.139	.161	
Q	2.54	3.42	.100	.135	
Ø	90°-93°		90°-93°		



TO-220AB Part Marking Information

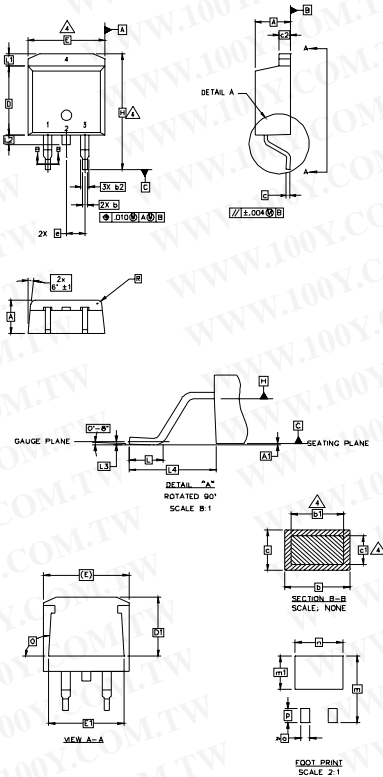
EXAMPLE: THIS IS AN IRF1010
 LOT CODE 1789
 ASSEMBLED ON WW 19, 1997
 IN THE ASSEMBLY LINE "C"

Note: "P" in assembly line position indicates "Lead-Free"



D²Pak Package Outline

Dimensions are shown in millimeters (inches)



- NOTES:
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994
 2. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES]
 3. DIMENSION D & E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.127 [0.005"] PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTMOST EXTREMES OF THE PLASTIC BODY.
 4. DIMENSION b1 AND c1 APPLY TO BASE METAL ONLY.
 5. CONTROLLING DIMENSION: INCH.

SYMBOL	DIMENSIONS				NOTES
	MILLIMETERS		INCHES		
	MIN.	MAX.	MIN.	MAX.	
A	4.06	4.83	.160	.190	4
A1	0.00	0.254	.000	.010	
b	0.51	0.99	.020	.039	
b1	0.51	0.89	.020	.035	
b2	1.14	1.78	.045	.070	4
c	0.38	0.74	.015	.029	
c1	0.38	0.58	.015	.023	4
c2	1.14	1.65	.045	.065	
D	8.51	9.65	.335	.380	3
D1	6.86		.270		
E	9.65	10.67	.380	.420	3
E1	6.22		.245		
e	2.54 BSC		.100 BSC		
H	14.61	15.88	.575	.625	
L	1.78	2.79	.070	.110	
L1		1.65		.065	
L2	1.27	1.78	.050	.070	
L3	0.25 BSC		.010 BSC		
L4	4.78	5.28	.188	.208	
m1	17.78		.700		
n	8.89		.350		
o	11.43		.450		
p	2.08		.082		
q	3.81		.150		
R	0.51	0.71	.020	.028	
θ	90°	93°	90°	93°	

LEAD ASSIGNMENTS

- HEXFET**
- 1.- GATE
 - 2, 4.- DRAIN
 - 3.- SOURCE

IGBTs, CoPACK

- 1.- GATE
- 2, 4.- COLLECTOR
- 3.- EMITTER

DIODES

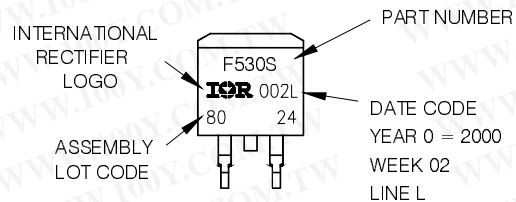
- 1.- ANODE *
- 2, 4.- CATHODE
- 3.- ANODE

* PART DEPENDENT.

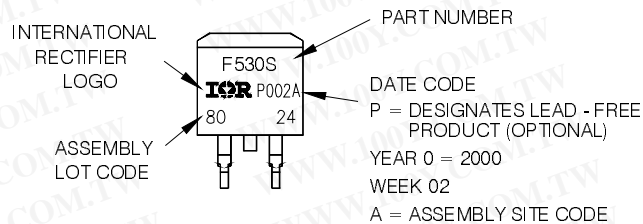
D²Pak Part Marking Information

EXAMPLE: THIS IS AN IRF530S WITH
LOT CODE 8024
ASSEMBLED ON WW 02, 2000
IN THE ASSEMBLY LINE "L"

Note: "P" in assembly line position
indicates "Lead - Free"



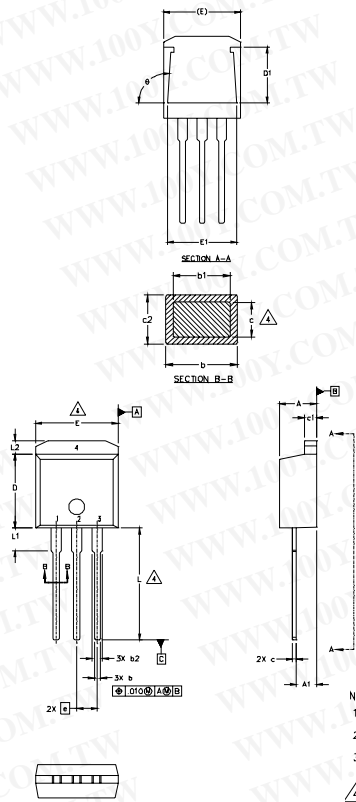
OR



IRGB/S/SL30B60KPbF

TO-262 Package Outline

Dimensions are shown in millimeters (inches)



SYMBOL	DIMENSIONS				NOTES
	MILLIMETERS		INCHES		
	MIN.	MAX.	MIN.	MAX.	
A	4.06	4.83	.160	.190	
A1	2.03	2.92	.080	.115	
b	0.51	0.99	.020	.039	
b1	0.51	0.89	.020	.035	4
b2	1.14	1.40	.045	.055	
c	0.38	0.63	.015	.025	4
c1	1.14	1.40	.045	.055	
c2	0.43	.063	.017	.029	
D	8.51	9.65	.335	.380	3
D1	5.33		.210		
E	9.65	10.67	.380	.420	3
E1	6.22		.245		
e	2.54	BSC	.100	BSC	
L	13.46	14.09	.530	.555	
L1	3.56	3.71	.140	.146	
L2		1.65		.065	

LEAD ASSIGNMENTS

HEXFET

- 1. - GATE
- 2. - DRAIN
- 3. - SOURCE
- 4. - DRAIN

IGBT

- 1- GATE

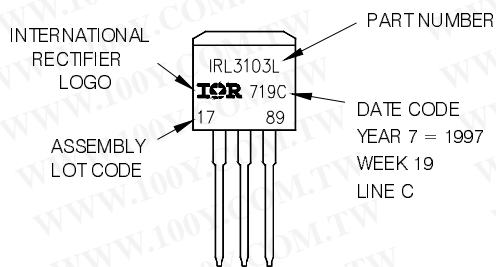
NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994
2. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].
3. DIMENSION D & E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.127 [0.005"] PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTMOST EXTREMES OF THE PLASTIC BODY.
4. DIMENSION b1 AND c1 APPLY TO BASE METAL ONLY.
5. CONTROLLING DIMENSION: INCH.

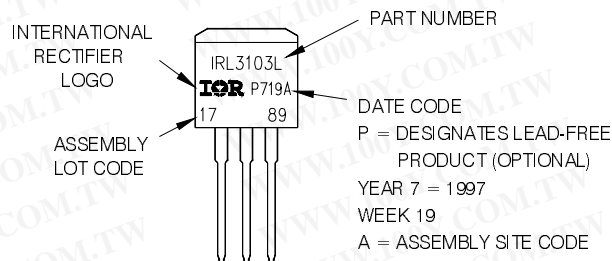
TO-262 Part Marking Information

EXAMPLE: THIS IS AN IRL3103L
LOT CODE 1789
ASSEMBLED ON WW 19, 1997
IN THE ASSEMBLY LINE "C"

Note: "P" in assembly line position indicates "Lead - Free"



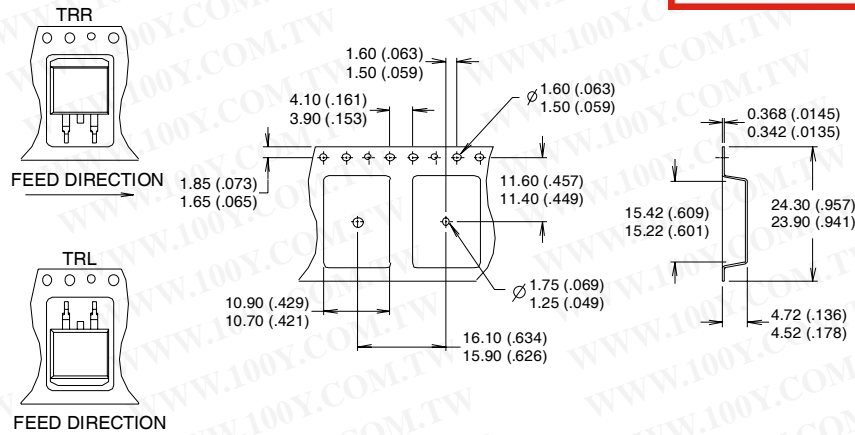
OR



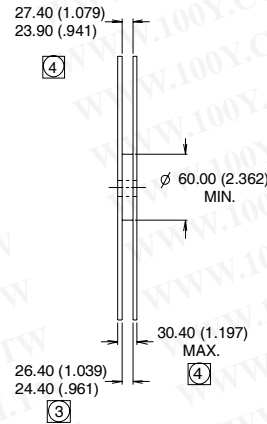
D²Pak Tape & Reel Information

Dimensions are shown in millimeters (inches)

勝特力材料 886-3-5753170
 勝特力电子(上海) 86-21-34970699
 勝特力电子(深圳) 86-755-83298787
 Http://www.100y.com.tw



- NOTES:
1. CONFORMS TO EIA-418.
 2. CONTROLLING DIMENSION: MILLIMETER.
 - ③ DIMENSION MEASURED @ HUB.
 - ④ INCLUDES FLANGE DISTORTION @ OUTER EDGE.



Notes:

- ① $V_{CC} = 80\% (V_{CES})$, $V_{GE} = 20V$, $L = 28\mu H$, $R_G = 22\Omega$.
- ② This is only applied to TO-220AB package.
- ③ This is applied to D²Pak, when mounted on 1" square PCB (FR-4 or G-10 Material).
For recommended footprint and soldering techniques refer to application note #AN-994.
- ④ Energy losses include "tail" and diode reverse recovery.
- ⑤ Calculated continuous current based on maximum allowable junction temperature. Package limitation current is 75A.

TO-220AB package is not recommended for Surface Mount Application.

Data and specifications subject to change without notice.