

PD - 95580

International **IR** Rectifier

SMPS MOSFET

IRL3714PbF
 IRL3714SPbF
 IRL3714LPbF

HEXFET® Power MOSFET

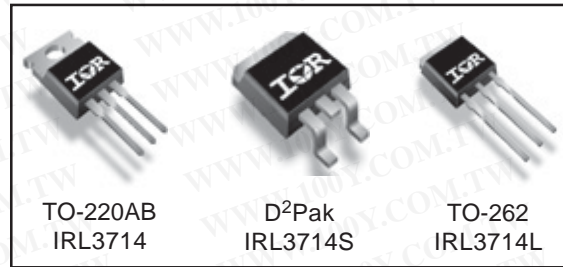
Applications

- High Frequency Isolated DC-DC Converters with Synchronous Rectification for Telecom and Industrial Use
- High Frequency Buck Converters for Computer Processor Power
- Lead-Free

| V _{DSS} | R _{DS(on)} max | I _D |
|------------------|-------------------------|----------------|
| 20V | 20mΩ | 36A |

Benefits

- Ultra-Low Gate Impedance
- Very Low R_{DS(on)} at 4.5V V_{GS}
- Fully Characterized Avalanche Voltage and Current



Absolute Maximum Ratings

| Symbol | Parameter | Max. | Units |
|--|---|--------------|-------|
| V _{DS} | Drain-Source Voltage | 20 | V |
| V _{GS} | Gate-to-Source Voltage | ± 20 | V |
| I _D @ T _C = 25°C | Continuous Drain Current, V _{GS} @ 10V | 36 | A |
| I _D @ T _C = 70°C | Continuous Drain Current, V _{GS} @ 10V | 31 | |
| I _{DM} | Pulsed Drain Current ^① | 140 | |
| P _D @ T _C = 25°C | Maximum Power Dissipation ^③ | 47 | W |
| P _D @ T _C = 70°C | Maximum Power Dissipation ^③ | 33 | W |
| | Linear Derating Factor | 0.31 | W/°C |
| T _J , T _{STG} | Junction and Storage Temperature Range | -55 to + 175 | °C |

Thermal Resistance

| | Parameter | Typ. | Max. | Units |
|------------------|--|------|------|-------|
| R _{θJC} | Junction-to-Case | — | 3.2 | °C/W |
| R _{θCS} | Case-to-Sink, Flat, Greased Surface ^④ | 0.50 | — | |
| R _{θJA} | Junction-to-Ambient ^④ | — | 62 | |
| R _{θJA} | Junction-to-Ambient (PCB mount) ^⑤ | — | 40 | |

Notes ① through ⑥ are on page 11

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Static @ T_J = 25°C (unless otherwise specified)

| | Parameter | Min. | Typ. | Max. | Units | Conditions |
|--------------------------------------|--------------------------------------|------|-------|------|-------|---|
| V _{(BR)DSS} | Drain-to-Source Breakdown Voltage | 20 | — | — | V | V _{GS} = 0V, I _D = 250μA |
| ΔV _{(BR)DSS/ΔT_J} | Breakdown Voltage Temp. Coefficient | — | 0.022 | — | V/°C | Reference to 25°C, I _D = 1mA |
| R _{DS(on)} | Static Drain-to-Source On-Resistance | — | 15 | 20 | mΩ | V _{GS} = 10V, I _D = 18A ③ |
| | | — | 21 | 28 | | V _{GS} = 4.5V, I _D = 14A ③ |
| V _{GS(th)} | Gate Threshold Voltage | 1.0 | — | 3.0 | V | V _{DS} = V _{GS} , I _D = 250μA |
| I _{DSS} | Drain-to-Source Leakage Current | — | — | 20 | μA | V _{DS} = 16V, V _{GS} = 0V |
| | | — | — | 100 | | V _{DS} = 16V, V _{GS} = 0V, T _J = 125°C |
| I _{GSS} | Gate-to-Source Forward Leakage | — | — | 200 | nA | V _{GS} = 16V |
| | Gate-to-Source Reverse Leakage | — | — | -200 | | V _{GS} = -16V |

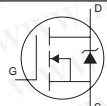
Dynamic @ T_J = 25°C (unless otherwise specified)

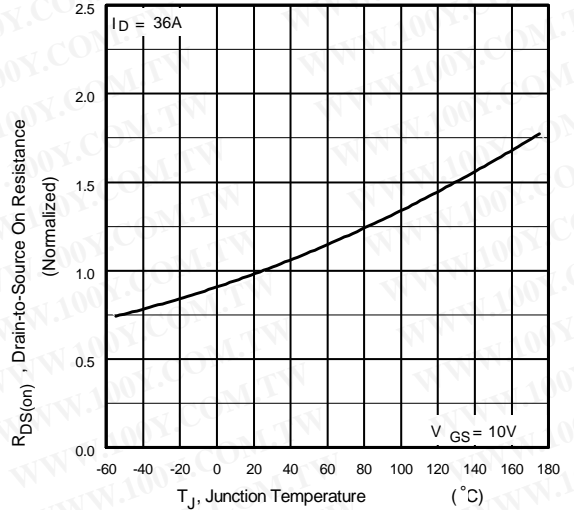
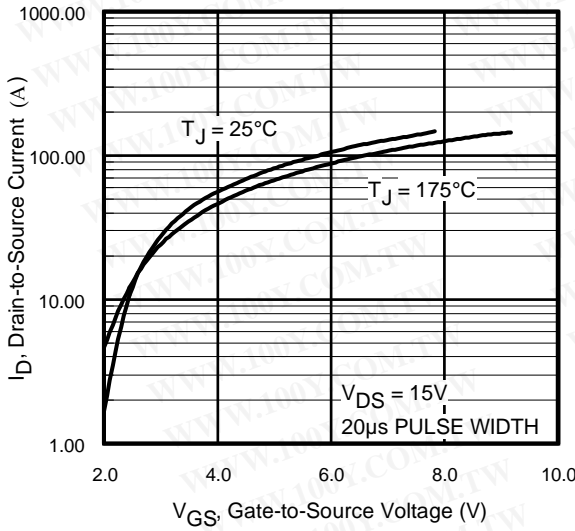
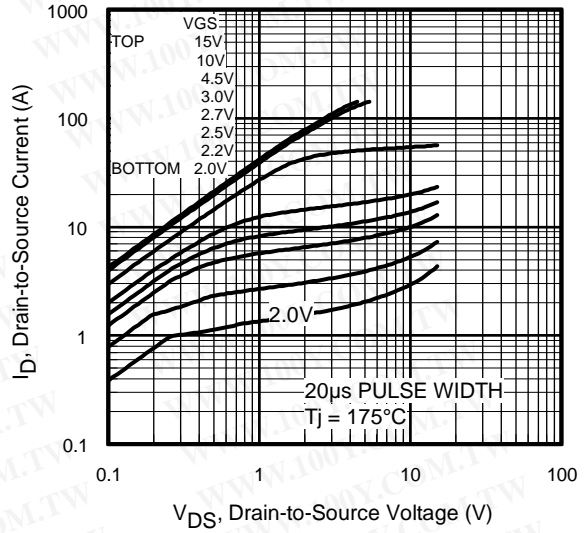
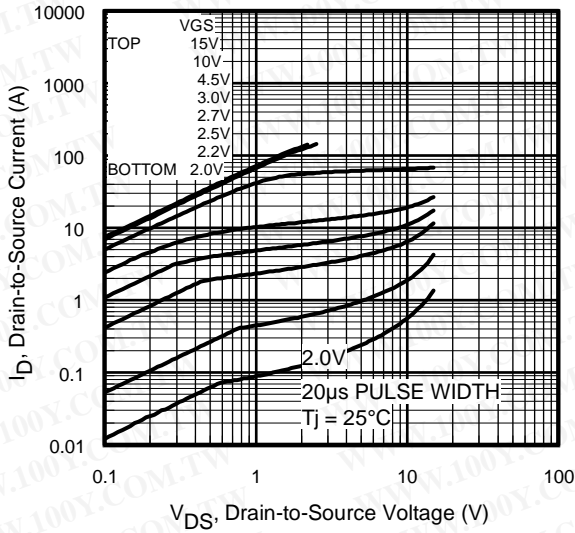
| Symbol | Parameter | Min. | Typ. | Max. | Units | Conditions |
|---------------------|---------------------------------|------|------|------|-------|---|
| g _{fs} | Forward Transconductance | 17 | — | — | S | V _{DS} = 10V, I _D = 14A |
| Q _g | Total Gate Charge | — | 6.5 | 9.7 | nC | I _D = 14A |
| Q _{gs} | Gate-to-Source Charge | — | 1.8 | — | | V _{DS} = 10V |
| Q _{gd} | Gate-to-Drain ("Miller") Charge | — | 2.9 | — | | V _{GS} = 4.5V |
| Q _{oss} | Output Gate Charge | — | 7.1 | — | | V _{GS} = 0V, V _{DS} = 10V |
| t _{d(on)} | Turn-On Delay Time | — | 8.7 | — | ns | V _{DD} = 10V |
| t _r | Rise Time | — | 78 | — | | I _D = 14A |
| t _{d(off)} | Turn-Off Delay Time | — | 10 | — | | R _G = 1.8Ω |
| t _f | Fall Time | — | 4.5 | — | | V _{GS} = 4.5V ③ |
| C _{iss} | Input Capacitance | — | 670 | — | pF | V _{GS} = 0V |
| C _{oss} | Output Capacitance | — | 470 | — | | V _{DS} = 10V |
| C _{rss} | Reverse Transfer Capacitance | — | 68 | — | | f = 1.0MHz |

Avalanche Characteristics

| Symbol | Parameter | Typ. | Max. | Units |
|-----------------|--------------------------------|------|------|-------|
| E _{AS} | Single Pulse Avalanche Energy② | — | 72 | mJ |
| I _{AR} | Avalanche Current② | — | 14 | A |

Diode Characteristics

| Symbol | Parameter | Min. | Typ. | Max. | Units | Conditions |
|-----------------|--|------|------|------|-------|--|
| I _S | Continuous Source Current (Body Diode) | — | 36 | — | A | MOSFET symbol showing the integral reverse p-n junction diode.  |
| I _{SM} | Pulsed Source Current (Body Diode) ① | — | 140 | — | | |
| V _{SD} | Diode Forward Voltage | — | — | 1.3 | V | T _J = 25°C, I _S = 18A, V _{GS} = 0V ③ |
| | | — | 0.88 | — | | T _J = 125°C, I _S = 18A, V _{GS} = 0V ③ |
| t _{rr} | Reverse Recovery Time | — | 35 | 53 | ns | T _J = 25°C, I _F = 18A, V _R = 10V |
| Q _{rr} | Reverse Recovery Charge | — | 34 | 51 | nC | di/dt = 100A/μs ③ |
| t _{rr} | Reverse Recovery Time | — | 35 | 53 | ns | T _J = 125°C, I _F = 18A, V _R = 10V |
| Q _{rr} | Reverse Recovery Charge | — | 35 | 53 | nC | di/dt = 100A/μs ③ |



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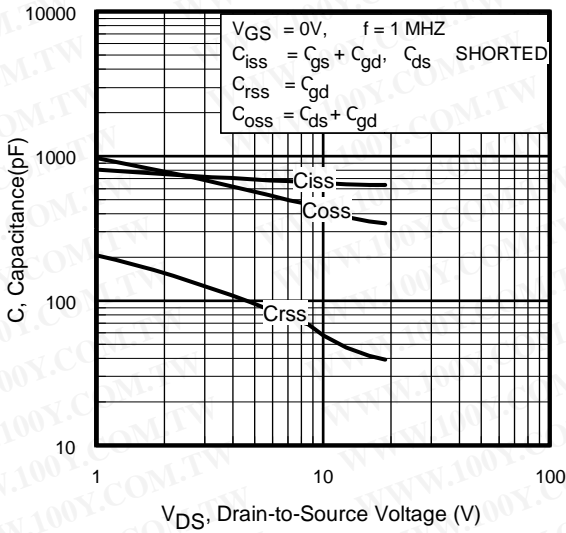


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

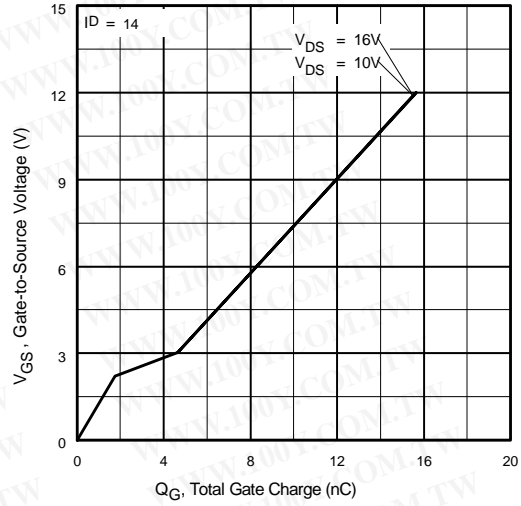


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

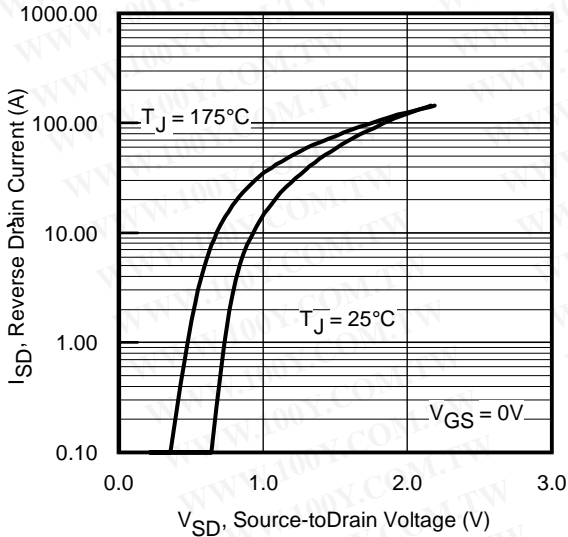


Fig 7. Typical Source-Drain Diode Forward Voltage

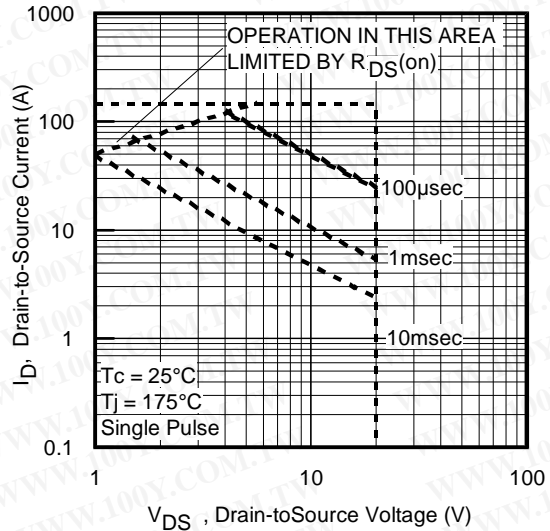


Fig 8. Maximum Safe Operating Area

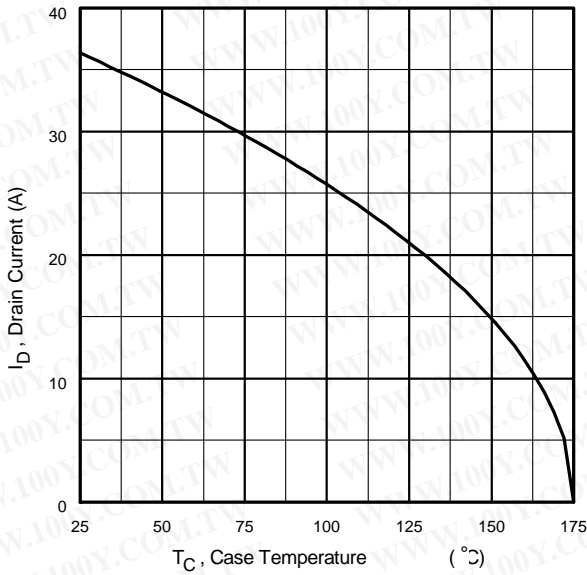


Fig 9. Maximum Drain Current Vs. Case Temperature

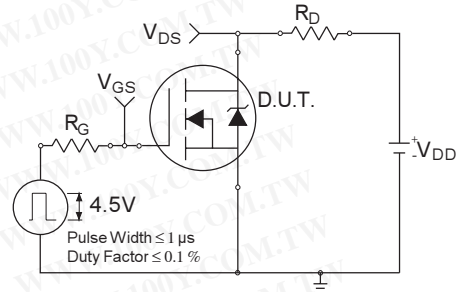


Fig 10a. Switching Time Test Circuit

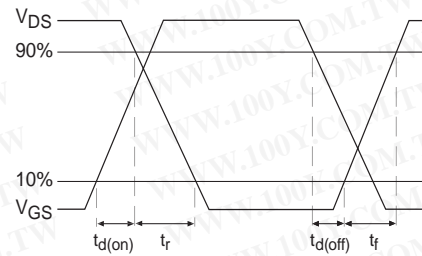


Fig 10b. Switching Time Waveforms

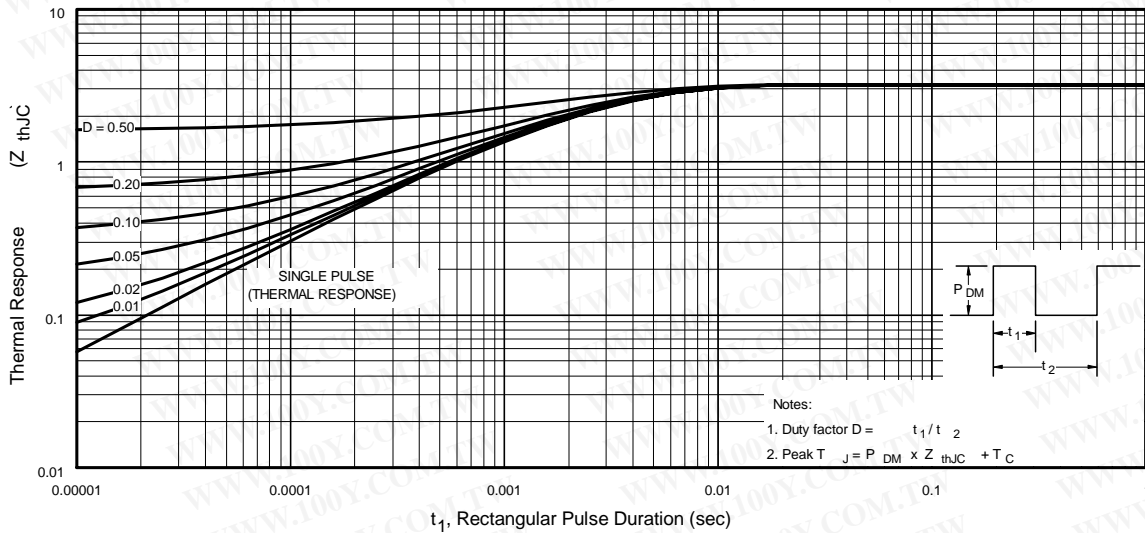


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case

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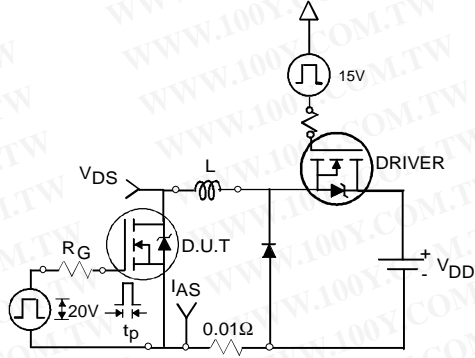


Fig 12a. Unclamped Inductive Test Circuit

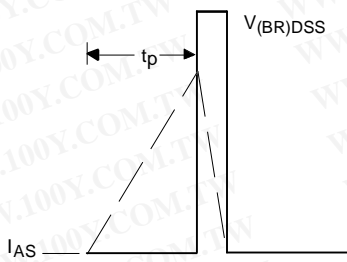


Fig 12b. Unclamped Inductive Waveforms

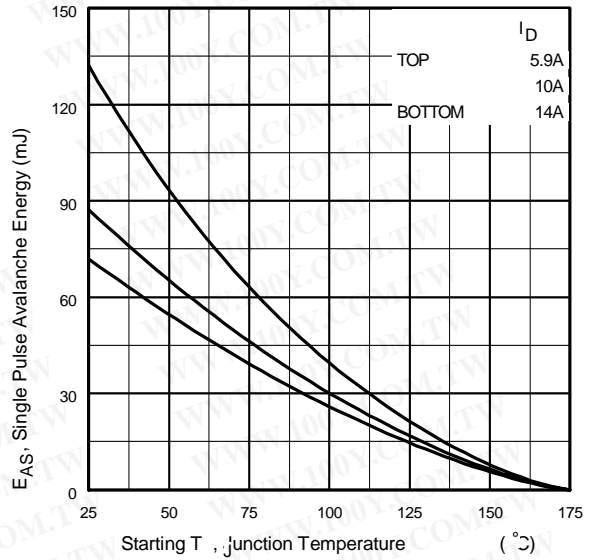


Fig 12c. Maximum Avalanche Energy Vs. Drain Current

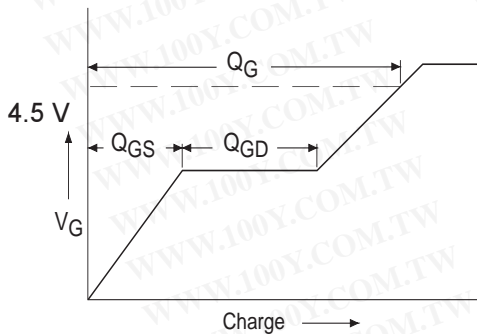


Fig 13a. Basic Gate Charge Waveform

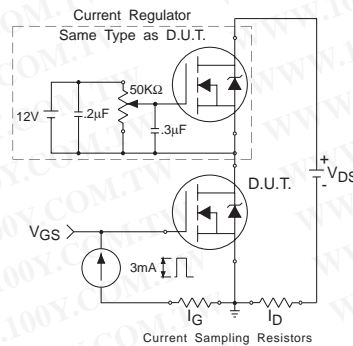
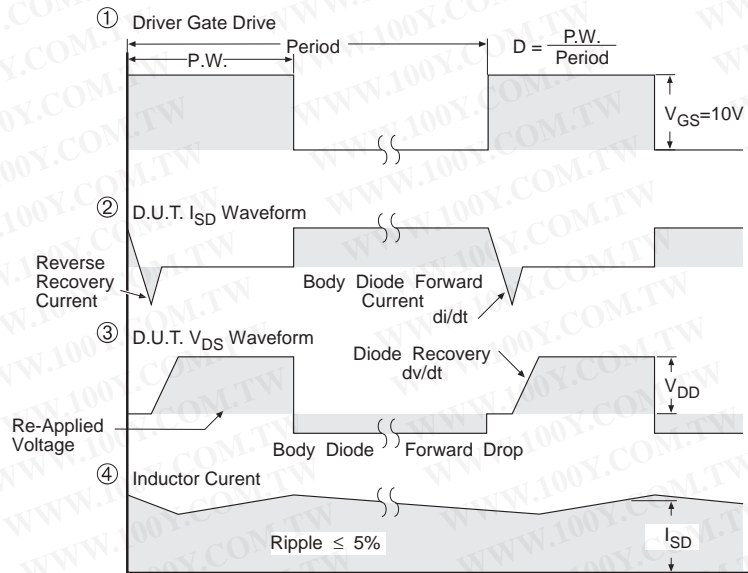
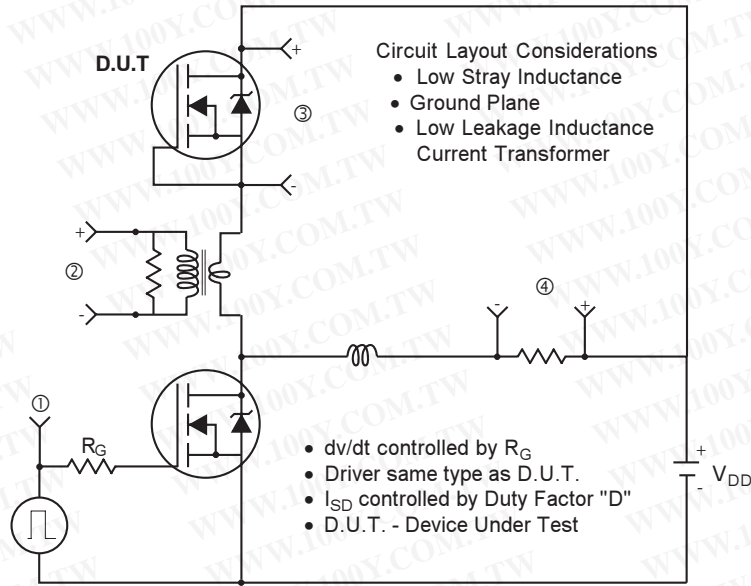


Fig 13b. Gate Charge Test Circuit

Peak Diode Recovery dv/dt Test Circuit



* $V_{GS} = 5V$ for Logic Level Devices

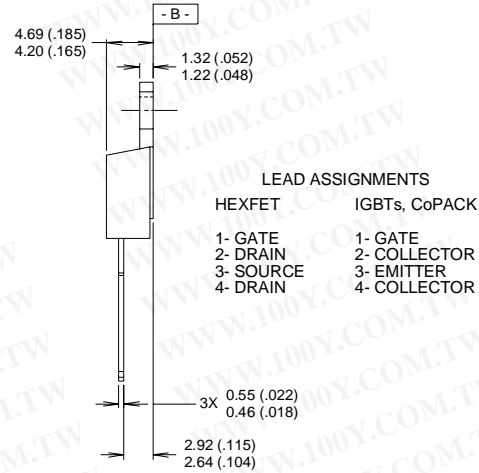
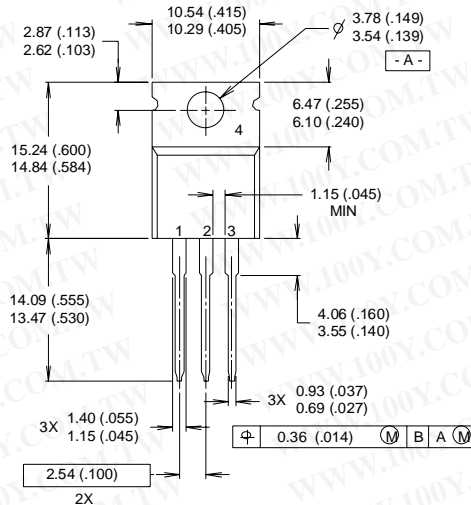
Fig 14. For N-Channel HEXFET® Power MOSFETs

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TO-220AB Package Outline

Dimensions are shown in millimeters (inches)



NOTES:

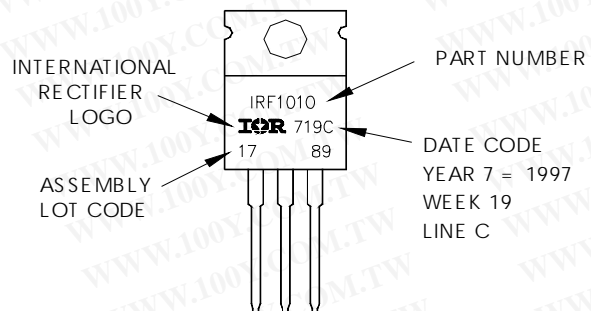
- 1 DIMENSIONING & TOLERANCING PER ANSI Y14.5M, 1982.
- 2 CONTROLLING DIMENSION : INCH

- 3 OUTLINE CONFORMS TO JEDEC OUTLINE TO-220AB.
- 4 HEATSINK & LEAD MEASUREMENTS DO NOT INCLUDE BURRS.

TO-220AB Part Marking Information

EXAMPLE: THIS IS AN IRF1010
 LOT CODE 1789
 ASSEMBLED ON WW 19, 1997
 IN THE ASSEMBLY LINE "C"

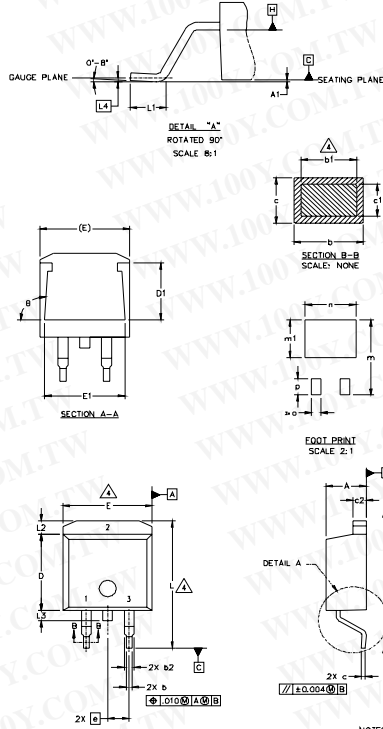
Note: "P" in assembly line position indicates "Lead-Free"



International
IR Rectifier

D²Pak Package Outline

Dimensions are shown in millimeters (inches)



| SYMBOL | DIMENSIONS | | | | NOTES |
|--------|-------------|-------|----------|------|-------|
| | MILLIMETERS | | INCHES | | |
| | MIN. | MAX. | MIN. | MAX. | |
| A | 4.06 | 4.83 | .160 | .190 | 4 |
| A1 | 0.51 | 0.127 | .020 | .005 | |
| b | 0.51 | 0.99 | .020 | .039 | 4 |
| b1 | 0.51 | 0.89 | .020 | .035 | |
| b2 | 1.14 | 1.40 | .045 | .055 | 4 |
| c | 0.43 | 0.63 | .017 | .025 | |
| c1 | 0.38 | 0.74 | .015 | .029 | 3 |
| c2 | 1.14 | 1.40 | .045 | .055 | |
| D | 8.51 | 9.65 | .335 | .380 | 3 |
| D1 | 5.33 | | .210 | | |
| E | 9.65 | 10.67 | .380 | .420 | 3 |
| E1 | 6.22 | | .245 | | |
| e | 2.54 BSC | | .100 BSC | | |
| L | 14.61 | 15.88 | .575 | .625 | |
| L1 | 1.78 | 2.79 | .070 | .110 | |
| L2 | | 1.65 | | .065 | |
| L3 | 1.27 | 1.78 | .050 | .070 | |
| L4 | 0.25 BSC | | .010 BSC | | |
| m | 17.78 | | .700 | | |
| m1 | 8.89 | | .350 | | |
| n | 11.43 | | .450 | | |
| o | 2.08 | | .082 | | |
| p | 3.81 | | .150 | | |
| theta | 90° | 93° | 90° | 93° | |

LEAD ASSIGNMENTS

| HEXFET | IGBTs, CoPACK | DIODES |
|------------|---------------|-------------|
| 1.- GATE | 1.- GATE | 1.- ANODE * |
| 2.- DRAIN | 2.- COLLECTOR | 2.- CATHODE |
| 3.- SOURCE | 3.- EMITTER | 3.- ANODE |

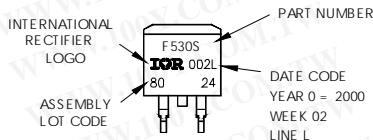
* PART DEPENDENT.

NOTES:

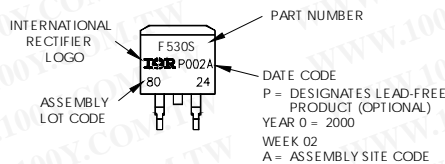
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994
2. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES]
3. DIMENSION D & E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.127 [0.005] PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTMOST EXTREMES OF THE PLASTIC BODY.
4. DIMENSION b1 AND c1 APPLY TO BASE METAL ONLY.
5. CONTROLLING DIMENSION: INCH.

D²Pak Part Marking Information

EXAMPLE: THIS IS AN IRF530S WITH LOT CODE 8024 ASSEMBLED ON WW 02, 2000 IN THE ASSEMBLY LINE "L"
Note: "P" in assembly line position indicates "Lead-Free"



OR

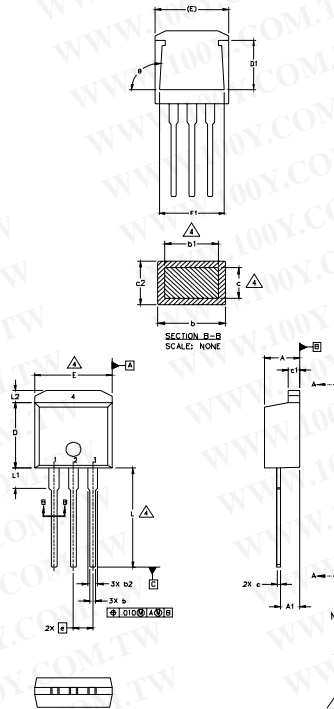


IRL3714/S/LPbF

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IR Rectifier

TO-262 Package Outline

Dimensions are shown in millimeters (inches)



| SYMBOL | DIMENSIONS | | | | NOTES |
|--------|-------------|-------|----------|------|-------|
| | MILLIMETERS | | INCHES | | |
| | MIN. | MAX. | MIN. | MAX. | |
| A | 4.06 | 4.83 | .160 | .190 | |
| A1 | 2.03 | 2.92 | .080 | .115 | |
| b | 0.51 | 0.99 | .020 | .039 | |
| b1 | 0.51 | 0.89 | .020 | .035 | 4 |
| b2 | 1.14 | 1.40 | .045 | .055 | |
| c | 0.38 | 0.63 | .015 | .025 | 4 |
| c1 | 1.14 | 1.40 | .045 | .055 | |
| c2 | 0.43 | .063 | .017 | .029 | |
| D | 8.51 | 9.65 | .335 | .380 | 3 |
| D1 | 5.33 | | .210 | | |
| E | 9.65 | 10.67 | .380 | .420 | 3 |
| E1 | 6.22 | | .245 | | |
| e | 2.54 BSC | | .100 BSC | | |
| L | 13.46 | 14.09 | .530 | .555 | |
| L1 | 3.56 | 3.71 | .140 | .146 | |
| L2 | | 1.65 | | .065 | |

LEAD ASSIGNMENTS

HEXFET

- 1.- GATE
- 2.- DRAIN
- 3.- SOURCE
- 4.- DRAIN

IGBT

- 1 - GATE
- 2 - COLLECTOR
- 3 - EMITTER

NOTES:

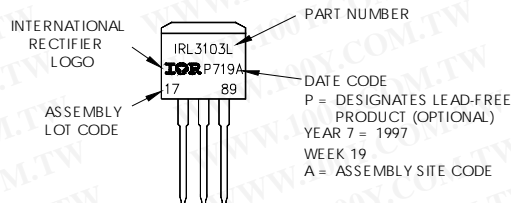
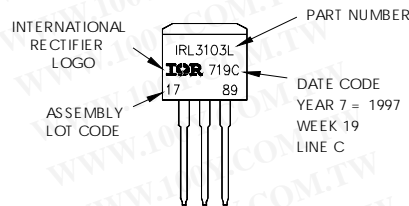
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994
2. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES]
3. DIMENSION D & E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.127 [.005"] PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTMOST EXTREMES OF THE PLASTIC BODY.
4. DIMENSION b1 AND c1 APPLY TO BASE METAL ONLY.
5. CONTROLLING DIMENSION: INCH.

TO-262 Part Marking Information

EXAMPLE: THIS IS AN IRL3103L
LOT CODE 1789
ASSEMBLED ON WW 19, 1997
IN THE ASSEMBLY LINE "C"

Note: "P" in assembly line position indicates "Lead-Free"

OR

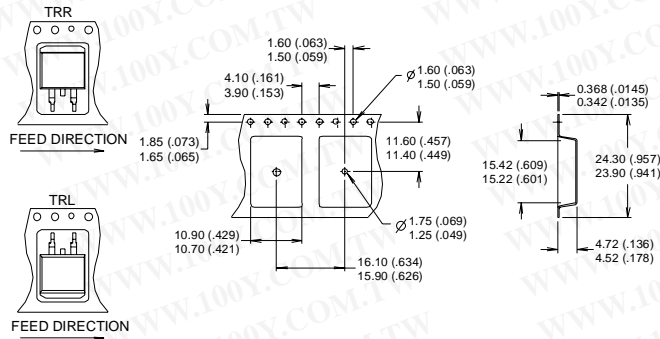


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IRL3714/S/LPbF

D²Pak Tape & Reel Information

Dimensions are shown in millimeters (inches)



- NOTES:
1. CONFORMS TO EIA-418.
 2. CONTROLLING DIMENSION: MILLIMETER.
 3. DIMENSION MEASURED @ HUB.
 4. INCLUDES FLANGE DISTORTION @ OUTER EDGE.

Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature.
- ② Starting $T_j = 25^\circ\text{C}$, $L = 0.69\text{ mH}$
 $R_G = 25\Omega$, $I_{AS} = 14\text{A}$.
- ③ Pulse width $\leq 400\mu\text{s}$; duty cycle $\leq 2\%$.
- ④ This is only applied to TO-220AB package.
- ⑤ This is applied to D²Pak, when mounted on 1" square PCB (FR-4 or G-10 Material).
 For recommended footprint and soldering techniques refer to application note #AN-994.

Data and specifications subject to change without notice.

These products have been designed and qualified for the Industrial market.

Qualification Standards can be found on IR's Web site.

International
IR Rectifier

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TAC Fax: (310) 252-7903

Visit us at www.irf.com for sales contact information.07/04