

勝特力材料 886-3-5753170  
 勝特力电子(上海) 86-21-34970699  
 勝特力电子(深圳) 86-755-83298787  
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PD - 95554A

# International **IR** Rectifier

SMPS MOSFET

# IRLR3714PbF IRLU3714PbF

HEXFET® Power MOSFET

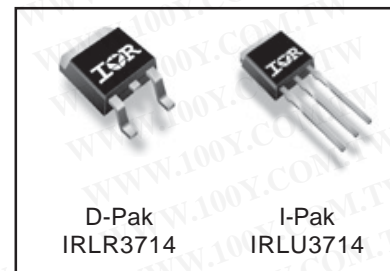
### Applications

- High Frequency Isolated DC-DC Converters with Synchronous Rectification for Telecom and Industrial Use
- High Frequency Buck Converters for Computer Processor Power
- Lead-Free

### Benefits

- Ultra-Low Gate Impedance
- Very Low  $R_{DS(on)}$  at 4.5V  $V_{GS}$
- Fully Characterized Avalanche Voltage and Current

$V_{DSS}$	$R_{DS(on)}$ max	$I_D$
20V	20m $\Omega$	36A



### Absolute Maximum Ratings

Symbol	Parameter	Max.	Units
$V_{DS}$	Drain-Source Voltage	20	V
$V_{GS}$	Gate-to-Source Voltage	$\pm 20$	V
$I_D @ T_C = 25^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10\text{V}$	36 <sup>⑤</sup>	A
$I_D @ T_C = 70^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10\text{V}$	31	
$I_{DM}$	Pulsed Drain Current <sup>①</sup>	140	
$P_D @ T_C = 25^\circ\text{C}$	Maximum Power Dissipation <sup>③</sup>	47	W
$P_D @ T_C = 70^\circ\text{C}$	Maximum Power Dissipation <sup>③</sup>	33	W
	Linear Derating Factor	0.31	W/ $^\circ\text{C}$
$T_J, T_{STG}$	Junction and Storage Temperature Range	-55 to +175	$^\circ\text{C}$

### Thermal Resistance

	Parameter	Typ.	Max.	Units
$R_{\theta JC}$	Junction-to-Case	—	3.2	$^\circ\text{C/W}$
$R_{\theta JA}$	Junction-to-Ambient	—	50	
$R_{\theta JA}$	Junction-to-Ambient (PCB mount) <sup>④</sup>	—	110	

Notes <sup>①</sup> through <sup>⑤</sup> are on page 10

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## Static @ T<sub>J</sub> = 25°C (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
V <sub>(BR)DSS</sub>	Drain-to-Source Breakdown Voltage	20	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA
ΔV <sub>(BR)DSS/ΔT<sub>J</sub></sub>	Breakdown Voltage Temp. Coefficient	—	0.022	—	V/°C	Reference to 25°C, I <sub>D</sub> = 1mA
R <sub>DS(on)</sub>	Static Drain-to-Source On-Resistance	—	15	20	mΩ	V <sub>GS</sub> = 10V, I <sub>D</sub> = 18A ③
		—	21	28		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 14A ③
V <sub>GS(th)</sub>	Gate Threshold Voltage	1.0	—	3.0	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA
I <sub>DSS</sub>	Drain-to-Source Leakage Current	—	—	20	μA	V <sub>DS</sub> = 16V, V <sub>GS</sub> = 0V
		—	—	100		V <sub>DS</sub> = 16V, V <sub>GS</sub> = 0V, T <sub>J</sub> = 125°C
I <sub>GSS</sub>	Gate-to-Source Forward Leakage	—	—	200	nA	V <sub>GS</sub> = 16V
	Gate-to-Source Reverse Leakage	—	—	-200		V <sub>GS</sub> = -16V

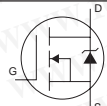
## Dynamic @ T<sub>J</sub> = 25°C (unless otherwise specified)

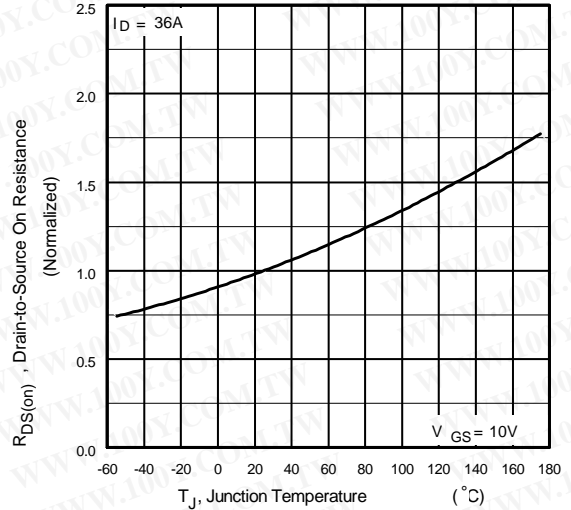
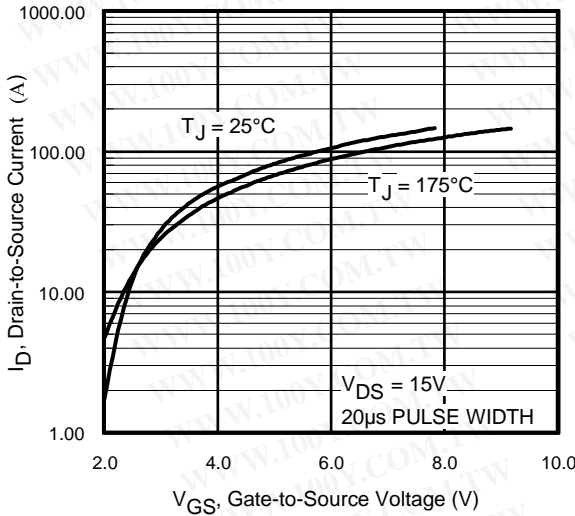
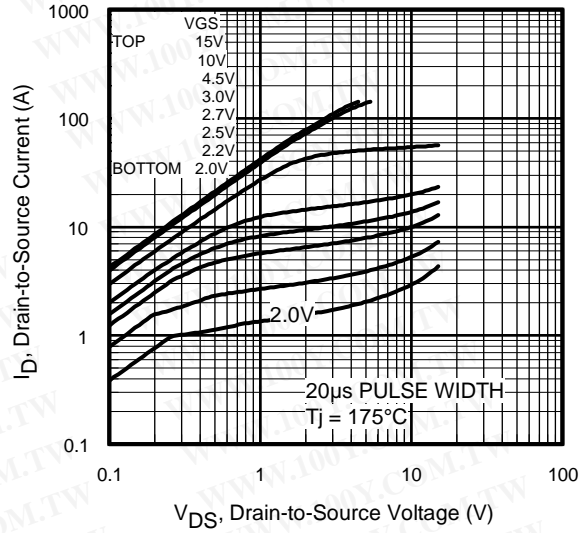
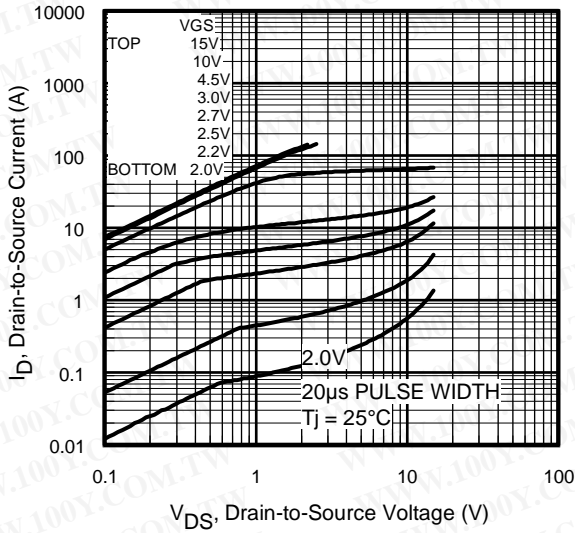
Symbol	Parameter	Min.	Typ.	Max.	Units	Conditions
g <sub>fs</sub>	Forward Transconductance	17	—	—	S	V <sub>DS</sub> = 10V, I <sub>D</sub> = 14A
Q <sub>g</sub>	Total Gate Charge	—	6.5	9.7	nC	I <sub>D</sub> = 14A
Q <sub>gs</sub>	Gate-to-Source Charge	—	1.8	—		V <sub>DS</sub> = 10V
Q <sub>gd</sub>	Gate-to-Drain ("Miller") Charge	—	2.9	—		V <sub>GS</sub> = 4.5V
Q <sub>oss</sub>	Output Gate Charge	—	7.1	—		V <sub>GS</sub> = 0V, V <sub>DS</sub> = 10V
t <sub>d(on)</sub>	Turn-On Delay Time	—	8.7	—	ns	V <sub>DD</sub> = 10V
t <sub>r</sub>	Rise Time	—	78	—		I <sub>D</sub> = 14A
t <sub>d(off)</sub>	Turn-Off Delay Time	—	10	—		R <sub>G</sub> = 1.8Ω
t <sub>f</sub>	Fall Time	—	4.5	—		V <sub>GS</sub> = 4.5V ③
C <sub>iss</sub>	Input Capacitance	—	670	—	pF	V <sub>GS</sub> = 0V
C <sub>oss</sub>	Output Capacitance	—	470	—		V <sub>DS</sub> = 10V
C <sub>rss</sub>	Reverse Transfer Capacitance	—	68	—		f = 1.0MHz

## Avalanche Characteristics

Symbol	Parameter	Typ.	Max.	Units
E <sub>AS</sub>	Single Pulse Avalanche Energy②	—	72	mJ
I <sub>AR</sub>	Avalanche Current①	—	14	A

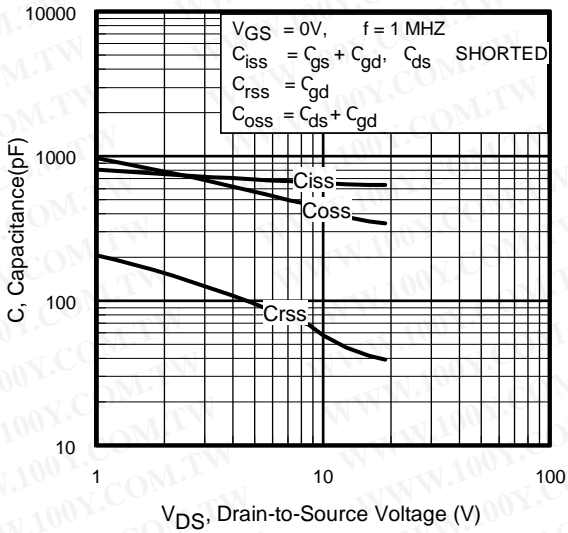
## Diode Characteristics

Symbol	Parameter	Min.	Typ.	Max.	Units	Conditions
I <sub>S</sub>	Continuous Source Current (Body Diode)	—	36⑤	—	A	MOSFET symbol showing the integral reverse p-n junction diode. 
I <sub>SM</sub>	Pulsed Source Current (Body Diode) ①	—	140	—		
V <sub>SD</sub>	Diode Forward Voltage	—	—	1.3	V	T <sub>J</sub> = 25°C, I <sub>S</sub> = 18A, V <sub>GS</sub> = 0V ③
		—	0.88	—		T <sub>J</sub> = 125°C, I <sub>S</sub> = 18A, V <sub>GS</sub> = 0V ③
t <sub>rr</sub>	Reverse Recovery Time	—	35	53	ns	T <sub>J</sub> = 25°C, I <sub>F</sub> = 18A, V <sub>R</sub> = 10V
Q <sub>rr</sub>	Reverse Recovery Charge	—	34	51	nC	di/dt = 100A/μs ③
t <sub>rr</sub>	Reverse Recovery Time	—	35	53	ns	T <sub>J</sub> = 125°C, I <sub>F</sub> = 18A, V <sub>R</sub> = 10V
Q <sub>rr</sub>	Reverse Recovery Charge	—	35	53	nC	di/dt = 100A/μs ③

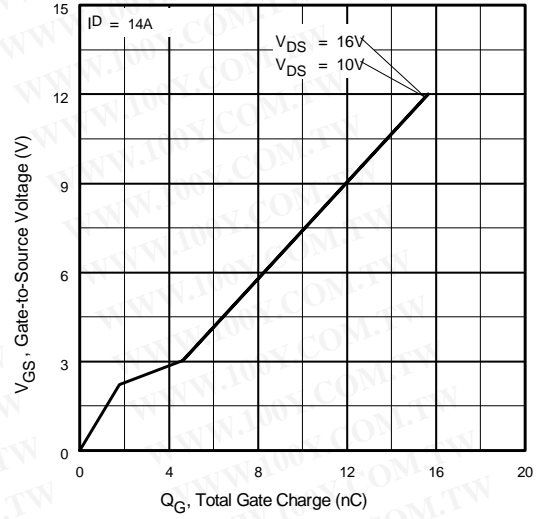


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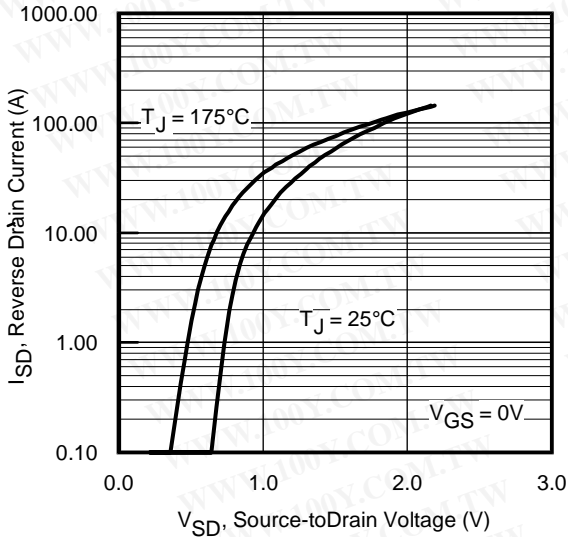
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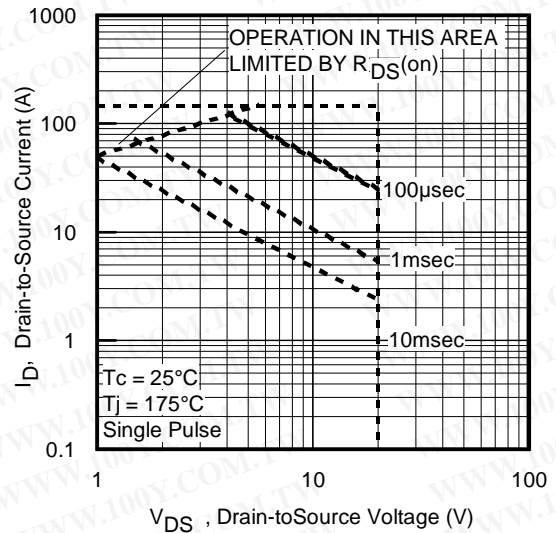
**Fig 5.** Typical Capacitance Vs. Drain-to-Source Voltage



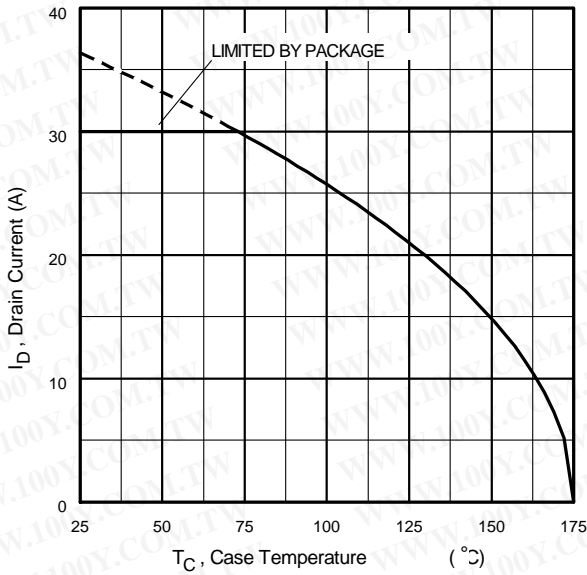
**Fig 6.** Typical Gate Charge Vs. Gate-to-Source Voltage



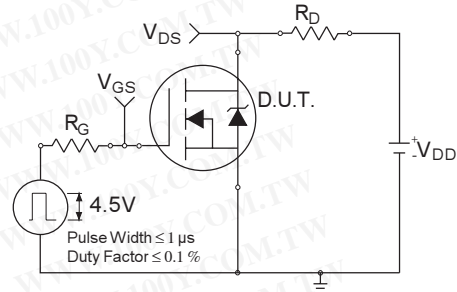
**Fig 7.** Typical Source-Drain Diode Forward Voltage



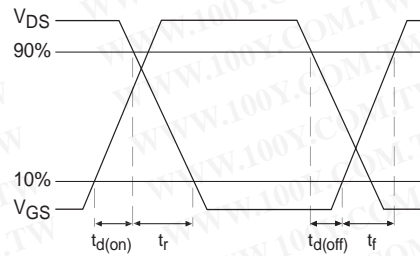
**Fig 8.** Maximum Safe Operating Area



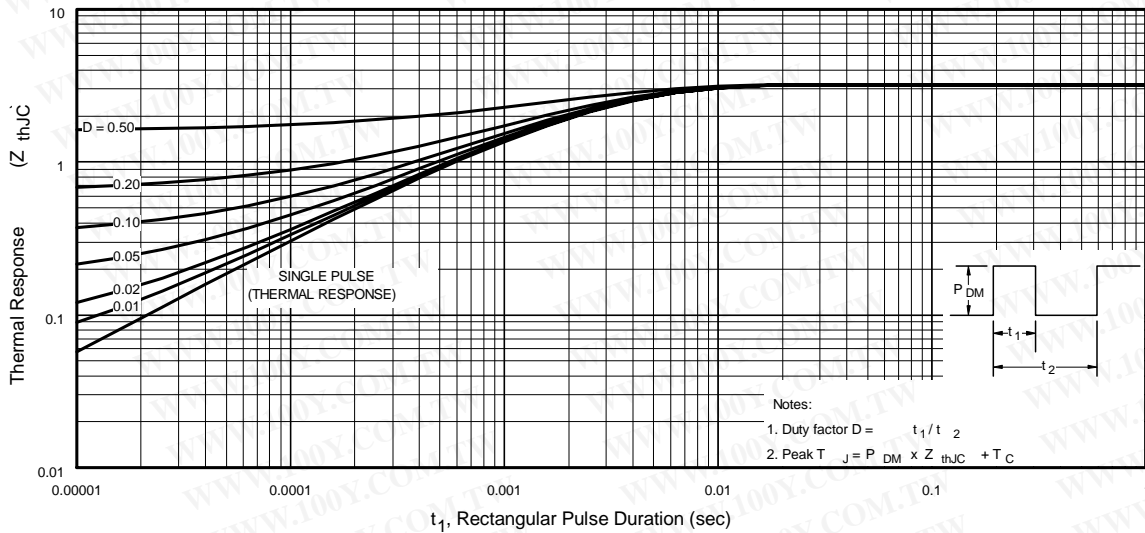
**Fig 9.** Maximum Drain Current Vs. Case Temperature



**Fig 10a.** Switching Time Test Circuit



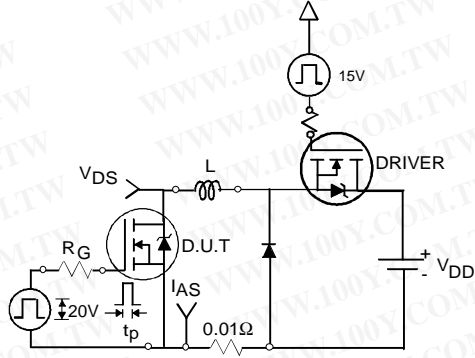
**Fig 10b.** Switching Time Waveforms



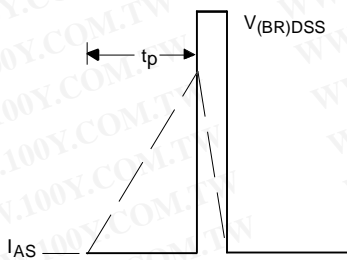
**Fig 11.** Maximum Effective Transient Thermal Impedance, Junction-to-Case

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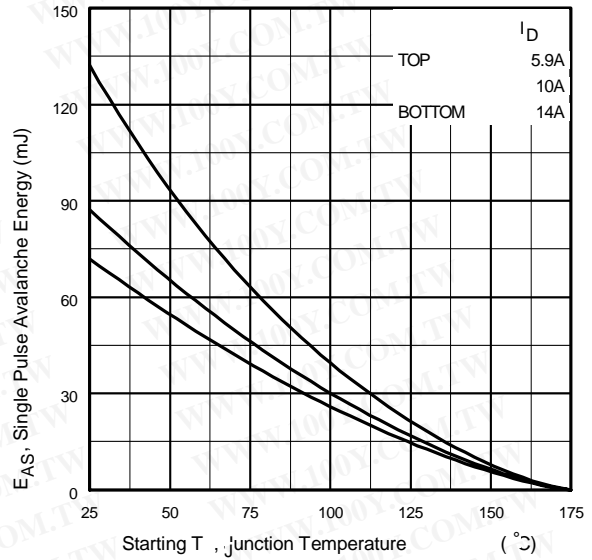
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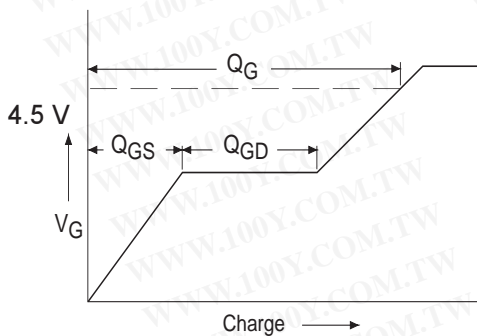
**Fig 12a.** Unclamped Inductive Test Circuit



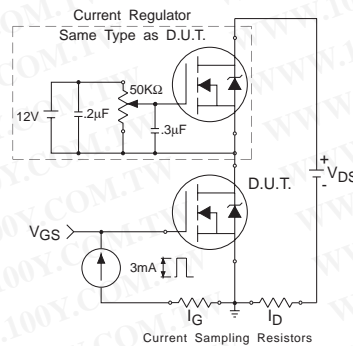
**Fig 12b.** Unclamped Inductive Waveforms



**Fig 12c.** Maximum Avalanche Energy Vs. Drain Current

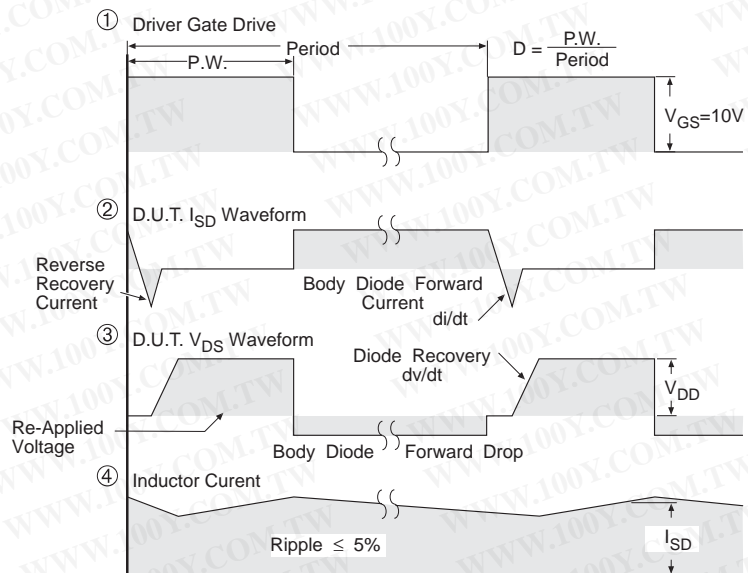
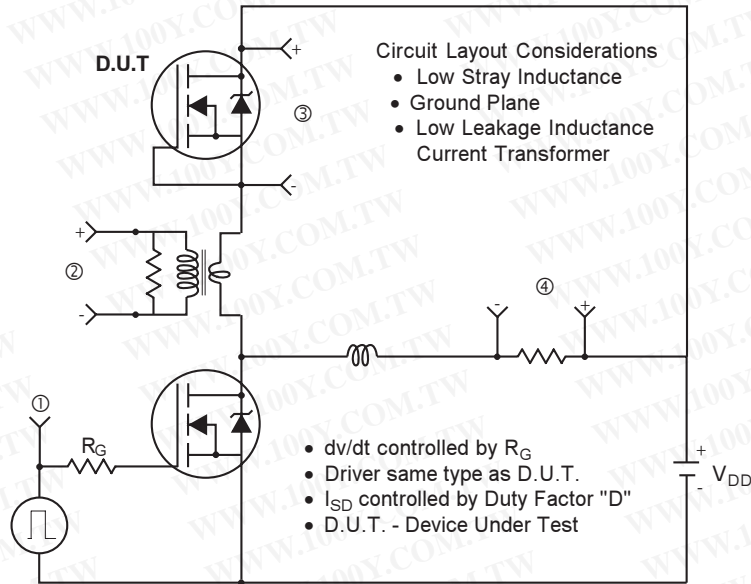


**Fig 13a.** Basic Gate Charge Waveform



**Fig 13b.** Gate Charge Test Circuit

## Peak Diode Recovery dv/dt Test Circuit



\*  $V_{GS} = 5V$  for Logic Level Devices

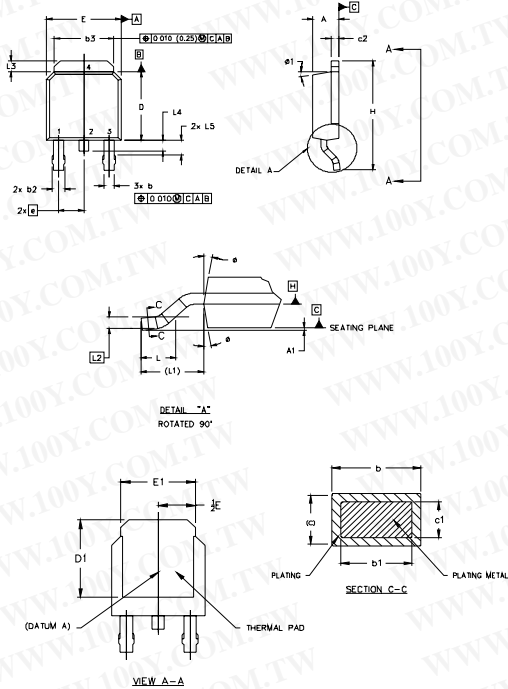
**Fig 14.** For N-Channel HEXFET<sup>®</sup> Power MOSFETs

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## D-Pak (TO-252AA) Package Outline

Dimensions are shown in millimeters (inches)



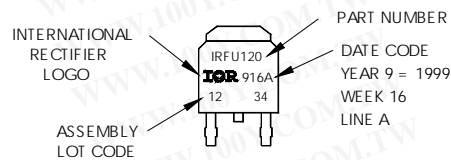
- NOTES:
- 1.0 DIMENSIONING AND TOLERANCING PER ASME Y14.5 M- 1994.
  - 2.0 DIMENSIONS ARE SHOWN IN INCHES [MILLIMETERS].
  - 3.0 LEAD DIMENSION UNCONTROLLED IN L5
  - 4.0 DIMENSION D1 AND E1 ESTABLISH A MINIMUM MOUNTING SURFACE FOR THERMAL PAD.
  - 5.0 SECTION C-C DIMENSIONS APPLY TO THE FLAT SECTION OF THE LEAD BETWEEN .005 [0.127] AND .010 [0.2540] FROM THE LEAD TIP.
  - 6.0 DIMENSION D & E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED .005" (0.127) PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY.
  - 7.0 OUTLINE CONFORMS TO JEDEC OUTLINE TO-252AA.

SYMBOL	DIMENSIONS				NOTES
	MILLIMETERS		INCHES		
	MIN.	MAX.	MIN.	MAX.	
A	2.18	2.39	.086	.094	
A1	0.13	0.13	.005	.005	
a	0.64	0.89	.025	.035	LEAD ASSIGNMENTS
b1	0.64	0.78	.025	0.031	
b2	0.76	1.14	.030	.045	HEXFEET
b3	4.85	5.46	.195	.215	1.- GATE
c	0.46	0.61	.018	.024	2.- DRAIN
c1	0.41	0.56	.016	.022	3.- SOURCE
c2	.046	0.89	.018	.035	4.- DRAIN
D	5.97	6.22	.235	.245	
D1	5.21	-	.205	-	
E	6.35	6.73	.250	.265	
E1	4.32	-	.170	-	
e	2.29		.090 BSC		IGBTs, CoPACK
H	9.40	10.41	.370	.410	1.- GATE
L	1.40	1.78	.055	.070	2.- COLLECTOR
L1	2.74 REF.		.108 REF.		3.- EMITTER
L2	0.051 BSC		.020 BSC		4.- COLLECTOR
L5	0.89	1.27	.035	.050	
L4	1.14	1.52	.045	.060	
ø	0"	10"	0"	10"	
ø1	0"	15"	0"	15"	

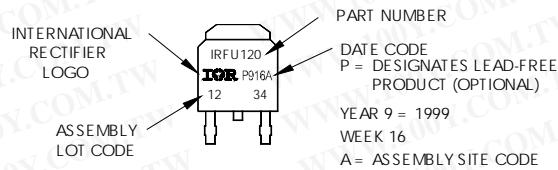
## D-Pak (TO-252AA) Part Marking Information

EXAMPLE: THIS IS AN IRFR120  
WITH ASSEMBLY  
LOT CODE 1234  
ASSEMBLED ON WW 16, 1999  
IN THE ASSEMBLY LINE "A"

Note: "P" in assembly line position  
indicates "Lead-Free"



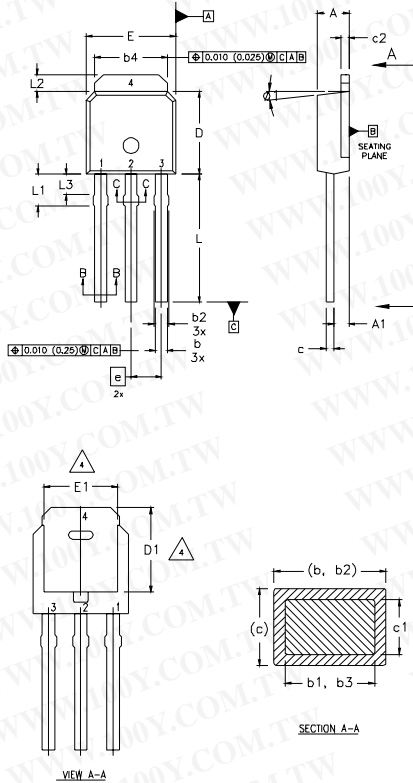
OR





## I-Pak (TO-251AA) Package Outline

Dimensions are shown in millimeters (inches)



**NOTES:**

- 1 DIMENSIONING AND TOLERANCING PER ASME Y14.5 M- 1994.
- 2 DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].
- 3 DIMENSION D & E DO NOT INCLUDE MOLD FLASH, MOLD FLASH SHALL NOT EXCEED 0.005" (0.127) PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY.
- 4 THERMAL PAD CONTOUR OPTION WITHIN DIMENSION b4, L2, E1 & D1.
- 5 LEAD DIMENSION UNCONTROLLED IN L3.
- 6 DIMENSION b1, b3 APPLY TO BASE METAL ONLY.
- 7 OUTLINE CONFORMS TO JEDEC OUTLINE TO-251AA.
- 8 CONTROLLING DIMENSION : INCHES.

**LEAD ASSIGNMENTS**

SYMBOL	DIMENSIONS		DIMENSIONS		NOTES
	MIN.	MAX.	MIN.	MAX.	
A	2.18	2.39	0.086	.094	
A1	0.89	1.14	0.035	0.045	
b	0.64	0.89	0.025	0.035	
b1	0.64	0.79	0.025	0.031	4
b2	0.76	1.14	0.030	0.045	
b3	0.76	1.04	0.030	0.041	
b4	5.00	5.46	0.195	0.215	4
c	0.46	0.61	0.018	0.024	
c1	0.41	0.56	0.016	0.022	
c2	.046	0.86	0.018	0.035	3, 4
D	5.97	6.22	0.235	0.245	3, 4
D1	5.21	-	0.205	-	4
E	6.35	6.73	0.250	0.265	3, 4
E1	4.32	-	0.170	-	4
e	2.29		0.090 BSC		
L	8.89	9.60	0.350	0.380	
L1	1.91	2.29	0.075	0.090	
L2	0.89	1.27	0.035	0.050	4
L3	1.14	1.52	0.045	0.060	5
ø1	0"	15'	0"	15'	

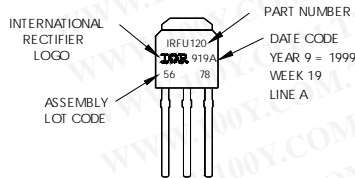
**HEXFET**

- 1.- GATE
- 2.- DRAIN
- 3.- SOURCE
- 4.- DRAIN

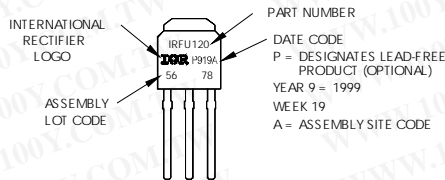
## I-Pak (TO-251AA) Part Marking Information

EXAMPLE: THIS IS AN IRFU120 WITH ASSEMBLY LOT CODE 5678 ASSEMBLED ON WW 19, 1999 IN THE ASSEMBLY LINE "A"

Note: "P" in assembly line position indicates "Lead-Free"



OR

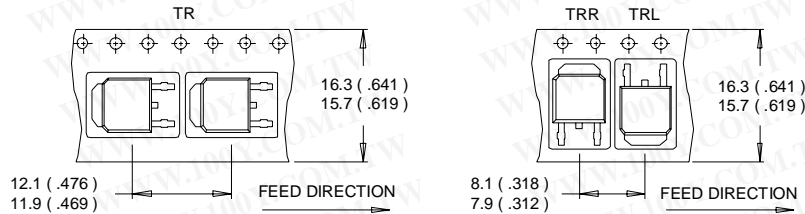


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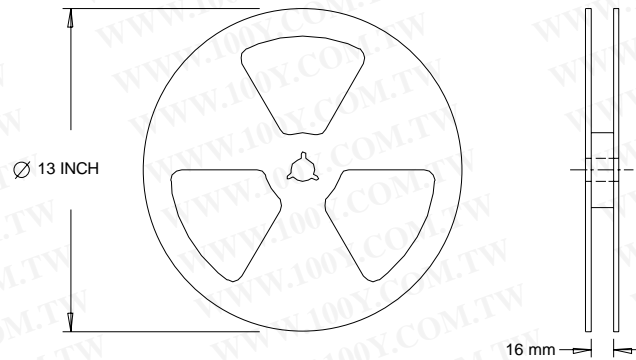
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## D-Pak (TO-252AA) Tape & Reel Information

Dimensions are shown in millimeters (inches)



- NOTES:
1. CONTROLLING DIMENSION : MILLIMETER.
  2. ALL DIMENSIONS ARE SHOWN IN MILLIMETERS ( INCHES ).
  3. OUTLINE CONFORMS TO EIA-481 & EIA-541.



- NOTES:
1. OUTLINE CONFORMS TO EIA-481.

### Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature.
- ② Starting  $T_J = 25^\circ\text{C}$ ,  $L = 0.69\text{ mH}$   
 $R_G = 25\Omega$ ,  $I_{AS} = 14\text{A}$ .
- ③ Pulse width  $\leq 400\mu\text{s}$ ; duty cycle  $\leq 2\%$ .
- ④ When mounted on 1" square PCB ( FR-4 or G-10 Material ). For recommended footprint and soldering techniques refer to application note #AN-994.
- ⑤ Calculated continuous current based on maximum allowable junction temperature; Package limitation current is 30A

Data and specifications subject to change without notice.

These products have been designed and qualified for the Industrial market.

Qualification Standards can be found on IR's Web site.

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**IR** Rectifier

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