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N		REVISIONS	DOC. NO. SPC-F005 * Effective: 7/8/02 * DCP No: 1398						
DCP #	REV	DESCRIPTION	DRAWN	DATE	CHECKD	DATE	APPRVD	DATE	
1262	Α	RELEASED	НО	2/4/03	JWM	2/4/03	JC	2/4/03	
1885	В	UPDATED TO ROHS COMPLIANT	EO	02/03/06	НО	2/6/06	но	2/6/06	

	RoHS
9	Compliant

Absolute Maximum Ratings:

- Collector-Emitter Voltage, $V_{CEO} = 50V$
- Collector-Base Voltage, $V_{CBO} = 75V$ Emitter-Base Voltage, $V_{CBO} = 75V$ Emitter-Base Voltage, $V_{EBO} = 5V$ Continuous Collector Current, $I_C = 2A$ Base Current, $I_B = 1A$

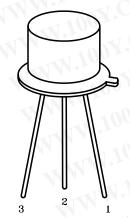
- Total Power Dissipation ($T_C = +25^{\circ}C$), $P_D = 10W$
 - Derate Above 25°C = 0.057nW/°C
- Operating Junction Temperature Range, $T_J=-65^\circ$ to +200°C Storage Temperature Range, $T_{stg}=-65^\circ$ to +200°C Thermal Resistance, Junction-to-Case, $R_{thJC}=17.5^\circ C/W$



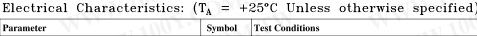








- 1. EMITTER
- 2. BASE
- 3 Collector

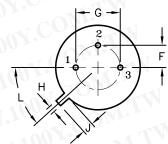


SPC-F005.DWG

Parameter	Symbol	Test Conditions	Min	Max	Unit
OFF Characteristics	CO_{D}		7.0		
Collector—Emitter Breakdown Voltage	V _{(BR)CEO}	$I_{\rm C} = 100$ mA, $I_{\rm B} = 0$ (Note 1)	50	7(V
Collector Cutoff Current	I _{CEX}	$V_{CE} = 75V, V_{BE} = 1.5V$	(-) X	0.1	mΑ
W.Iv	- ($V_{CE} = 45V, V_{BE} = 1.5V, T_{C} = +150^{\circ}C$	-	5.0	mΑ
Emitter Cutoff Current	I _{EBO}	$V_{BE} = 5V, I_{C} = 0$	90	0.1	mΑ
ON Characteristics (Note 1)	-7 (ONI		* 7	
DC Current Gain	h _{FE}	$I_C = 500$ mA, $V_{CE} = 4V$	40	250	-
Collector—Emitter Saturation Voltage	V _{CE(sat)}	I_C = 500mA, I_B = 50mA	-	1.2	V
Base—Emitter ON Voltage	V _{BE(on)}	$I_{C} = 500 \text{mA}, V_{CE} = 4 \text{V}$		1.4	٧
Small-Signal Characteristics	. 00	Z.C. TW		400	M.
Small—Signal Current Gain	hfe	$I_C = 50$ mA, $V_{CE} = 4$ V, $f = 10$ MHz	5.0	To.	-
Switching Characteristics	100	N. C. TIN		- 10	10
Turn-On Time	ton	$V_{CC} = 30V, I_{C} = 500$ mA, $I_{B1} = 50$ mA	FV	100	ns

Note 1. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.

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Dimensions	A	В	С	D	E	F	G	H	J	K	L
Min.	8.50	7.74	6.09	0.40	1 4.	2.41	4.82	0.71	0.73	12.70	45°
Max.	9.39	8.50	6.60	0.53	0.88	2.66	5.33	0.86	1.02	LAF	48°

Turn-On Time Turn-Off Time

ALL STATEMENTS AND TECHNICAL INFORMATION CONTAINED HEREIN ARE BASED UPON INFORMATION AND/OR TESTS WE BELIEVE TO BE ACCURATE AND RELIABLE. SINCE CONDITIONS OF USE ARE BEYOND OUR CONTROL, THE USER SHALL DETERMINE THE SUITABILITY OF THE PRODUCT FOR THE INTENDED USE AND ASSUME ALL RISK AND LIABILITY WHATSOEVER IN CONNECTION THEREWITH.

TOLERANCES:

UNLESS OTHERWISE SPECIFIED, DIMENSIONS ARE FOR REFERENCE PURPOSES ONLY.

 $V_{CC} = 30V$, $I_{C} = 500$ mA, $I_{B1} = I_{B2} = 50$ mA

DRAWN BY:	DATE:
HISHAM ODISH	2/4/03
CHECKED BY:	DATE:
JEFF MCVICKER	2/4/03
APPROVED BY:	DATE:
JOHN, COLF	2/4/03

DRAWING TITLE:

Transistor Silicon TO-39 PNP General Purpose

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<u> </u>	SIZE	DWG. NO.		ELECTRONIC FILE REV	,			
3	Α	VY.COn.	2N5323	35C0723.DWG B				
3	SCALE: NTS		U.O.M.: Millimeters	SHEET: 1 OF 1	N			