

THYRISTOR MODULE

100A / 1200 to 1600V

PDT10012 PDT10016

PDH10012 PDH10016

FEATURES

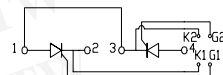
- * Isolated Base
- * Dual Thyristors or Thyristor and Diode Cascaded Circuit
- * High Surge Capability
- * UL Recognized, File No. E187184

OUTLINE DRAWING

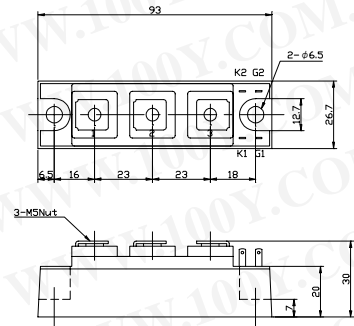
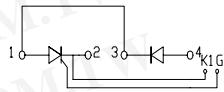
TYPICAL APPLICATIONS

- * Rectified For General Use

PDT



PDH



勝特力材料 886-3-5753170
 勝特力电子(上海) 86-21-34970699
 勝特力电子(深圳) 86-755-83298787
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Maximum Ratings

Approx Net Weight:155g

Parameter	Symbol	Grade		Unit
		PDT/PDH10012	PDT/PDH10016	
Repetitive Peak Off-State Voltage	V _{DRM}	1200	1600	V
Non Repetitive Peak Off-State Voltage	V _{DSM}	1300	1700	V
Repetitive Peak Reverse Voltage	V _{RDM}	1200	1600	V
Non Repetitive Peak Reverse Voltage	V _{RSM}	1300	1700	V

Parameter		Conditions	Max Rated Value	Unit
Average Rectified Output Current	I _{O(AV)}	50Hz Half Sine Wave condition T _c =77°C	100	A
RMS On-State Current	I _{T(RMS)}		156	A
Surge On-State Current	I _{TSM}	50 Hz Half Sine Wave, 1Pulse Non-Repetitive	2000	A
I Squared t	I ² t	2msec to 10msec	20000	A ² s
Critical Rate of Turned-On Current	di/dt	V _D =2/3V _{DRM} , I _{TM} =2·I _O , T _j =125°C I _G =200mA, dig/dt=0.2A/μs	100	A/μs
Peak Gate Power	P _{GM}		5	W
Average Gate Power	P _{G(AV)}		1	W
Peak Gate Current	I _{GM}		2	A
Peak Gate Voltage	V _{GM}		10	V
Peak Gate Reverse Voltage	V _{RGM}		5	V
Operating JunctionTemperature Range	T _{jw}		-40 to +125	°C
Storage Temperature Range	T _{stg}		-40 to +125	°C
Isoration Voltage	Viso	Base Plate to Terminals, AC1min	2500	V
Mounting torque	Case mounting	F _{tor}	M6 Screw	2.4 to 3.5
	Terminals		M5 Screw	2.4 to 2.8

Value per 1 Arm

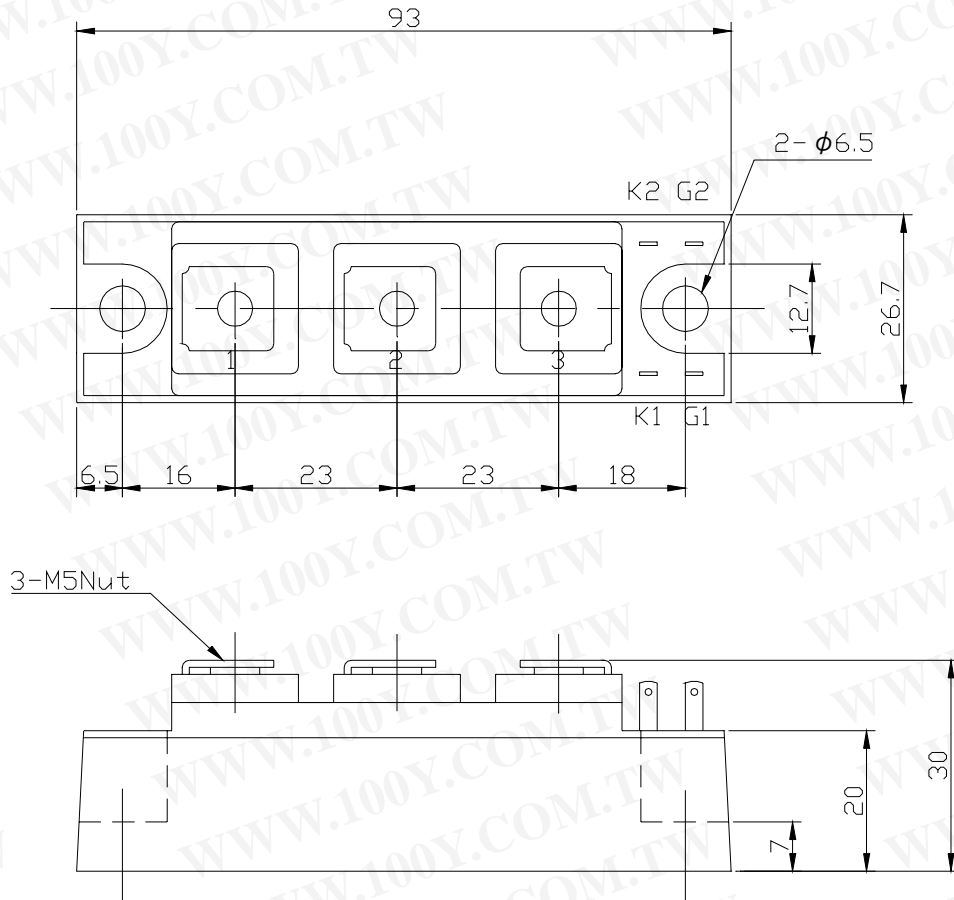
Electrical • Thermal Characteristics

Characteristics	Symbol	Test Conditions	Maximum Value.			Unit
			Min.	Typ.	Max.	
Peak Off-State Current	I_{DM}	$V_{DM} = V_{DRM}, T_j = 125^\circ\text{C}$			20	mA
Peak Reverse Current	I_{RM}	$V_{RM} = V_{RRM}, T_j = 125^\circ\text{C}$			20	mA
Peak Forward Voltage	V_{TM}	$I_{TM} = 300\text{A}, T_j = 25^\circ\text{C}$			1.38	V
Gate Current to Trigger	I_{GT}	$V_D = 6\text{V}, I_T = 1\text{A}$	$T_j = -40^\circ\text{C}$		200	mA
			$T_j = 25^\circ\text{C}$		100	
			$T_j = 125^\circ\text{C}$		50	
Gate Voltage to Trigger	V_{GT}	$V_D = 6\text{V}, I_T = 1\text{A}$	$T_j = -40^\circ\text{C}$		4	V
			$T_j = 25^\circ\text{C}$		2.5	
			$T_j = 125^\circ\text{C}$		2	
Gate Non-Trigger Voltage	V_{GD}	$V_D = 2/3V_{DRM}, T_j = 125^\circ\text{C}$	0.25			V
Critical Rate of Rise of Off-State Voltage	dv/dt	$V_D = 2/3V_{DRM}, T_j = 125^\circ\text{C}$	500			V/ μs
Turn-Off Time	tq	$I_{TM} = I_o, V_D = 2/3V_{DRM}$ $dv/dt = 20\text{V}/\mu\text{s}, V_R = 100\text{V}$ $-di/dt = 20\text{A}/\mu\text{s}, T_j = 125^\circ\text{C}$		100		μs
Turn-On Time	tgt	$V_D = 2/3V_{DRM}, T_j = 125^\circ\text{C}$ $I_G = 200\text{mA}, di/dt = 0.2\text{A}/\mu\text{s}$		6		μs
Delay Time	td			2		μs
Rise Time	tr			4		μs
Latching Current	I_L	$T_j = 25^\circ\text{C}$		100		mA
Holding Current	I_H	$T_j = 25^\circ\text{C}$		50		
Thermal Resistance	Rth(j-c)	Junction to Case			0.35	$^\circ\text{C}/\text{W}$
	Rth(c-f)	Base Plate to Heat Sink with Thermal Compound			0.2	

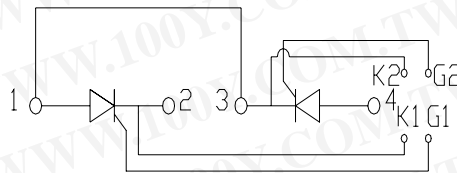
Value Per 1Arm

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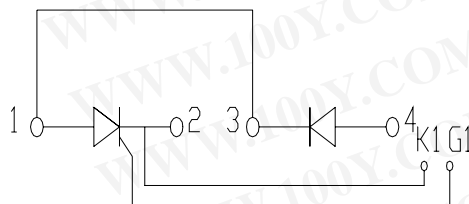
PDT/PDH1001x OUTLINE DRAWING (Dimensions in mm)



PDT



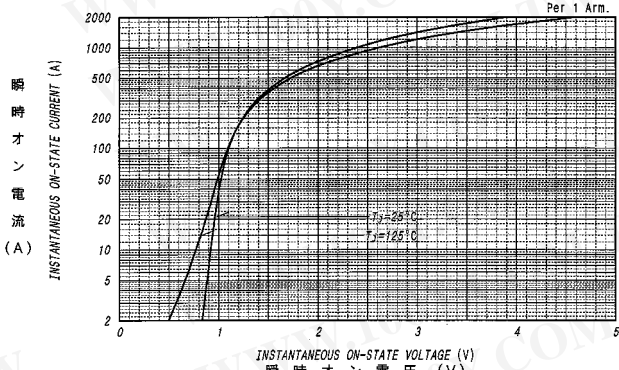
PDH



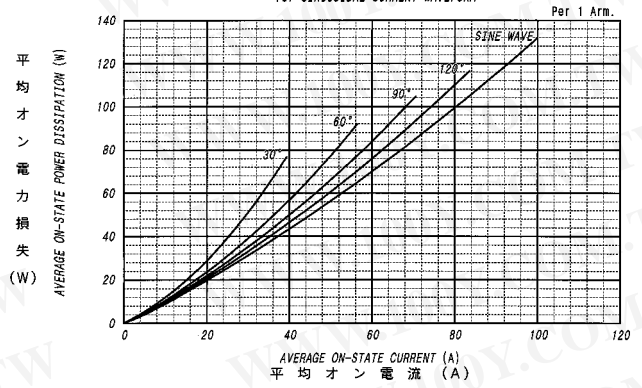
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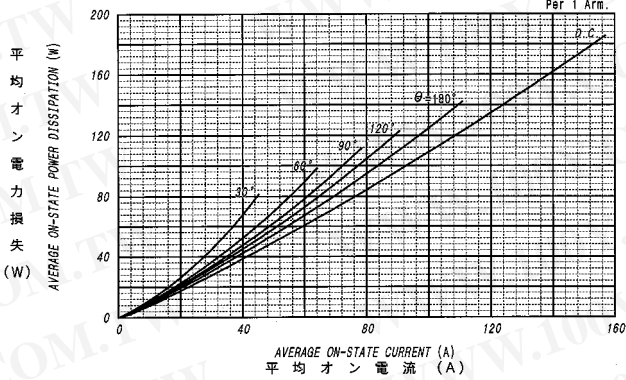
オン電圧特性
ON-STATE CURRENT VS. VOLTAGE



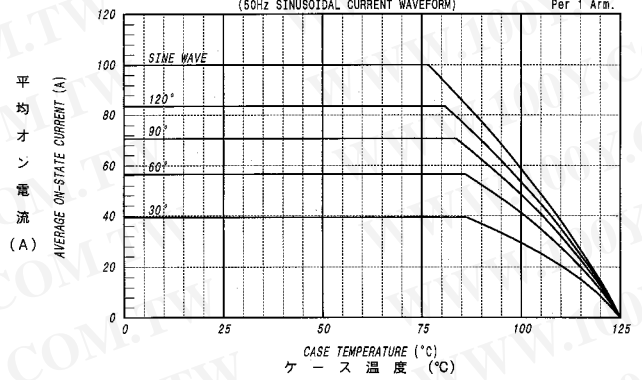
平均オン電力損失特性
AVERAGE ON-STATE POWER DISSIPATION
for SINUSOIDAL CURRENT WAVEFORM



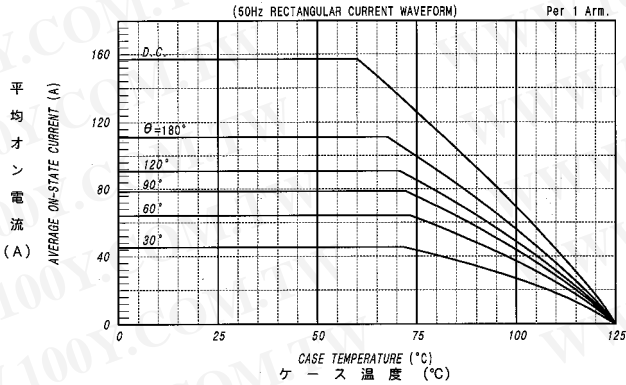
平均オン電力損失特性
AVERAGE ON-STATE POWER DISSIPATION
for RECTANGULAR CURRENT WAVEFORM



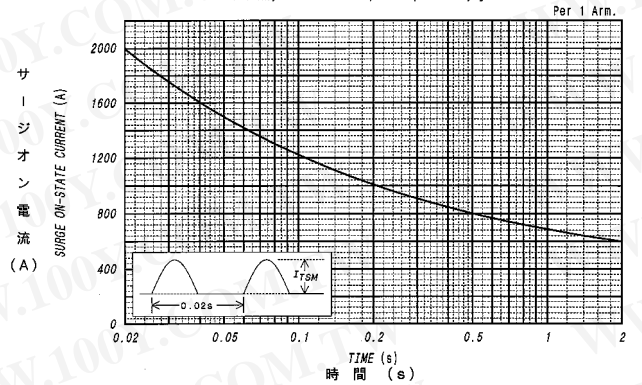
平均オン電流 - ケース温度定格
AVERAGE ON-STATE CURRENT VS. CASE TEMPERATURE



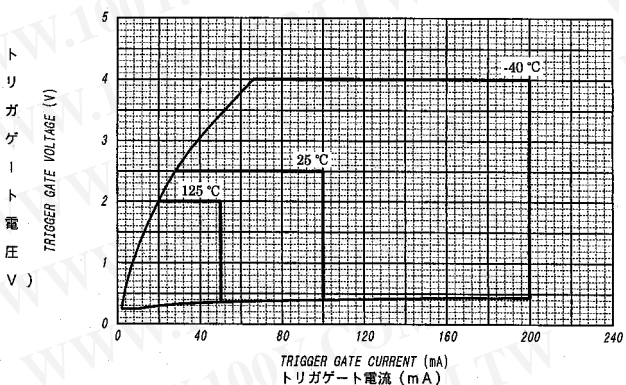
平均オン電流 - ケース温度定格
AVERAGE ON-STATE CURRENT VS. CASE TEMPERATURE



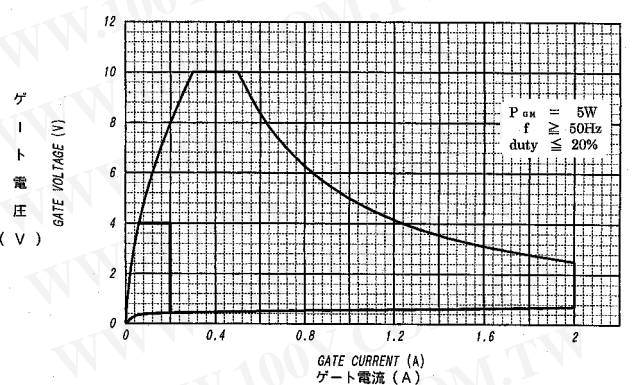
サージオン電流定格
SURGE CURRENT RATINGS
f=50Hz, Half Sine Wave, Non-Repetitive, Tj=125°C



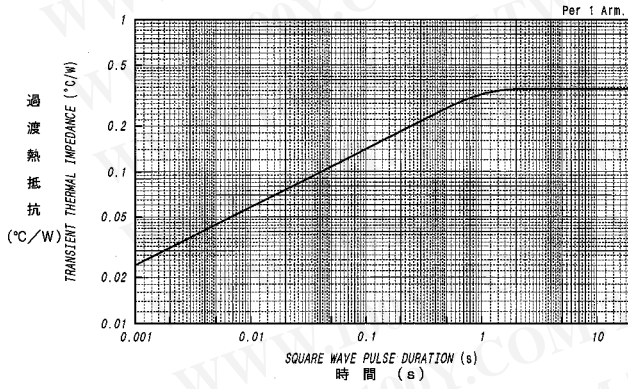
ゲート特性
GATE CHARACTERISTICS



ゲート定格
GATE RATINGS



過渡熱抵抗特性
MAXIMUM TRANSIENT THERMAL IMPEDANCE
Junction to Case



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