勝 特 力 材 料 886-3-5753170 胜特力电子(上海) 86-21-54151736 胜特力电子(深圳) 86-755-83298787

Http://www.100y.com.tw

Preferred Device

Dual Switching Diode

Features

 Pb–Free Package May be Available.* The G–Suffix Denotes a Pb–Free Lead Finish

MAXIMUM RATINGS $(T_A = 25^{\circ}C)$

Rating	Symbol	Max	Unit
Reverse Voltage	V _R	70	Vdc
Forward Current	TIF 100	200	mAdc
Peak Forward Surge Current	I _{FM(surge)}	500	mAdc

THERMAL CHARACTERISTICS

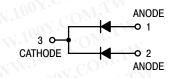
Characteristic	Symbol	Max	Unit
Total Device Dissipation, FR-4 Board (1) T _A = 25°C	PD	225	mW
Derated above 25°C		1.8	mW/°C
Thermal Resistance, Junction to Ambient (1)	$R_{ heta}$ JA	555	°C/W
Total Device Dissipation, FR-4 Board (2)	PD	360	mW
$T_A = 25^{\circ}C$	1	. 000 	V C
Derated above 25°C	T	2.9	mW/°C
Thermal Resistance, Junction-to-Ambient (2)	$R_{\theta JA}$	345	°C/W
Junction and Storage Temperature Range	T _J , T _{stg}	-55 to +150	√N.°C

- 1. FR-4 @ Minimum Pad
- 2. FR-4 @ 1.0×1.0 Inch Pad



ON Semiconductor®

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CASE 463 SOT-416/SC-75 STYLE 3

DEVICE MARKING



ORDERING INFORMATION

Device Package		Shipping [†]
BAV70TT1	SOT-416	3000 / Tape & Reel
BAV70TT1G	SOT-416 (Pb-Free)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

Preferred devices are recommended choices for future use and best overall value.

BAV70TT1 100Y.COM.TW

Characteristic	Symbol	Min	Max	Ur
OFF CHARACTERISTICS	N. To O.Y.CC	MIN		
Reverse Breakdown Voltage (I _(BR) = 100 μAdc)	V _(BR)	70	_	Vd
Reverse Voltage Leakage Current (Note 3) (VR = 70 Vdc) (VR = 50 Vdc)	I _R	COM.T	5.0 100	μΑα nΑα
Diode Capacitance (V _R = 0, f = 1.0 MHz)	CD 100	V.COM	1.5	pF
Forward Voltage (IF = 1.0 mAdc) (IF = 10 mAdc) (IF = 50 mAdc) (IF = 150 mAdc)	VFW.II	.100 <u>7</u> .CO	715 855 1000 1250	mV
Reverse Recovery Time (I _F = I _R = 10 mAdc, R _L = 100 Ω , I _{R(REC)} = 1.0 mAdc) (Figure 1)	t _{rr}	W.100Y.	6.0	ns
Forward Recovery Voltage (I _F = 10 mAdc, t _r = 20 ns) (Figure 2)	V _{RF}	M. 100X	1.75	V

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^{3.} For each individual diode while the second diode is unbiased. WWW.100Y.COM.TW WWW.100X. WWW.100Y

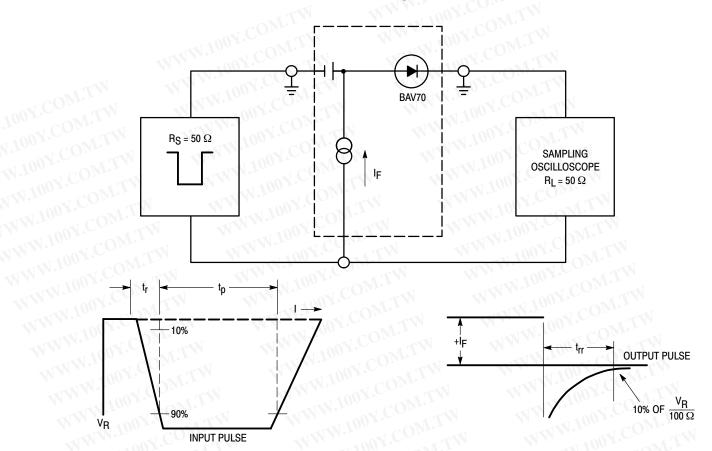
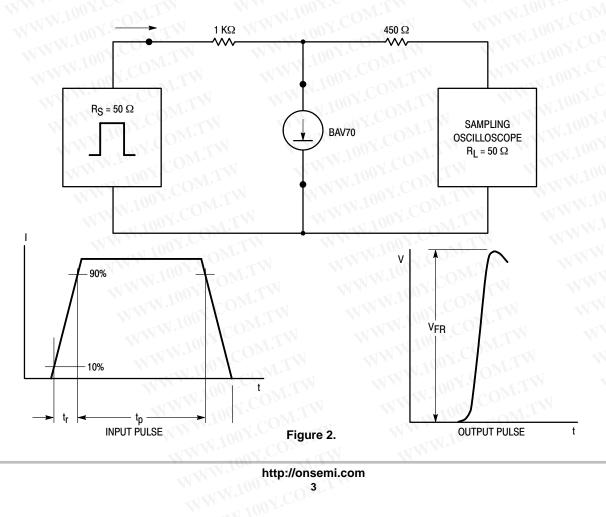


Figure 1. Recovery Time Equivalent Test Circuit



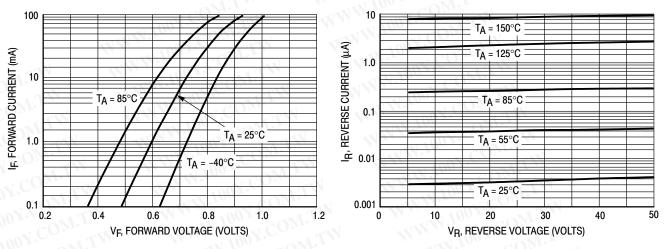


Figure 3. Forward Voltage

Figure 4. Leakage Current

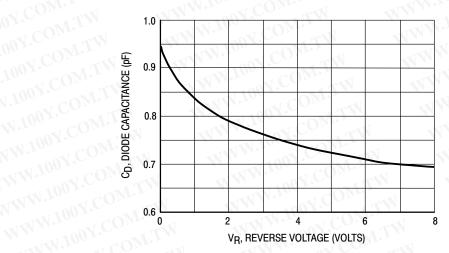


Figure 5. Capacitance

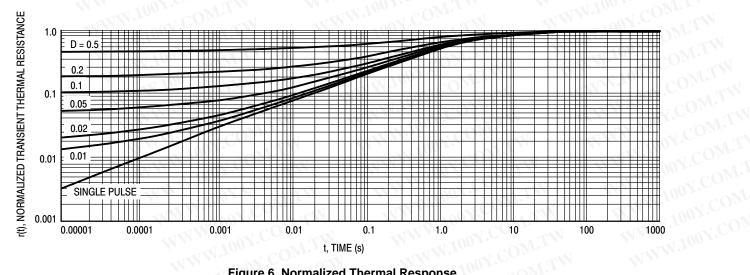


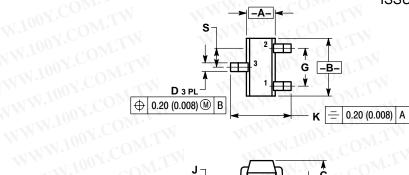
Figure 6. Normalized Thermal Response WWW.100Y.COM.TW

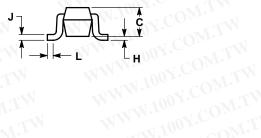
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PACKAGE DIMENSIONS

NW.100Y.COM.TW SC-75 (SC-90, SOT-416)

CASE 463-01 ISSUE C





- WWW.100Y.COM.TW NOTES:

ANSI	NSIONIN Y14.5M, FROLLING	1982.			
W. Carl	MILLIN	METERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
A	0.70	0.90	0.028	0.035	
В	1.40	1.80	0.055	0.071	
C	0.60	0.90	0.024	0.035	
D	0.15	0.30	0.006	0.012	
G	1.00	BSC	0.039	BSC	
Н	(J-C)	0.10		0.004	
J	0.10	0.25	0.004	0.010	
K	1.45	1.75	0.057	0.069	
L	0.10	0.20	0.004	0.008	
S	0.50	BSC	0.020	BSC	

STYLE 3:

PIN 1. ANODE 2. ANODE WWW.100Y.COM.TW

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