Preferred Device

Complementary Plastic Silicon Power Transistors

The MJE170/180 series is designed for low power audio amplifier and low current, high speed switching applications.

Features

• Collector-Emitter Sustaining Voltage -

V_{CEO(sus)} = 40 Vdc - MJE170, MJE180 = 60 Vdc - MJE171, MJE181 = 80 Vdc - MJE172, MJE182

• DC Current Gain -

 $h_{FE} = 30 \text{ (Min)} @ I_C = 0.5 \text{ Adc}$ = 12 (Min) @ $I_C = 1.5 \text{ Adc}$

• Current-Gain - Bandwidth Product -

 $f_T = 50 \text{ MHz (Min)} @ I_C = 100 \text{ mAdc}$

• Annular Construction for Low Leakages –

 $I_{CBO} = 100 \text{ nA (Max)}$ @ Rated V_{CB}

• Epoxy Meets UL 94 V-0 @ 0.125 in

• ESD Ratings: Machine Model, C

Human Body Model, 3B
• Pb–Free Packages are Available*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Base Voltage MJE170, MJE180 MJE171, MJE181 MJE172, MJE182	V _{CB}	60 80 100	Vdc
Collector–Emitter Voltage MJE170, MJE180 MJE171, MJE181 MJE172, MJE182	V _{CEO}	40 60 80	Vdc
Emitter-Base Voltage	V_{EB}	7.0	Vdc
Collector Current – Continuous – Peak	Ic	3.0 6.0	Adc
Base Current	I _B	1.0	Adc
Total Power Dissipation @ T _C = 25°C Derate above 25°C	P _D	1.5 0.012	W W/°C
Total Power Dissipation @ T _A = 25°C Derate above 25°C	P _D	12.5 0.1	W W/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-65 to +150	°C

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.



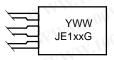
ON Semiconductor®

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3 AMPERES POWER TRANSISTORS COMPLEMENTARY SILICON 40 - 60 - 80 VOLTS 12.5 WATTS



MARKING DIAGRAM



Y = Year WW = Work Week JE1xx = Specific Device Code

x = 70, 71, 72, 80, 81, or 82 = Pb–Free Package

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 5 of this data sheet.

Preferred devices are recommended choices for future use and best overall value.

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^{*}For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

THERMAL CHARACTERISTICS

WWW.I	Characteristic	M. COn T	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ca	se OM.	M. To. COM.	θJC	10	°C/W
Thermal Resistance, Junction-to-An	nbient	MM.100 TOM	θЈА	83.4	°C/W

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Characteristic		Symbol	Min	Max	Unit
OFF CHARACTERISTICS	TW WW	V.1007.	N.T.W		
Collector–Emitter Sustaining Voltage (I _C = 10 mAdc, I _B = 0)	MJE170, MJE180 MJE171, MJE181 MJE172, MJE182	V _{CEO(sus)}	40 60 80	N I	Vdc
	MJE170, MJE180 MJE171, MJE181 MJE172, MJE182 MJE170, MJE180 MJE171, MJE181 MJE172, MJE182	I _{CBO}	7.0 <u>01</u> .C O 11	0.1 0.1 0.1 0.1	μAdc mAdc
Emitter Cutoff Current (V _{BE} = 7.0 Vdc, I _C = 0)	N.COM.TW	I _{EBO}	01.CO	0.1	μAdc
ON CHARACTERISTICS	ON.TH	W TOWN	001.	OM.T.	- 1
DC Current Gain $(I_C = 100 \text{ mAdc}, V_{CE} = 1.0 \text{ Vdc})$ $(I_C = 500 \text{ mAdc}, V_{CE} = 1.0 \text{ Vdc})$ $(I_C = 1.5 \text{ Adc}, V_{CE} = 1.0 \text{ Vdc})$	100Y.COM.TW	h _{FE}	50 30 12	250 - -	TW TW
Collector–Emitter Saturation Voltage ($I_C = 500 \text{ mAdc}$, $I_B = 50 \text{ mAdc}$) ($I_C = 1.5 \text{ Adc}$, $I_B = 150 \text{ mAdc}$) ($I_C = 3.0 \text{ Adc}$, $I_B = 600 \text{ mAdc}$)	W.100Y.COM.TW	V _{CE(sat)}	1. NETO,	0.3 0.9 1.7	Vdc
Base–Emitter Saturation Voltage ($I_C = 1.5$ Adc, $I_B = 150$ mAdc) ($I_C = 3.0$ Adc, $I_B = 600$ mAdc)	WW.100X.COM.T	V _{BE(sat)}	MATAN.	1.5 2.0	Vdc
Base-Emitter On Voltage (I _C = 500 mAdc, V _{CE} = 1.0 Vdc)	MM. 100X.COM	V _{BE(on)}	MA	1.2	Vdc
DYNAMIC CHARACTERISTICS	W.100 - CO	W.I	-111	W.100	A COL
Current–Gain – Bandwidth Product (Note 1) $(I_C = 100 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}, f_{test} = 10 \text{ MHz})$	M.M. 1007.CC	f _T	50	NV+.10	MHz
Output Capacitance (V _{CB} = 10 Vdc, I _E = 0, f = 0.1 MHz)	MJE171/MJE172 MJE181/MJE182	C _{ob}	-	60 40	pF .
1. $f_T = h_{fe} \cdot f_{test}$.	料 886-3-5753170				

^{1.} $f_T = |h_{fe}| \cdot f_{test}$.

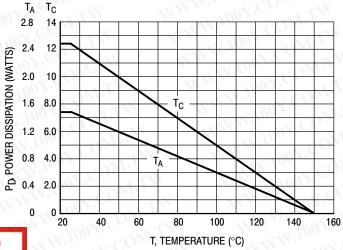
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Figure 1. Power Derating

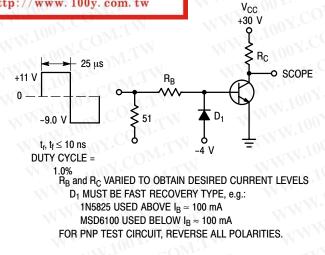


Figure 2. Switching Time Test Circuit

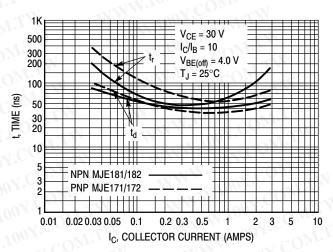


Figure 3. Turn-On Time

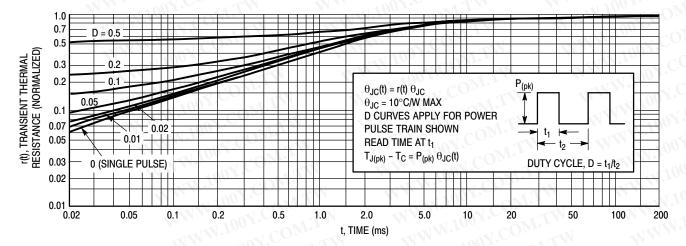


Figure 4. Thermal Response

ACTIVE-REGION SAFE OPERATING AREA

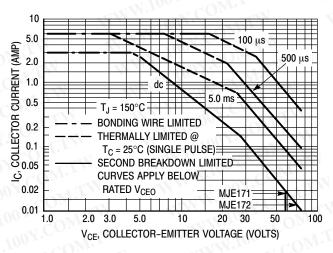


Figure 5. MJE171, MJE172

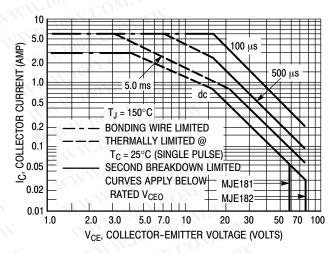


Figure 6. MJE181, MJE182

There are two limitations on the power handling ability of a transistor – average junction temperature and second breakdown. Safe operating area curves indicate $I_C - V_{CE}$ limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

The data of Figures 5 and 6 is based on $T_{J(pk)} = 150^{\circ}C$; T_{C} is variable depending on conditions. Second breakdown pulse limits are valid for duty cycles to 10% provided $T_{J(pk)} < 150^{\circ}C$. $T_{J(pk)}$ may be calculated from the data in Figure 4. At high case temperature, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown.

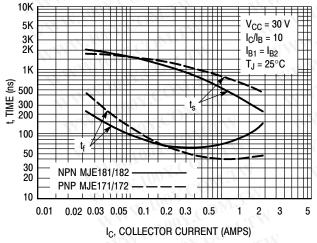


Figure 7. Turn-Off Time

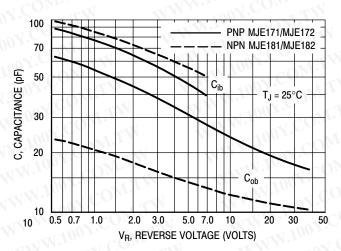


Figure 8. Capacitance

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MJE171G TO-225 MJE172 TO-225 MJE172G TO-225 MJE180 TO-225 MJE180G TO-225 MJE181 TO-225 MJE181G TO-225 MJE182 TO-225 MJE182G TO-225 MJE182G TO-225 MJE182 TO-225 MJE182G TO-225	MJE170G	TO-225 (Pb-Free)	COMITW		
MJE172 TO-225 MJE172G TO-225 (Pb-Free) MJE180 TO-225 MJE180G TO-225 (Pb-Free) MJE181 TO-225 (Pb-Free) MJE181G TO-225 (Pb-Free) MJE182 TO-225 (Pb-Free) MJE182G TO-225	MJE171	TO-225	COM		
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MJE181G TO-225 (Pb-Free) MJE182 TO-225 MJE182G TO-225	MJE180G	TO-225 (Pb-Free)	W.100X.COM.TW		
(Pb–Free) MJE182 TO–225 MJE182G TO–225	MJE181	TO-225	M. M. Too COM. T.		
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MJE182G (TO-225 (Pb-Free)	MJE182	TO-225			
	MJE182G	TO-225 (Pb-Free)	MAM. TOOX. COM		

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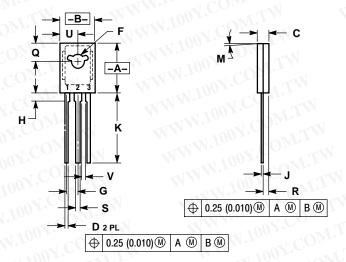
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PACKAGE DIMENSIONS

TO-225 CASE 77-09 ISSUE Z



NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI
 V14 FM 1092
- Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
- 077-01 THRU -08 OBSOLETE, NEW STANDARD 077-09.

V.	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.425	0.435	10.80	11.04
В	0.295	0.305	7.50	7.74
С	0.095	0.105	2.42	2.66
D	0.020	0.026	0.51	0.66
F	0.115	0.130	2.93	3.30
G	0.094	BSC	2.39	BSC
н	0.050	0.095	1.27	2.41
J	0.015	0.025	0.39	0.63
K	0.575	0.655	14.61	16.63
M	5°	TYP	5°	TYP
Q	0.148	0.158	3.76	4.01
R	0.045	0.065	1.15	1.65
S	0.025	0.035	0.64	0.88
U	0.145	0.155	3.69	3.93
V	0.040		1 02	VV-2

STYLE 1:

PIN 1. EMITTER 2. COLLECTOR

3. BASE

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