Data Sheet

High-Frequency Amplifier Transistor(11V, 50mA, 3.2GHz)

2SC5662 / 2SC4726 /2SC4083 / 2SC3838K

●Features

1) High transition frequency. (Typ. fT= 3.2GHz)
2) Small rbb'·Cc and high gain. (Typ. 4ps)
3) Small NF.

●Packaging specifications and hFE

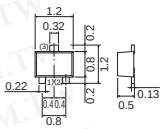
Type	2SC5662	2SC4726	2SC4083	2SC3838K
Package	VMT3	EMT3	UMT3	SMT3
hFE	NP	NP	NP	NP
Marking	AD	AD	1D	AD
Code	T2L	TL	T106	T146
Basic ordering unit (pieces)	8000	3000	3000	3000

●Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit
Collector-base voltage		VCBO	20	V
Collector-emitter voltage		VCEO	11	V
Emitter-base voltage		VEBO	3	V
Collector current		IC	50	mA
Collector power dissipation	2SC5662, 2SC4726	PC	0.15	W
	2SC4083, 2SC3838K		0.2	
Junction temperature		Tj	150	°C
Storage temperature		Tstg	−55 to +150	°C

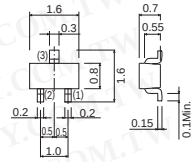
●Dimensions (Unit : mm)

2SC5662



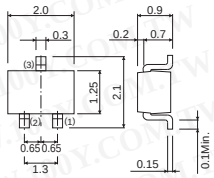
ROHM : VMT3
(1) Base
(2) Emitter
(3) Collector

2SC4726



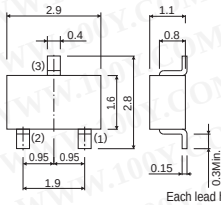
ROHM : EMT3
EIAJ : SC-75A
(1) Emitter
(2) Base
(3) Collector

2SC4083



ROHM : UMT3
EIAJ : SC-70
(1) Emitter
(2) Base
(3) Collector
Each lead has same dimensions

2SC3838K



ROHM : SMT3
EIAJ : SC-59
(1) Emitter
(2) Base
(3) Collector
Each lead has same dimensions

●Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Min.	Typ.	Max.	Unit	Conditions
Collector-base breakdown voltage		BVCBO	20	–	–	V	IC = 10μA
Collector-emitter breakdown voltage		BVCEO	11	–	–	V	IC = 1mA
Emitter-base breakdown voltage		BVEBO	3	–	–	V	IE = 10μA
Collector cutoff current		ICBO	–	–	0.5	μA	VCB = 10V
Emitter cutoff current		IEBO	–	–	0.5	μA	VEB = 2V
Collector-emitter saturation voltage		VCE(sat)	–	–	0.5	V	IC/IB = 10mA/5mA
DC current transfer ratio	2SC5662, 2SC4726, 2SC4083, 2SC3838K	hFE	56	–	180	–	VCE/IC = 10V/5mA
Transition frequency		fT	1.4	3.2	–	GHz	VCE = 10V , IE = −10mA , f = 500MHz
Output capacitance		Cob	–	0.8	1.5	pF	VCB = 10V , IE = 0A , f = 1MHz
Collector-base time constant		rbb'·Cc	–	4	12	ps	VCB = 10V , IC = 10mA , f = 31.8MHz
Noise factor		NF	–	3.5	–	dB	VCE = 6V , IC = 2mA , f = 500MHz , Rg = 50Ω

This product might cause chip aging and breakdown under the large electrified environment.
Please consider to design ESD protection circuit.

●Electric characteristics curves

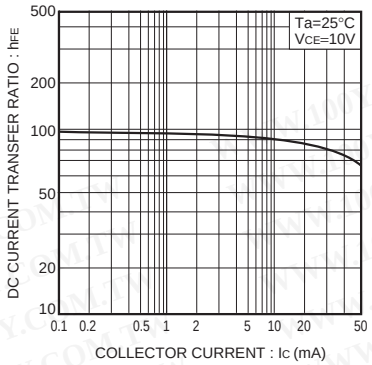


Fig.1 DC current gain vs. collector current

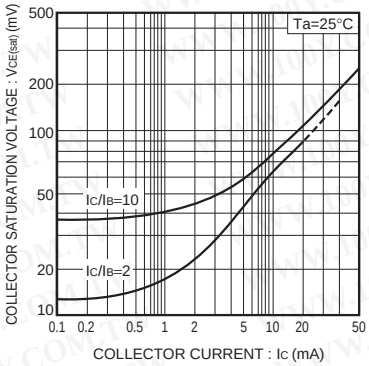


Fig.2 Collector-emitter saturation voltage vs. collector current

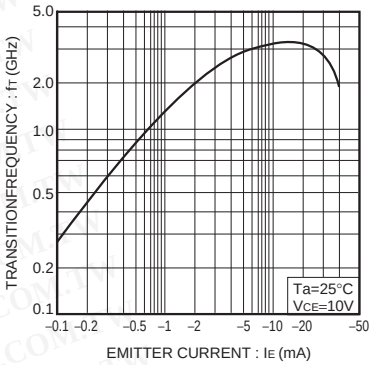


Fig.3 Gain bandwidth product vs. emitter current

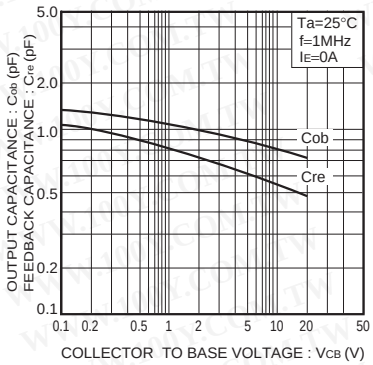


Fig.4 Capacitance vs. reverse bias voltage

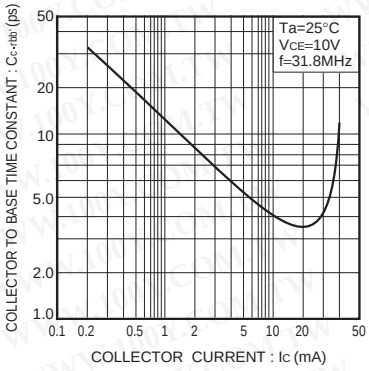


Fig.5 Collector to base time constance vs. collector current

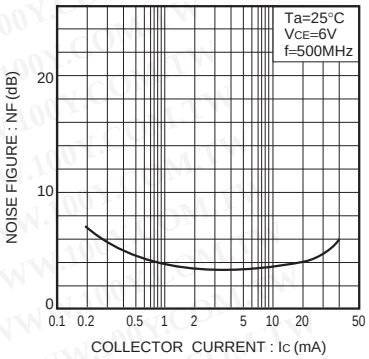


Fig.6 Noisfactor vs. collector current characteristics

Notes

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