

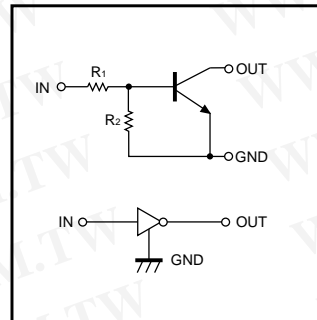
## Digital transistors (built-in resistors)

**DTC144EM / DTC144EE / DTC144EUA /  
DTC144EKA / DTC144ESA**

## ●Features

- 1) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 2) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- 3) Only the on/off conditions need to be set for operation, making device design easy.

- Equivalent circuit



## ● Structure

NPN digital transistor (Built-in resistors)

●External dimensions (Unit : mm)

DTC144EM

Top view dimensions: 1.2, 0.2, 0.8, 0.2, 0.32, 0.4, 0.4, 0.8, 0.22, 0.15Max.

Side view dimensions: 0.5, 0.13, 0.5, 0.15Max.

ROHM : VMT3

Abbreviated symbol : 26

(1) IN  
(2) GND  
(3) OUT

DTC144EE

Top view dimensions: 1.6±0.2, 1.0±0.1, 0.5±0.5, 0.2'±0.1, 0.2'±0.1, 0.8±0.2, 1.6±0.2, 0.3'±0.1, 0.3'±0.1.

Side view dimensions: 0.7±0.1, 0.55±0.1, 0 to 0.1, 0.15±0.05, 0.1Min.

ROHM : EMT3

Abbreviated symbol : 26

(1) GND  
(2) IN  
(3) OUT

DTC144EUA

Top view dimensions: 2.0±0.2, 1.3±0.1, 0.65, 0.65, 2.1±0.1, 2.5±0.1, 0.3'±0.1, 0.15±0.05.

Side view dimensions: 0.9±0.1, 0.2, 0.7±0.1, 0 to 0.1, 0.1Min.

ROHM : UMT3  
EIAJ : SC-70

All terminals have same dimensions

Abbreviated symbol : 26

(1) GND  
(2) IN  
(3) OUT

DTC144EKA

Top view dimensions: 2.9±0.2, 1.9±0.2, 0.95, 0.95, 1.6±0.2, 2.8±0.2, 0.4'±0.1, 0.4'±0.1.

Side view dimensions: 1.1'±0.2, 0.8±0.1, 0 to 0.1, 0.15±0.1, 0.06, 0.3Min.

ROHM : SMT3  
EIAJ : SC-59

All terminals have same dimensions

Abbreviated symbol : 26

(1) GND  
(2) IN  
(3) OUT

DTC144ESA

Top view dimensions: 4±0.2, 3±0.2, 3Min., 0.45'±0.15, 2.5±0.4, 0.5, 0.45'±0.15.

Side view dimensions: 2±0.2, 0.5, 0.45'±0.15.

ROHM : SPT  
EIAJ : SC-72

(1) GND  
(2) OUT  
(3) IN

# DTC144EM / DTC144EE / DTC144EUA DTC144EKA / DTC144ESA

## Transistors

### ●Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits(DTC144E□)					Unit
		M	E	UA	KA	SA	
Supply voltage	V <sub>CC</sub>	50					V
Input voltage	V <sub>IN</sub>	-10 to +40					V
Output current	I <sub>O</sub>	100					mA
	I <sub>C(Max.)</sub>	100					
Power dissipation	P <sub>d</sub>	150		200		300	mW
Junction temperature	T <sub>J</sub>	150					°C
Storage temperature	T <sub>stg</sub>	-55 to +150					°C

### ●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Input voltage	V <sub>I(off)</sub>	—	—	0.5	V	V <sub>CC</sub> =5V, I <sub>O</sub> =100μA
	V <sub>I(on)</sub>	3	—	—		V <sub>O</sub> =0.3V, I <sub>O</sub> =2mA
Output voltage	V <sub>O(on)</sub>	—	0.1	0.3	V	I <sub>O</sub> /I <sub>I</sub> =10mA/0.5mA
Input current	I <sub>I</sub>	—	—	0.18	mA	V <sub>I</sub> =5V
Output current	I <sub>O(off)</sub>	—	—	0.5	μA	V <sub>CC</sub> =50V, V <sub>I</sub> =0V
DC current gain	G <sub>I</sub>	68	—	—	—	V <sub>O</sub> =5V, I <sub>O</sub> =5mA
Input resistance	R <sub>I</sub>	32.9	47	61.1	kΩ	—
Resistance ratio	R <sub>2</sub> /R <sub>1</sub>	0.8	1	1.2	—	—
Transition frequency	f <sub>T</sub>	—	250	—	MHz	V <sub>CE</sub> =10V, I <sub>E</sub> =-5mA, f=100MHz *

\*Transition frequency of the device

### ●Packaging specifications

Type	Package	VMT3	EMT3	UMT3	SMT3	SPT
	Packaging type	Taping	Taping	Taping	Taping	Taping
	Code	T2L	TL	T106	T146	TP
	Basic ordering unit (pieces)	8000	3000	3000	3000	5000
DTC144EM		○	—	—	—	—
DTC144EE		—	○	—	—	—
DTC144EUA		—	—	○	—	—
DTC144EKA		—	—	—	○	—
DTC144ESA		—	—	—	—	○

# DTC144EM / DTC144EE / DTC144EUA DTC144EKA / DTC144ESA

## Transistors

### ●Electrical characteristic curves

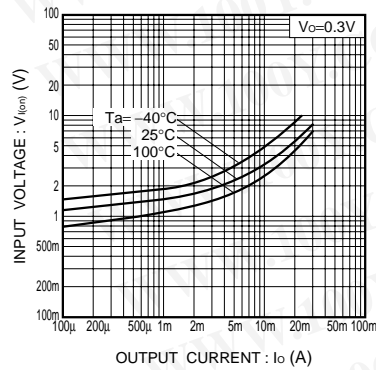


Fig.1 Input voltage vs. output current  
(ON characteristics)

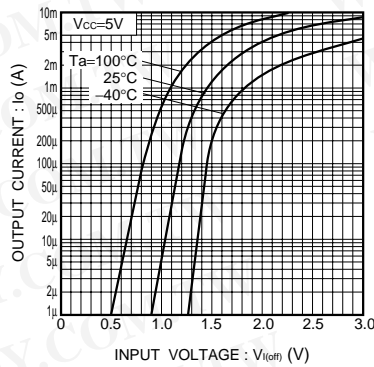


Fig.2 Output current vs. input voltage  
(OFF characteristics)

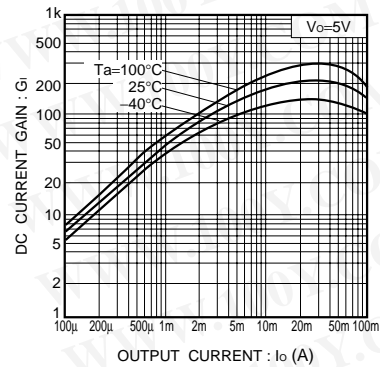


Fig.3 DC current gain vs. output  
current

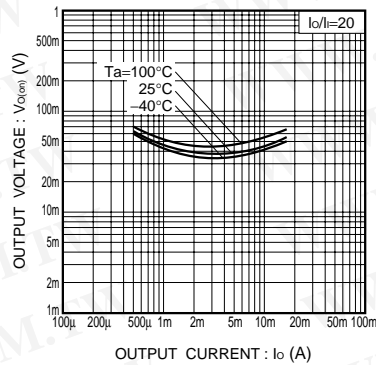


Fig.4 Output voltage vs. output  
current

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勝特力电子(上海) 86-21-34970699  
勝特力电子(深圳) 86-755-83298787  
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## Appendix

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