

500mA / 50V Digital transistors

(with built-in resistors)

DTD113ZK / DTD113ZU

Applications

Inverter, Interface, Driver

Features

- Built-in bias resistors enable theconfiguration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 2) The bias resistors consist of thin-film resistors negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- Only the on/off conditions need to be set for operation, making the device design easy.

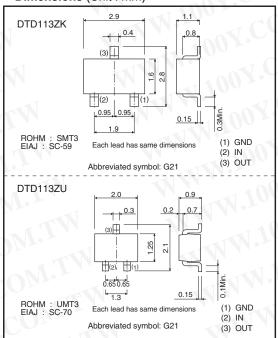
Structure

NPN epitaxial planar silicon transistor (Resistor built-in type)

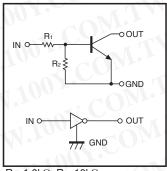
Packaging specifications

	Package	SMT3	UMT3
	Packaging type	Taping	Taping
	Code	T146	T106
Part No.	Basic ordering unit (pieces)	3000	3000
DTD113ZK		0	H I
DTD113ZU		-	0

Dimensions (Unit : mm)



Equivalent circuit



 $R_1=1.0k\Omega$, $R_2=10k\Omega$

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Absolute maximum ratings (Ta=25°C)

Dovomotov	Cumbal	Limits	Unit	
Parameter	Symbol	DTD113ZU DTD113ZK		
Supply voltage	Vcc	50	V	
Input voltage	V _{IN}	−5 to +10	V	
Output current	lc	500	mA	
Power dissipation	Pp	200	mW	
Junction temperature	Tj	150	°C	
Storage temperature	Tstg	-55 to +150	°C	

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dunction temperature		U	100		A Pro	<u> </u>	
Storage temperature	Tstg		-55 to +	150	0	C	
Electrical characte		a=25°(7.	V -		
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Innut voltage	VI(off)	1	-	0.3	V	Vcc=5V, Io=100μA	- XIVI VI
Input voltage	VI(on)	1.5	4	17.	\ \ \	Vo=0.3V, Io=20mA	
Output voltage	Vo(on)		0.1	0.3	V	lo/li=50mA/2.5mA	
Input current	lı	V _	-	7.2	mA	V _I =5V	
Output current	IO(off)	-71	(1-)	0.5	μА	Vcc=50V, Vi=0V	
DC current gain	Gı	82	-	50	- -	Vo=5V, Io=50mA	
Input resistance	R ₁	0.7	1	1.3	kΩ	I	
Resistance ratio	R2/R1	8	10	12	1		
Transition frequency	f⊤ *		200		MHz	Vc==10V, I==-50mA, f=100MH	

^{*} Characteristics of built-in transistor

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Electrical characteristic curves

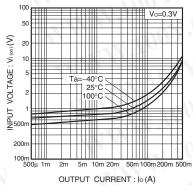


Fig.1 Input voltage vs. output current (ON characteristics)

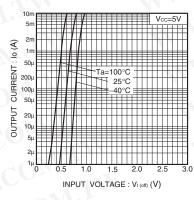


Fig.2 Output current vs. input voltage (OFF characteristics)

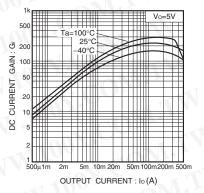


Fig. 3 DC current gain vs. output current

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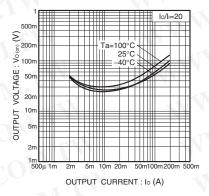


Fig.4 Output voltage vs. output current

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