

Transistors

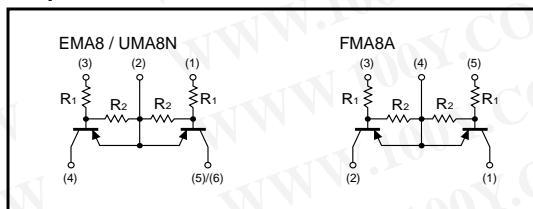
Emitter common (dual digital transistors)

EMA8 / UMA8N / FMA8A

●Features

- 1) Two DTA114Y chips in a EMT or UMT or SMT package.

●Equivalent circuit



●Package, marking, and packaging specifications

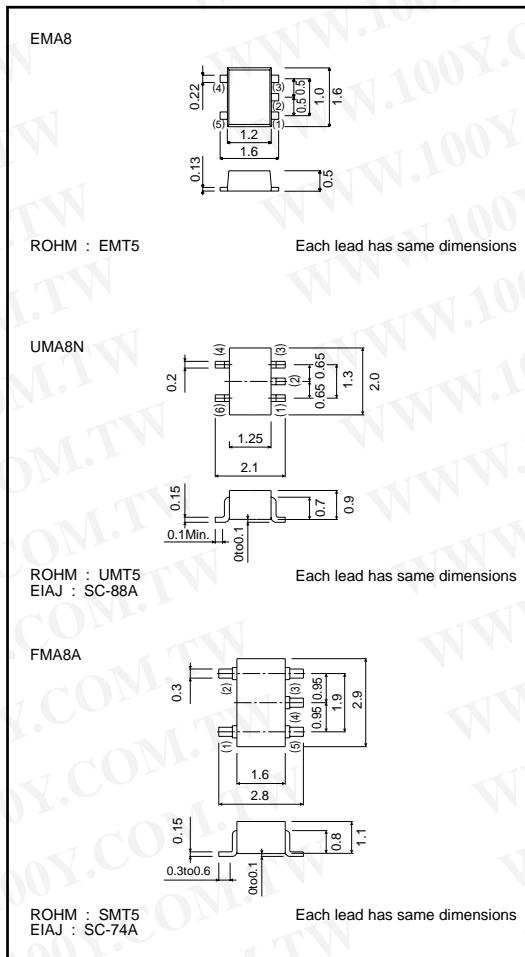
Type	EMA8	UMA8N	FMA8A
Package	EMT5	UMT5	SMT5
Marking	A8	A8	A8
Code	T2R	TR	T148
Basic ordering unit (pieces)	8000	3000	3000

●Absolute maximum ratings ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Limits	Unit
Supply voltage	V_{CC}	-50	V
Input voltage	V_{IN}	-40 6	V
Output current	I_O	-100	mA
Power dissipation	P_d	300(TOTAL)	mW *
Storage temperature	T_{STG}	-50~+150	°C

*200mW per element must not be exceeded.

●External dimensions (Units : mm)



●Electrical characteristics ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Input voltage	V_I (off)	—	—	-0.3	V	$V_{CC}=-5V, I_O=-100\mu\text{A}$
	V_I (on)	-1.4	—	—		$V_O=-0.3V, I_O=1\text{mA}$
Output voltage	V_O (on)	—	-0.1	-0.3	V	$I_O=-10\text{mA}, I_L=0.5\text{mA}$
Input current	I_I	—	—	-0.88	mA	$V_I=-5V$
Output current	I_O (off)	—	—	-0.5	μA	$V_{CC}=-50V, V_I=0V$
DC current gain	G_I	68	—	—		$I_O=5\text{mA}, V_O=5V$
Input resistance	R_I	—	10	—	kΩ	—
Resistance ratio	R_2/R_1	3.7	4.7	5.7	—	—