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EMG5 / UMG5N / FMG5A

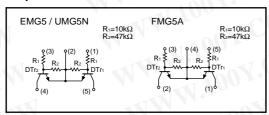
Transistors

Emitter common (dual digital transistors)

EMG5 / UMG5N / FMG5A

1) Two DTC114Y chips in a EMT or UMT or SMT package.

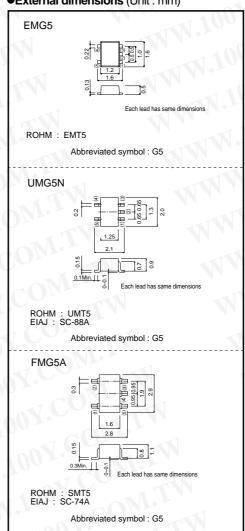
Equivalent circuit



◆Absolute maximum ratings (Ta = 25°C)

Parameter		Symbol	Limits	Unit	
Supply voltage		Vcc	50	V	
Input voltage		VIN	40	. V .	
		VIN	-6	1 10	
Output current		lo 70		^	
		Ic (Max.)	100	mA	
Power dissipation	EMG5, UMG5N	D4	150 (TOTAL)	mW *1	
	FMG5A	Pd	300 (TOTAL)		
Junction temperature		Tj	150	°C	
Storage temperature		Tstg	-55 to +150	°C	

●External dimensions (Unit : mm)



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EMG5 / UMG5N / FMG5A

Transistors

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Innuit valtana	VI (off)	II.	-	0.3	V	Vcc=5V, Io=100μA	
Input voltage	VI (on)	1.4	1	-] V	Vo=0.3V, Io=1mA	
Output voltage	Vo (on)	$\Omega_{D_{2}}$	0.1	0.3	V	lo=5mA, l⊫0.25mA	
Input current	li	- 1	(-)	0.88	mA	V⊫5V	
Output current	lo (off)		_	0.5	μА	Vcc=50V, Vi=0V	
DC current gain	Gı	68	A.	7.7	_	Vo=5V, Io=5mA	
Transition frequency	fτ	G	250	-	MHz	VcE=10V, IE= -5mA, f=100MHz	
Input resistance	R ₁	7	10	13	kΩ	-	
Resistance ratio	R ₂ /R ₁	3.7	4.7	5.7	c X I		

^{*} Transition frequency of the device

Packaging specifications

	Package		Taping	Y.C.
	Code	T2R	TR	T148
Туре	Basic ordering unit (pieces)	8000	3000	3000
EMG5			-	\
UMG5N		-	0	00
FMG5A			1	0

•Electrical characteristics curves

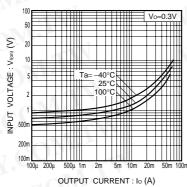


Fig.1 Input voltage vs. output current (ON characteristics)

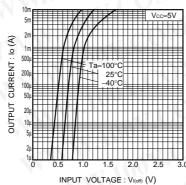


Fig.2 Output current vs. input voltage (OFF characteristics)

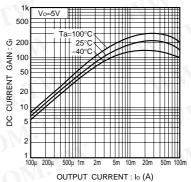


Fig.3 DC current gain vs. output current

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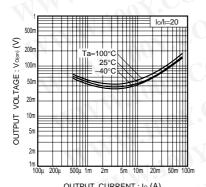


Fig.4 Output voltage vs. output current

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Appendix

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