# **Transistors**

# General purpose transistors (dual transistors)

# **EMX18 / UMX18N**

#### Features

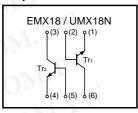
- 1) Two 2SC5585 chips in a EMT or UMT package.
- Mounting possible with EMT3 or UMT3 automatic mounting machines.
- Transistor elements are independent, eliminating interference.
- 4) Mounting cost and area can be cut in half.

# **●Structure**

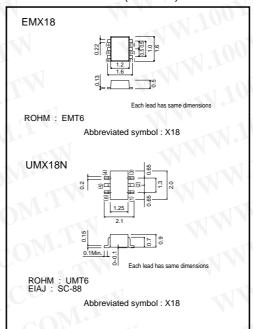
Epitaxial planar type NPN silicon transistor

The following characteristics apply to both Tr1 and Tr2

# Equivalent circuit



# ●External dimensions (Unit : mm)



# ● Absolute maximum ratings (Ta=25°C)

Symbol	Limits	Unit	
Vсво	15		
Vceo	12	V	
V <sub>ЕВО</sub>	6	V	
lc	500	mA	
Іср	1.0	Α	
Pd	150 (TOTAL)	mW *1	
Tj	150	°C	
Tstg	-55 to +150	°C	
	VCBO VCEO VEBO IC ICP Pd Tj	VCBO         15           VCEO         12           VEBO         6           Ic         500           IcP         1.0           Pd         150 (TOTAL)           Tj         150	

<sup>\*1 120</sup>mW per element must not be exceeded.

# **Transistors**

# ●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Collector-base breakdown voltage	ВУсво	15	_	-	V	Ic=10μA	
Collector-emitter breakdown voltage	BVceo	12	_	ci <del>c</del>	V	Ic=1mA	
Emitter-base breakdown voltage	ВУЕВО	6	-	-	V	Iε=10μA	
Collector cutoff current	Ісво	) F	-	0.1	μΑ	VcB=15V	
Emitter cutoff current	ІЕВО	-	A.	0.1	μА	V <sub>EB</sub> =6V	
Collector-emitter saturation voltage	VCE (sat)		90	250	mV	Ic/I <sub>B</sub> =200mA/10mA	
DC current transfer ratio	hfe	270	-	680	_	Vce=2V, Ic=10mA	
Transition frequency	fr	Ġ.	320	-	MHz	Vce=2V, Ie=-10mA, f=100MHz	
Output capacitance	Cob	_	7.5	A.	PF	Vcb=10V, IE=0A, f=1MHz	

#### Packaging specifications

	Package	Та	Taping		
	Code	T2R	TN		
Туре	Basic ordering unit (pieces)	8000	3000		
EMX18		0	1 (H)		
UMX18N			0		

#### Electrical characteristic curves

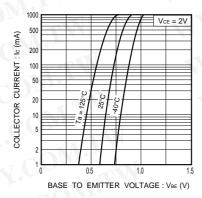


Fig.1 Grounded emitter propagation characteristics

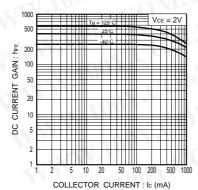


Fig.2 DC current gain vs. collector current

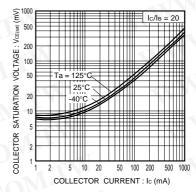


Fig.3 Collector-emitter saturation voltage vs. collector current (I)

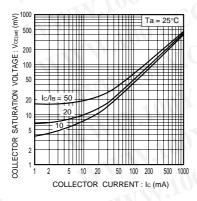


Fig.4 Collector-emitter saturation voltage vs. collector current ( II )

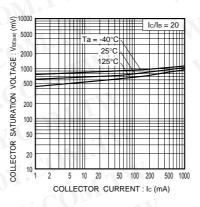


Fig.5 Base-emitter saturation voltage vs. collector current

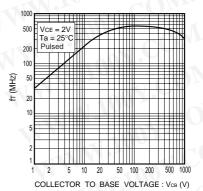


Fig.6 Collector output capacitance Emitter input capacitance vs. base voltage

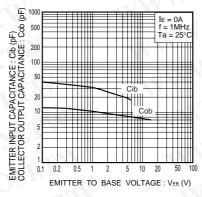


Fig.7 Collector output capacitance vs collector-base voltage Emitter input capacitance vs emitter-base voltage

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