

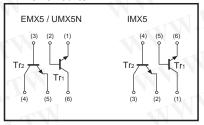
High transition frequency (dual transistors)

EMX5 / UMX5N / IMX5

●Features

- 1) Two 2SC3838K chips in a EMT or UMT or SMT package.
- 2) High transition frequency. (ft=3.2GHz)
- 3) Low output capacitance. (Cob=0.9pF)

Inner circuits



●Absolute maximum ratings (Ta=25°C)

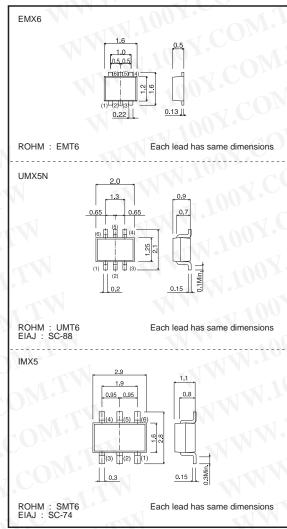
Parameter Collector-base voltage		Symbol	Limits	Unit V	
		Vсво	20		
Collector-emitter voltage		VCEO	11	V	
Emitter-base voltage		VEBO	3	V	
Collector current		Ic	50	mA	
Collector power dissipation	EMX5 / UMX5N	Pc	150(TOTAL)	mW *1	
	IMX5	FC	300(TOTAL)	*2	
Junction temperature		Tj	150	°C	
Storage temperature		Tstg	-55 to +150	°C	

^{*1 120}mW per element must not be exceeded

Package, marking, and packaging specifications

Туре	EMX5	UMX5N	IMX5		
Package	EMT6	UMT6	SMT6		
Marking	X5	X5	X5		
Code	T2R	TR	T108		
Basic ordering unit (pieces)	8000	3000	3000		

●Dimensions (Unit: mm)



This product might cause chip aging and breakdown under the large electrified environment Please consider to design ESD protection circuit.

勝 特 力 材 料 886-3-5753170 胜特力电子(上海) 86-21-34970699 胜特力电子(深圳) 86-755-83298787 Http://www.100y.com.tw

●Electrical characteristics (Ta=25°C)

Symbol	Min.	Тур.	Max.	Unit	Conditions
ВУсво	20	-	-	V	Ic=10μA
BVceo	11	1 -	-	V	Ic=1mA
ВУЕВО	3	11.	-	V	Iε=10μA
Ісво	17.5	=-	0.5	μА	VcB=10V
ІЕВО			0.5	μА	V _{EB} =2V
hfe	56		120	-	Vce/Ic=10V/5mA
VCE(sat)	MA	-	0.5	V	Ic/I _B =10mA/5mA
fτ	1.4	3.2	-	GHz	Vce/Ie=10V/-10mA, f=500MHz *
Cob		0.9	1.55	pF	Vcb/f=10V/1MHz, IE=0A
	BVCBO BVCEO BVEBO ICBO IEBO hFE VCE(sat)	BVCBO 20 BVCEO 11 BVEBO 3 ICBO - IEBO - hFE 56 VCE(sat) - fr 1.4	BVCBO 20 - BVCEO 11 - BVEBO 3 - ICBO IEBO hFE 56 - VCE(sat) fr 1.4 3.2	BVCBO 20 BVCBO 11	BVcbo 20 - - V BVcbo 11 - - V BVebo 3 - - V Icbo - - 0.5 μA Iebo - - 0.5 μA hFE 56 - 120 - VcE(sat) - - 0.5 V fr 1.4 3.2 - GHz

^{*}Transition frequency of the device.

Electrical characteristics curves

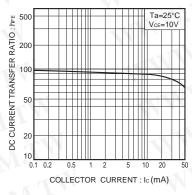


Fig.1 DC current gain vs. collector current

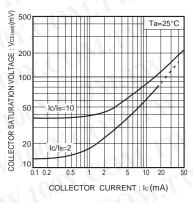


Fig.2 Collector-emitter saturation voltage vs. collector current

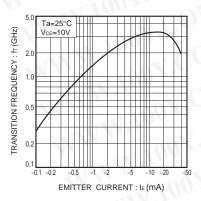


Fig.3 Gain bandwidth product vs. emitter current

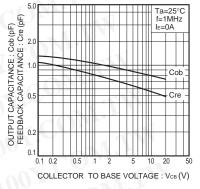


Fig.4 Capacitance vs. reverse bias voltage

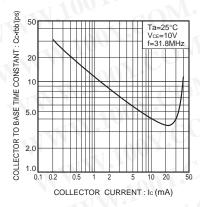


Fig.5 Collector to base time constant vs. collector current characteristics

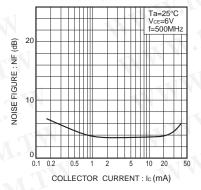


Fig.6 Noise factor vs. collector current characteristics

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