General purpose (dual digital transistors) UMH11N/IMH11A

Features

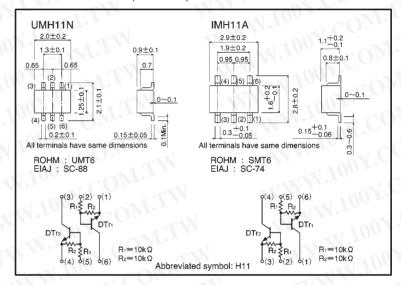
- Two DTC114E chips in a UMT or SMT package.
- Mounting possible with UMT3 or SMT3 automatic mounting machines.
- 3) Transistor elements are independent, eliminating interference.
- 4) Mounting cost and area can be cut in half.

Structure

Epitaxial planar type NPN silicon transistor (Built-in resistor type)

The following characteristics apply to both DTr_1 and DTr_2 .

External dimensions (Units: mm)



Absolute maximum ratings (Ta = 25°C)

Parameter		Symbol	Limits	Unit	
Supply voltage		Vcc	50	VCO	
Input voltage		Vin	40	00 7.	
		VIN	-10	, of C	
Output current		lo	50	mA	
Collector current		IC(Max.)	100	mA	
Power dissipation	UMH11N	Pd	150(TOTAL)	*1	
	IMH11A		300 (TOTAL)	mvv *2	
Junction temperature		Tj	150	°C (
Storage temperature		Tstg	− 55∼ + 150	C C	
	3/1/	333	3.3.3		

*1 120mW per element must not be exceeded. *2 200mW per element must not be exceeded. 勝 特 力 材 料 886-3-5753170 胜特力电子(上海) 86-21-34970699 胜特力电子(深圳) 86-755-83298787 Http://www.100y.com.tw

(96-490-C114E)



• Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
	VI (off)	_	nt 1	0.5	.,	Vcc=5V, lo=100 μA	
Input voltage	VI (on)	3	127	4	V	Vo=0.3V, lo=10mA	
Output voltage	VO(on)	-	0.1	0.3	V	lo/li=10mA/0.5mA	
Input current	Jol		_	0.88	mA	V _I =5V	
Output current	IO(off)	₹7 (0.5	μΑ	Vcc=50V, Vi=0V	
DC current gain	Gı	30		V=1		Vo=5V, Io=5mA	
Transition frequency	fт	OTT V	250		MHz	Vce=10mA, Ie=-5mA, f=100MHz *	
Input resistance	R ₁	7	10	13	kΩ	TINW. 10	
Resistance ratio	R2/R1	0.8	-1	1.2		NT 100	

^{*} Transition frequency of the device

Packaging specifications

1.7	Packaging type	Taping		
	Code	TN	T110	
Part No.	Basic ordering unit (pieces)	3000	3000	
UMH11N	XXI .		N	
IMH11A			0.00	

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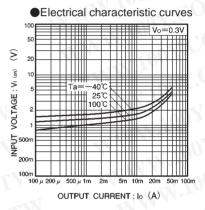


Fig.1 Input voltage vs. output current (ON characteristics)

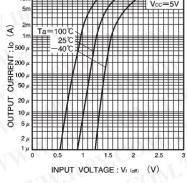


Fig.2 Output current vs. input voltage (OFF characteristics)

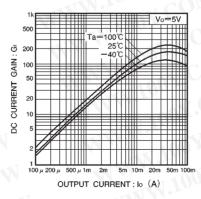


Fig.3 DC current gain vs. output current

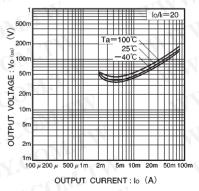


Fig.4 Output voltage vs. output current

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Appendix

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