

Transistor

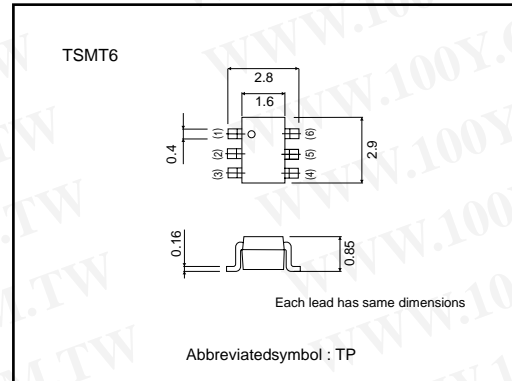
# DC-DC Converter (-30V, -2.5A)

## RSQ025P03

### ●Features

- 1) Low On-resistance.(120mΩ at 4.5V)
- 2) High Power Package.(P<sub>D</sub>=1.25W)
- 3) High speed switching.
- 4) Low voltage drive.(4V)

### ●External dimensions (Units : mm)



### ●Applications

DC-DC converter

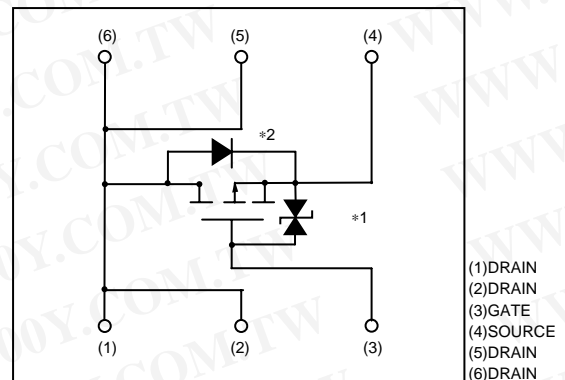
### ●Structure

Silicon P-channel  
MOSFET

### ●Packaging specifications

Type	Package	Taping
	Code	TR
	Basic ordering unit (pieces)	3000
RSQ025P03		○

### ●Equivalent circuit



- \*1 ESD PROTECTION DIODE  
 \*2 BODY DIODE

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●Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Drain-source voltage	V <sub>DSS</sub>	-30	V
Gate-source voltage	V <sub>GSS</sub>	±20	V
Drain current	Continuous	I <sub>D</sub>	±2.5
	Pulsed	I <sub>DP</sub>	±10
Source current (Body diode)	Continuous	I <sub>S</sub>	-1
	Pulsed	I <sub>SP</sub>	-4
Total power dissipation	P <sub>D</sub>	1.25	W <sup>*2</sup>
Channel temperature	T <sub>ch</sub>	150	°C
Range of Storage temperature	T <sub>stg</sub>	-55~+150	°C

\*1 Pw≤10μs, Duty cycle≤1%

\*2 Mounted on a ceramic board

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Gate-source leakage	I <sub>GSS</sub>	-	-	±10	μA	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	-30	-	-	V	I <sub>D</sub> =-1mA, V <sub>GS</sub> =0V
Zero gate voltage drain current	I <sub>DSS</sub>	-	-	-1	μA	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0V
Gate threshold voltage	V <sub>GS(th)</sub>	-1.0	-	-2.5	V	V <sub>DS</sub> =-10V, I <sub>D</sub> =-1mA
Static drain-source on-state resistance	R <sub>DS(on)</sub> <sup>*</sup>	-	80	110	mΩ	I <sub>D</sub> =-2.5A, V <sub>GS</sub> =-10V
		-	120	165	mΩ	I <sub>D</sub> =-1.25A, V <sub>GS</sub> =-4.5V
		-	145	200	mΩ	I <sub>D</sub> =-1.25A, V <sub>GS</sub> =-4.0V
Forward transfer admittance	Y <sub>fs</sub>   <sup>*</sup>	1.2	-	-	S	V <sub>DS</sub> =-10V, I <sub>D</sub> =-1.25A
Input capacitance	C <sub>iss</sub>	-	320	-	pF	V <sub>DS</sub> =-10V, V <sub>GS</sub> =0V f=1MHz
Output capacitance	C <sub>oss</sub>	-	85	-	pF	
Reverse transfer capacitance	C <sub>rss</sub>	-	60	-	pF	
Turn-on delay time	t <sub>d(on)</sub> <sup>*</sup>	-	8	-	ns	I <sub>D</sub> =-1.25A V <sub>DD</sub> =-15V V <sub>GS</sub> =-4.5V R <sub>L</sub> =12Ω R <sub>GS</sub> =10Ω
Rise time	t <sub>r</sub> <sup>*</sup>	-	11	-	ns	
Turn-off delay time	t <sub>d(off)</sub> <sup>*</sup>	-	33	-	ns	
Fall time	t <sub>f</sub> <sup>*</sup>	-	7	-	ns	
Total gate charge	Q <sub>g</sub>	-	4.4	-	nC	V <sub>DD</sub> =-15V V <sub>GS</sub> =-5V I <sub>D</sub> =-2.5A
Gate-source charge	Q <sub>gs</sub>	-	1.0	-	nC	
Gate-drain charge	Q <sub>gd</sub>	-	1.4	-	nC	

\*PULSED

Body diode characteristics (source-drain characteristics)

Forward voltage	V <sub>SD</sub>	-	-	-1.2	V	I <sub>S</sub> =-0.9A, V <sub>GS</sub> =0V
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●Electrical characteristic curves

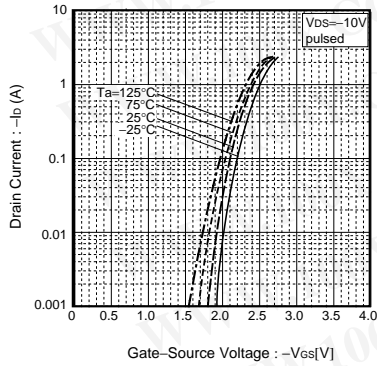


Fig.1 Typical Transfer Characteristics

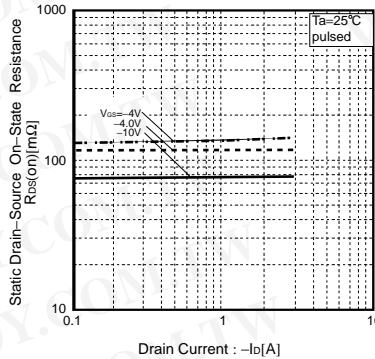


Fig.2 Static Drain-Source On-State Resistance vs. Drain Current

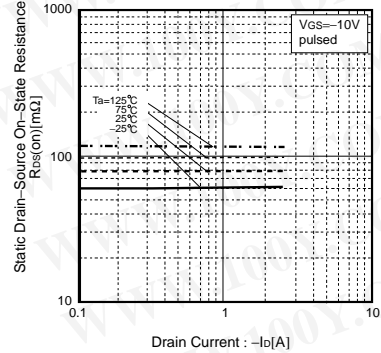


Fig.3 Static Drain-Source On-State Resistance vs. Drain Current

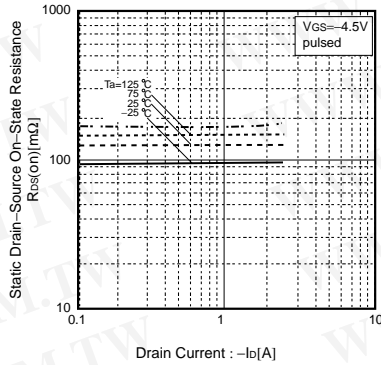


Fig.4 Static Drain-Source On-State Resistance vs. Drain-Current

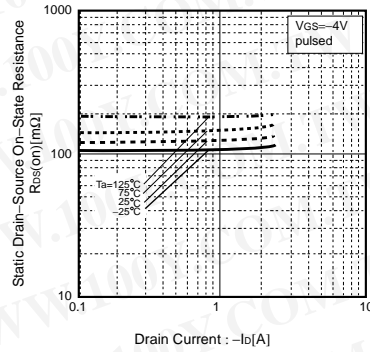


Fig.5 Static Drain-Source On-State Resistance vs. Drain-Current

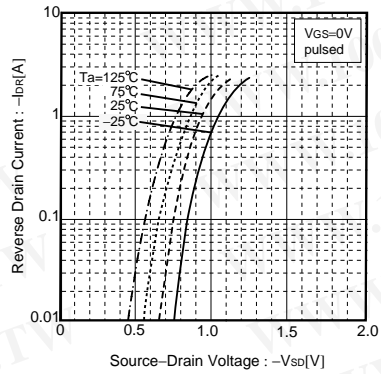


Fig.6 Reverse Drain Current Source-Drain Voltage

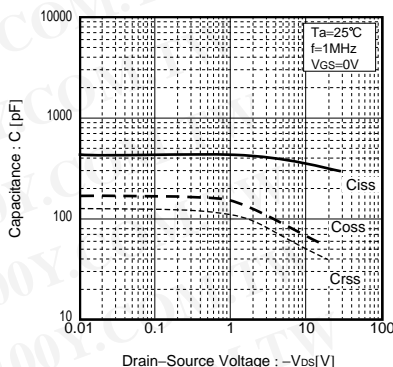


Fig.7 Typical Capacitance vs. Drain-Source Voltage

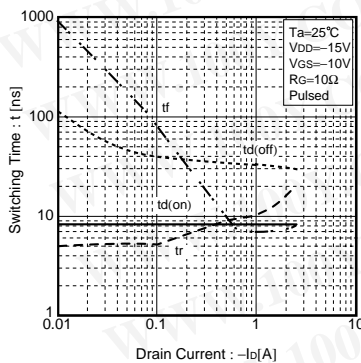


Fig.8 Switching Characteristics

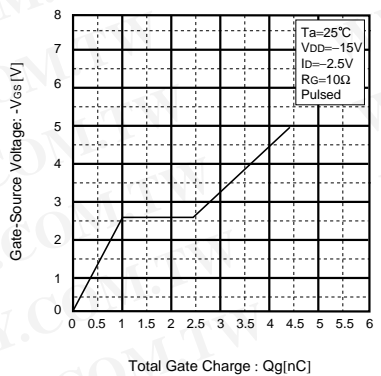


Fig.9 Dynamic Input Characteristics

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● Measurement circuits

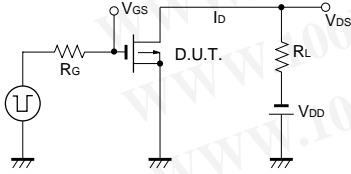


Fig.10 Switching Time Measurement Circuit

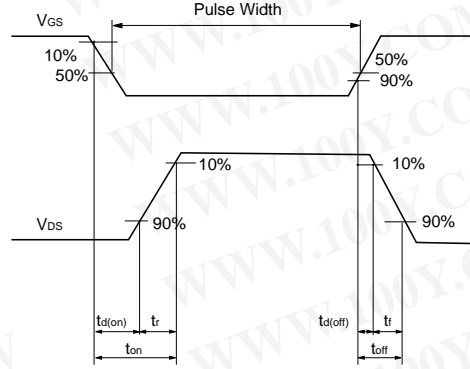


Fig.11 Switching Waveforms

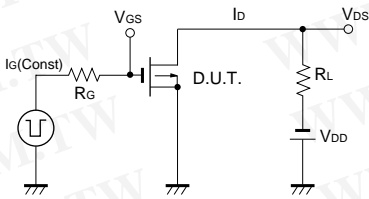


Fig.12 Gate Charge Measurement Circuit

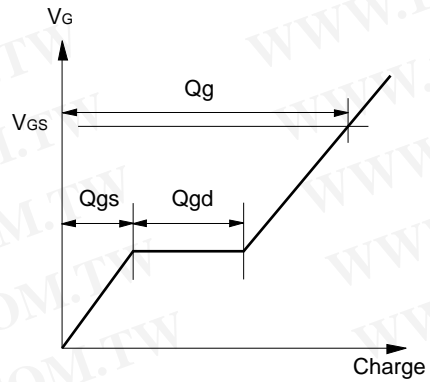


Fig.13 Gate Charge Waveforms

## Appendix

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