

SR520 thru SR5100

SCHOTTKY BARRIER RECTIFIERS	REVERSE VOLTAGE - 20 to 100 Volts FORWARD CURRENT - 5.0 Amperes
FEATURES <ul style="list-style-type: none"> ● Metal-Semiconductor junction with gard ring ● Epitaxial construction ● Low forward voltage drop ● High current capability ● The plastic material carries UL recognition 94V-0 ● For use in low vlotage, high frequency inverters, free wheeling, and polarity protection applications MECHANICAL DATA <ul style="list-style-type: none"> ● Case: JEDEC DO-27 molded plastic ● Polarity: Color band denotes cathode ● Weight: 0.04ounces , 1.1grams ● Mounting position: Any 	<p>Dimensions in inches and (millimeters)</p>

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Rating at 25°C ambient temperature unless otherwise specified.

Single phase, half wave ,60Hz, resistive or inductive load.

For capacitive load, derate current by 20%

CHARACTERISTICS	SYMBOL	SR520	SR530	SR540	SR550	SR560	SR580	SR5100	UNIT	
Maximum Recurrent Peak Reverse Voltage	VRRM	20	30	40	50	60	80	100	V	
Maximum RMS Voltage	VRMS	14	21	28	35	42	56	70	V	
Maximum DC Blocking Voltage	VDC	20	30	40	50	60	80	100	V	
Maximum Average Forward Rectified Current 0.375" (9.5mm) Lead Lengths @TL=95 °C	I(AV)	5.0							A	
Peak Forward Surage Current 8.3ms Single Half Sine-Wave Super Imposed on Rated Load(JEDEC Method)	IFSM	150							A	
Maximum Forward Voltage at 5.0A DC	VF	0.45	0.55	0.6	0.7		0.85		V	
Maximum DC Reverse Current @TJ=25°C at Rated DC Bolcking Voltage @TJ=100°C	IR					1.0		50		mA
Typical Junction Capacitance (Note1)	CJ	500			350				pF	
Typical Thermal Resistance (Note2)	RθJA	15			10				°C/W	
Operating Temperature Range	TJ	-55 to +150							°C	
Storage Temperature Range	TSTG	-55 to +150							°C	

NOTES: 1.Measured at 1.0 MHz and applied reverse voltage of 4.0V DC

2.Thermal resistance junction to ambient,