Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSIII)

2SK2749

Chopper Regulator, DC-DC Converter and Motor Drive Applications

Low drain-source ON resistance : R_{DS (ON)} = 1.6 Ω (typ.)
 High forward transfer admittance : |Y_{fs}| = 5.0 S (typ.)
 Low leakage current : I_{DSS} = 100 μA (max) (V_{DS} = 720 V)
 Enhancement mode : V_{th} = 2.0 to 4.0 V (V_{DS} = 10 V, I_D = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V _{DSS}	900	V	
Drain-gate voltage (R _{GS} = 20 kΩ)		V _{DGR}	900	V	
Gate-source voltage		V _{GSS}	±30	V	
Drain current	DC (Note 1)	ID	7		
	Pulse (Note 1)	I _{DP}	21	Α	
Drain power dissipation (Tc = 25°C)		PD	150	W	
Single pulse avalanche energy (Note 2)		E _{AS}	682	mJ	
Avalanche current		I _{AR}	7	Α	
Repetitive avalanche energy (Note 3)		E _{AR}	15	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55 to 150	°C	

Weight: 4.6 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	0.833	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	50	°C/W

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Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 25.5 mH, I_{AR} = 7 A, R_G = 25 Ω

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

Please handle with caution.

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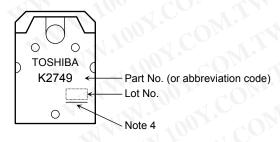
Electrical Characteristics (Ta = 25°C)

Chara	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	V _{GS} = ±25 V, V _{DS} = 0 V			±10	μΑ
Gate-source br	eakdown voltage	V (BR) GSS	I _G = ±10 μA, V _{DS} = 0 V	±30		4	V
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = 720 V, V _{GS} = 0 V	V	1	100	μΑ
Drain-source b	reakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	900		77/	V
Gate threshold	voltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0		4.0	V
Drain-source O	N resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 3.5 A	4.	1.6	2.0	Ω
Forward transfe	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 3.5 A	1.25	5.0		S
Input capacitance Reverse transfer capacitance		C _{iss}			1500	$O_{\hat{Z}_{[F]}}$	
		C _{rss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	_	30	-	pF
Output capacita	nce	Coss	The Man	140	140	C7.,	
Switching time	Rise time	tr	$V_{GS} \stackrel{10V}{\underset{0V}{\text{ID}}} \stackrel{I_{D}=3.5A}{\underset{\text{Po}}{\text{VOUT}}}$	1	35	(£)	$\mathcal{D}_{N_{P}}$
	Turn-on time	ton	$R_{L}=114\Omega$		80	1	ns
	Fall time	tf		-	50	720	
	Turn-off time	t _{off}	Duty $\leq 1\%$, $t_{\mathbf{W}} = 10 \mu \text{s}$		220	-10	
Total gate charge (gate-source plus gate-drain)		Qg	V _{DD} ≈ 400 V, V _{GS} = 10 V, I _D = 7 A		55		nC
Gate-source charge		Q _{gs}		_	30		
Gate-drain ("miller") Charge		Q _{gd}	ON. ON.		25	71	4.

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	14. 10-07 CO.		_	7	A
Pulse drain reverse current (Note 1)	I _{DRP}	My IM jag Co.			21	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 7 A, V _{GS} = 0 V	_		-1.9	V
Reverse recovery time	t _{rr}	I _{DR} = 7 A, V _{GS} = 0 V	2/4	1400	_	ns
Reverse recovery charge	Q _{rr}	dI _{DR} / dt = 100 A / μs		14		μC

Marking



Note 4: A line under a Lot No. identifies the indication of product Labels.

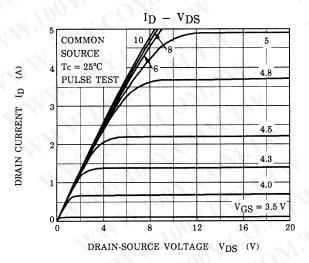
Not underlined: [[Pb]]/INCLUDES > MCV

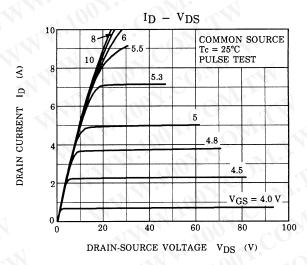
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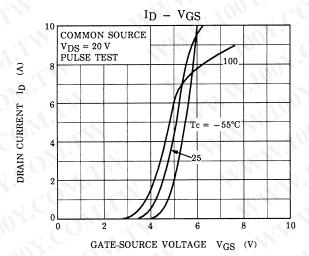
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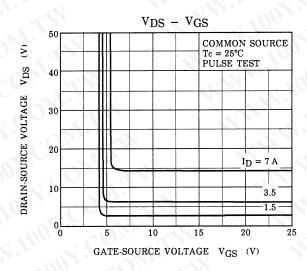
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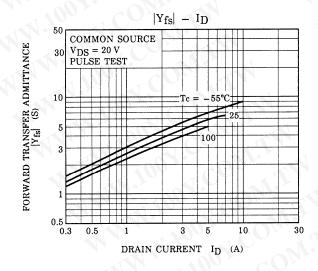
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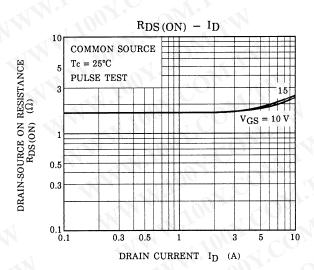




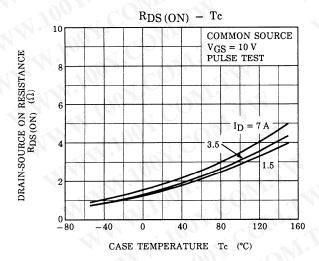


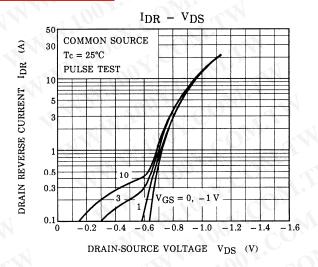


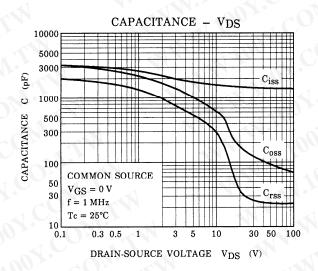


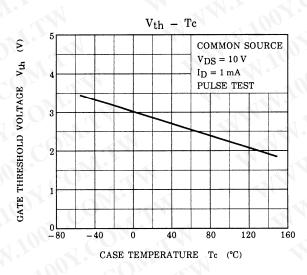


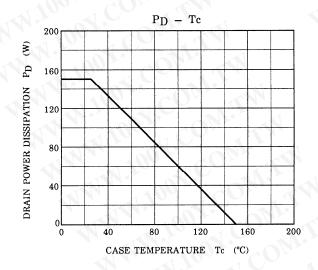
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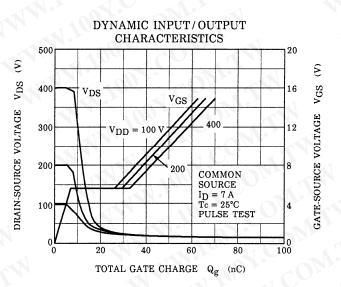




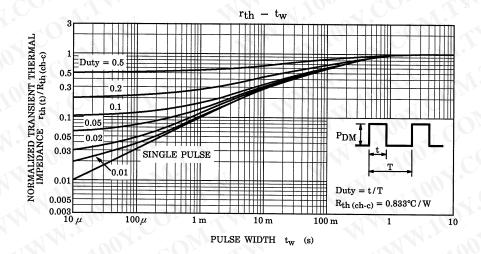


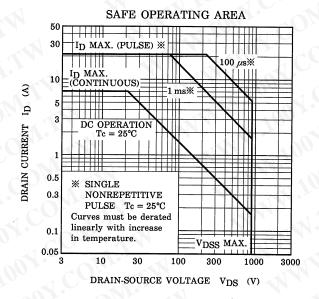


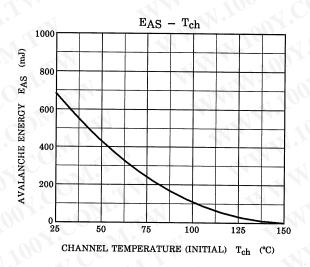


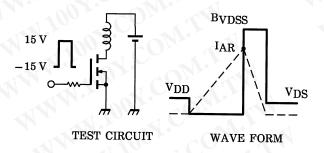


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$$\begin{aligned} R_G &= 25~\Omega \\ V_{DD} &= 90~V,~L = 25.5~mH \end{aligned} \quad E_{AS} &= \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{BVDSS}{BVDSS - V_{DD}} \right) \end{aligned}$$

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