TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSVI)

# 2SK3567

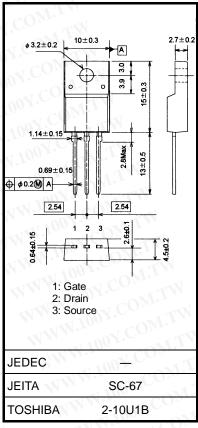
## **Switching Regulator Applications**

Unit: mm

- Low drain-source ON resistance: RDS (ON) =  $1.7\Omega$  (typ.)
- High forward transfer admittance:  $|Y_{fs}| = 2.5S$  (typ.)
- Low leakage current:  $I_{DSS} = 100 \, \mu \, A \, (V_{DS} = 600 \, V)$
- Enhancement mode:  $V_{th} = 2.0 \sim 4.0 \text{ V (VDS} = 10 \text{ V, ID} = 1 \text{ mA)}$

## Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain-source voltage		V <sub>DSS</sub>	600	V
Drain-gate voltage ( $R_{GS} = 20 \text{ k}\Omega$ )		$V_{DGR}$	600	V
Gate-source voltage		V <sub>GSS</sub>	±30	V
Drain current	DC (Note 1)	I <sub>D</sub>	3.5	TA
	Pulse (t = 1 ms) (Note 1)	I <sub>DP</sub>	14	T. A
Drain power dissipation (Tc = 25°C)		PD	35	W
Single pulse avalanche energy (Note 2)		E <sub>AS</sub>	201	mJ
Avalanche current		I <sub>AR</sub>	3.5	Α
Repetitive avalanche energy (Note 3)		E <sub>AR</sub>	3.5	_ mJ
Channel temperature		T <sub>ch</sub>	150	√ °C
Storage temperature range		T <sub>stg</sub>	-55~150	°C O



Weight: 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

### **Thermal Characteristics**

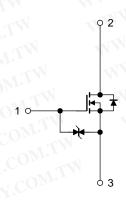
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Characteristics	Symbol	Max	Unit	
Thermal resistance, channel to case	R <sub>th</sub> (ch-c)	3.57	°C/W	
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	62.5	°C/W	



Note 2: 
$$V_{DD} = 90 \text{ V}$$
,  $T_{ch} = 25^{\circ}\text{C}(\text{initial})$ ,  $L = 28.8 \text{ mH}$ ,  $I_{AR} = 3.5 \text{ A}$ ,  $R_G = 25 \Omega$ 

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.



#### **Electrical Characteristics (Ta = 25°C)**

Char	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I <sub>GSS</sub>	$V_{GS} = \pm 25 \text{ V}, V_{DS} = 0 \text{ V}$	W.	_	±10	μΑ
Gate-source brea	Gate-source breakdown voltage		$I_G = \pm 10 \ \mu A, \ V_{DS} = 0 \ V$	±30	_	_	V
Drain cut-off current		I <sub>DSS</sub>	V <sub>DS</sub> = 600 V, V <sub>GS</sub> = 0 V	W-,	- I	100	μΑ
Drain-source breakdown voltage		V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	600	_	_	V
Gate threshold voltage		$V_{th}$	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0		4.0	V
Drain-source ON resistance		R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 1.8 A	-34	1.7	2.2	Ω
Forward transfer admittance		Yfs	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1.8 A	0.7	2.5	_	S
Input capacitance		C <sub>iss</sub>	TW WWW.	I.Co.	550	_	
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1 MHz	V-CC	6	N-	pF
Output capacitance		C <sub>oss</sub>		√-C	60	CV <del>V</del>	
Switching time	Rise time	t <sub>r</sub>	10 V I <sub>D</sub> = 1.8 A V <sub>OUT</sub>	(0 <del>0/</del> 1.	12	T	
	Turn-on time	t <sub>on</sub>	0 V	1003	45	LTW	
	Fall time	t <sub>f</sub>	V <sub>DD</sub> ≈ 200 V	N.100	13	M.I	ns
	Turn-off time	t <sub>off</sub>	Duty ≦ 1%, t <sub>W</sub> = 10 μs	M <u>41</u> .)	80	COM!	LM.
Total gate charge	Total gate charge		1001. ON I.A. M.	WW	16	c <del>o</del> N	. 1
Gate-source cha	Gate-source charge		$V_{DD} \simeq 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 3.5 \text{ A}$	- T	10	- <del></del> 01	nC
Gate-drain charge		Q <sub>gd</sub>	TIONY	MA.	< 6 O	7.	

## Source-Drain Ratings and Characteristics (Ta = 25°C)

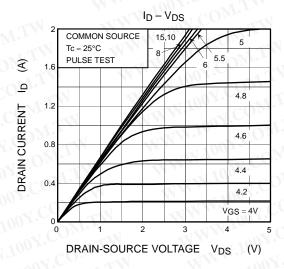
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	M.M. 100X-COM.TA	_	WAY	3.5	X A
Pulse drain reverse current (Note 1)	N I <sub>DRP</sub>	MAM. 100x CO. TA	_	M	14	A
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 3.5 A, V <sub>GS</sub> = 0 V	_	411	-1.7	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 3.5 A, V <sub>GS</sub> = 0 V,	N-	1400	MAN.	ns
Reverse recovery charge	$Q_{rr}$ $dl_{DR}/dt = 100 A/\mu$	dl <sub>DR</sub> /dt = 100 A/μs	-XX	9.0	N	μС

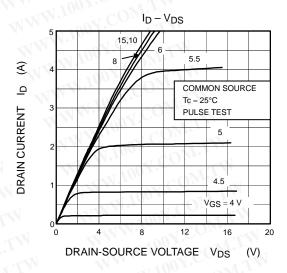
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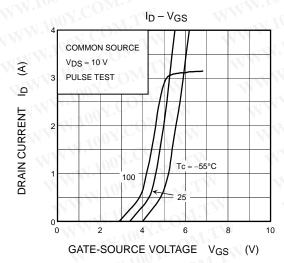


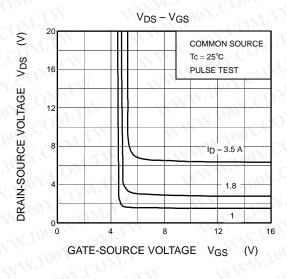
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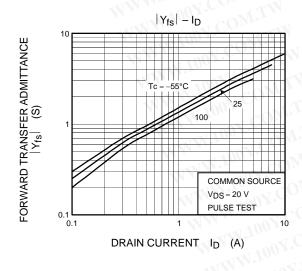
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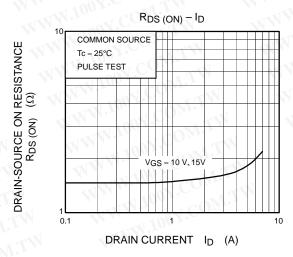




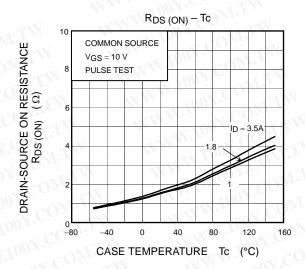


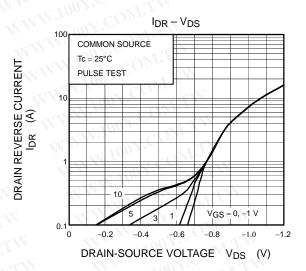


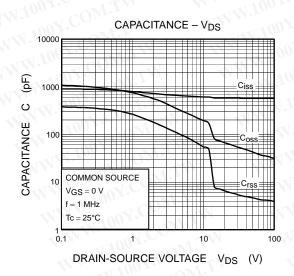


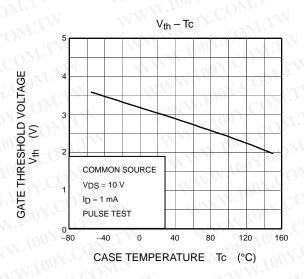


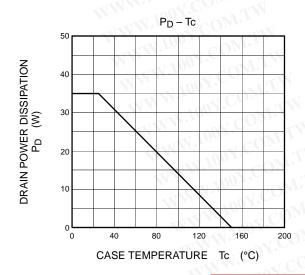
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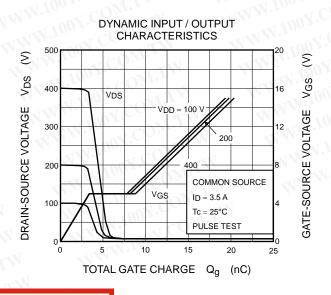




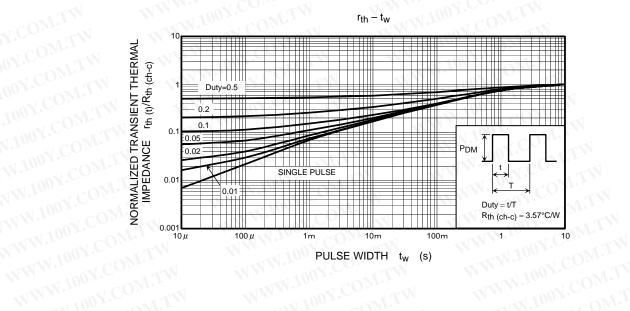


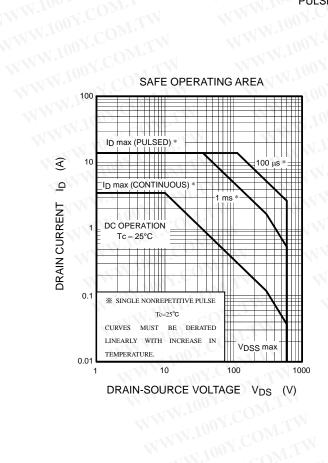


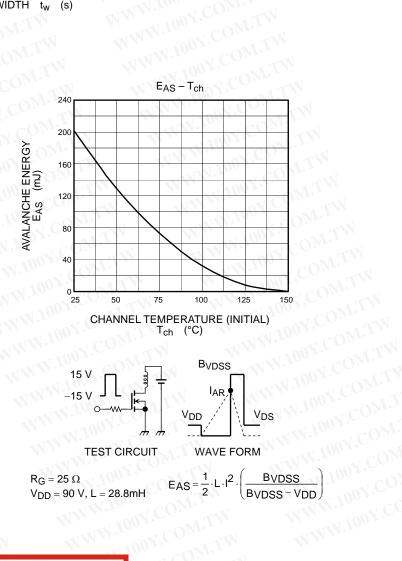




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