TOSHIBA Field Effect Transistor Silicon P Channel MOS Type (U-MOS III)

TPCF8101

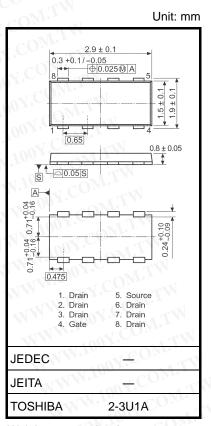
Notebook PC Applications Portable Equipment Applications

- Low drain-source ON resistance: RDS (ON) = 22 m Ω (typ.)
- High forward transfer admittance: $|Y_{fs}| = 14 \text{ S (typ.)}$
- Low leakage current: $IDSS = -10 \mu A (max) (VDS = -12 V)$
- Enhancement model: $V_{th} = -0.5$ to -1.2 V

 $(V_{DS} = -10 \text{ V}, I_{D} = -200 \mu\text{A})$

Absolute Maximum Ratings (Ta = 25°C)

Chara	cteristics	Symbol	Rating	Unit
Drain-source volta	ige	V _{DSS}	-12	V
Drain-gate voltage	e (R _{GS} = 20 kΩ)	V _{DGR}	-12	V
Gate-source volta	ge	V _{GSS}	±8	V
Ducin a 400 Y.C	DC (Note 1)	I _D	-6	TIN
Drain current	Pulsed (Note 1)	I _{DP}	-24	Α
Drain power dissip	oation (t = 5 s) (Note 2a)	PD	2.5	W
Drain power dissip	oation (t = 5 s) (Note 2b)	P _D	0.7	ow
Single pulse avala	inche energy (Note 3)	E _{AS}	6.3	mJ
Avalanche current	On r. COWIT	I _{AR}	-3	A
Repetitive avaland	che energy (Note 4)	E _{AR}	0.25	mJ
Channel temperat	ure	T _{ch}	150	°C
Storage temperatu	ure range	T _{stg}	-55~150	00°C



Weight: 0.011 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

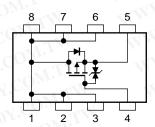
Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient (t = 5 s) (Note 2a)	R _{th (ch-a)}	50.0	°C/W
Thermal resistance, channel to ambient (t = 5 s) (Note 2b)	R _{th (ch-a)}	178.6	°C/W

Note: (Note 1), (Note 2), (Note 3), (Note 4) and (Note 5): See the next page.

This transistor is an electrostatic-sensitive device. Please handle with caution.

Circuit Configuration



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Electrical Characteristics (Ta = 25°C)

Ch	aracteristics	Symbol	Test Condition	Min	Тур.	Max	Uni
Gate leakage cui	rrent	I _{GSS}	$V_{GS} = \pm 8 \text{ V}, V_{DS} = 0 \text{ V}$	V er	_	±10	μΑ
Drain cut-off curr	ent	I _{DSS}	$V_{DS} = -12 \text{ V}, V_{GS} = 0 \text{ V}$	T.V.	_	-10	μА
Drain-source bre	akdown voltago	V (BR) DSS	$I_D = -10 \text{ mA}, V_{GS} = 0 \text{ V}$	-12	N —	_	V
Dialii-Source bre	akdowii voltage	V (BR) DSX	$I_D = -10 \text{ mA}, V_{GS} = 8 \text{ V}$	-4		_	v
Gate threshold ve	oltage	V _{th}	$V_{DS} = -10 \text{ V}, I_D = -200 \mu\text{A}$	-0.5		-1.2	V
TIV	WW	R _{DS} (ON)	$V_{GS} = -1.8 \text{ V}, I_D = -1.5 \text{ A}$	Mos	60	85	
Drain-source ON	resistance	R _{DS} (ON)	$V_{GS} = -2.5 \text{ V}, I_D = -3.0 \text{ A}$		32	40	mΩ
		R _{DS} (ON)	$V_{GS} = -4.5 \text{ V}, I_D = -3.0 \text{ A}$	I.Co.	22	28	
Forward transfer	admittance	Y _{fs}	$V_{DS} = -10 \text{ V}, I_{D} = -3.0 \text{ A}$	7	14	N-	S
Input capacitance	9	C _{iss}	M. T. WALLET	N.C	1600	W.	
Reverse transfer	capacitance	C _{rss}	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		260	TV.	pF
Output capacitan	ice	C _{oss}	OM.TWW.	100	335		
100X.CO	Rise time	100 tr	0 V] [I _D = -3.0 A	100 100	₁ .70)	<u> </u>	Ŋ.
Switching time	Turn-on time	ton	V _{GS} V _{OUT} C C C C	11.10	13	OM.T	ns
N.M.1007	Fall time	t _f 10	4.7.Ω W W W W W W W W W W W W W W W W W W W	NA.	21	COM.	TV
WW.100Y	Turn-off time	t _{off}	$V_{DD} \simeq -6 \ V$ Duty \leq 1%, $t_W = 10 \ \mu s$	V	68	CO_{N}	A.T
Total gate charge (gate-source plus		Qg	- V _{DD} ≈ -10 V, V _{GS} = -5 V,		18.0	V.C	M
Gate-source cha	rge	Q _{gs}	$I_D = -6.0 \text{ A}$	- NV	14.5	C	nC
Gate-drain ("mille	er") charge	Q _{gd}	W 100 1. CON'II.		3.5	00	100

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Source-Drain Ratings and Characteristics (Ta = 25°C)

Chara	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Drain reverse current	Pulse (Note 1)	I _{DRP}	MANN TOON COM	1.TV -		-24	O A
Forward voltage	e (diode)	V _{DSF}	I _{DR} = -6.0 A, V _{GS} = 0 V	M.T.W_		1.2	V

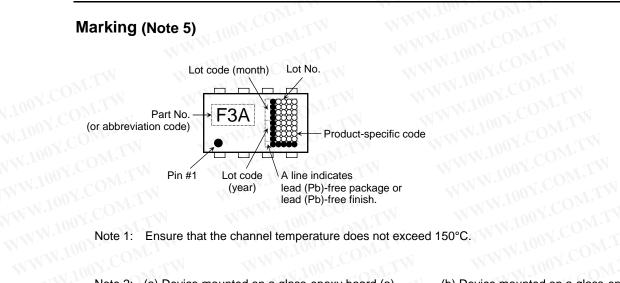
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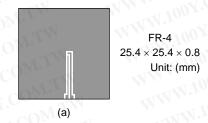
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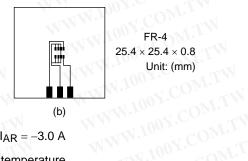
Marking (Note 5)



Note 2: (a) Device mounted on a glass-epoxy board (a)

(b) Device mounted on a glass-epoxy board (b)



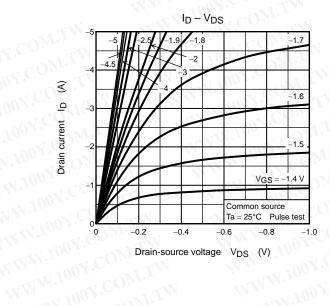


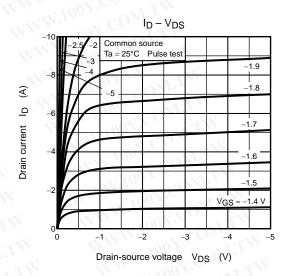
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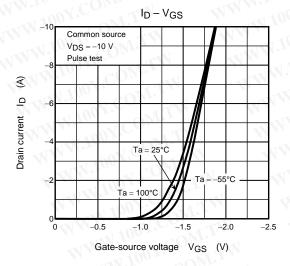
Note 3: $V_{DD} = -10 \text{ V}$, $T_{ch} = 25^{\circ}\text{C}$ (initial), L = 0.5 mH, $R_G = 25 \Omega$, $I_{AR} = -3.0 \text{ A}$

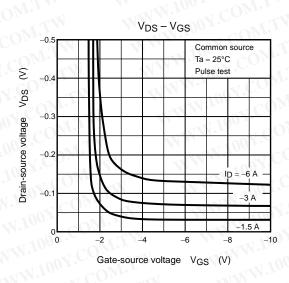
Note 4: Repetitive rating: pulse width limited bymaximum channel temperature

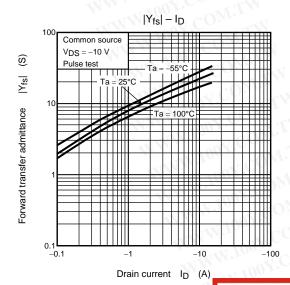
Note 5: A dot on the lower left of the marking indicates Pin 1. WWW.100Y.COM.TW TOSHIBA TPCF8101

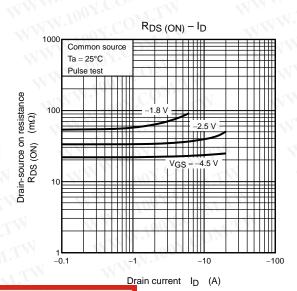






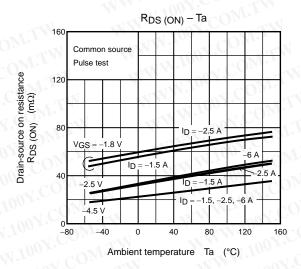


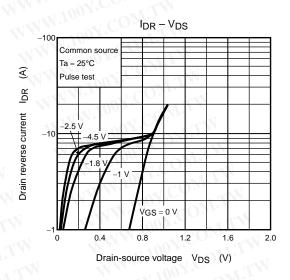


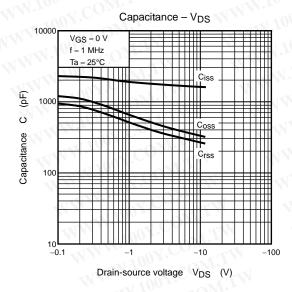


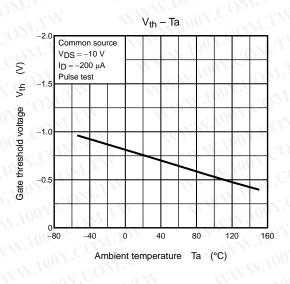
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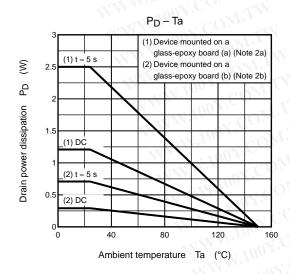
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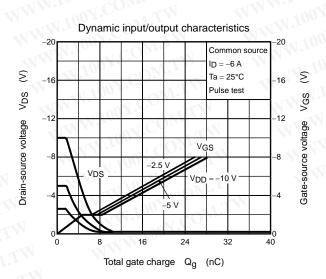




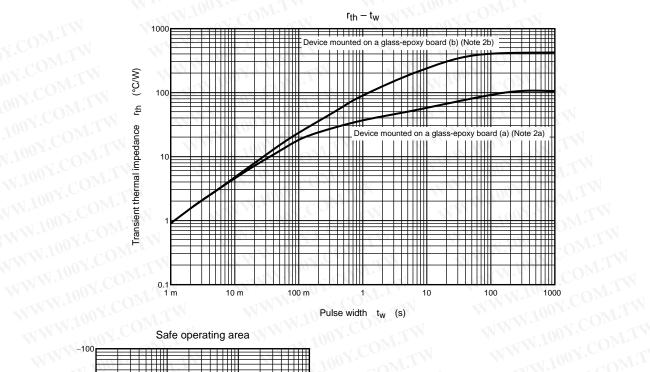


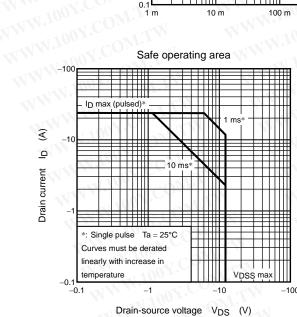






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