TOSHIBA Field Effect Transistor with Built-in Schottky Barrier Diode

Silicon N-Channel MOS Type (U-MOS V-H)

TPC8A03-H

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Unit: mm

High Efficiency DC-DC Converter Applications Notebook PC Applications Portable Equipment Applications

- Built-in schottky barrier diode
 Low forward voltage: V_{DSF} = -0.6 V (max)
- High-speed switching
- Small gate charge: Q_{SW} = 8.4 nC (typ.)
- Low drain-source ON-resistance: $R_{DS (ON)} = 4.1 \text{ m}\Omega \text{ (typ.)}$
- High forward transfer admittance: |Yfs| = 54 S (typ.)
- Low leakage current: $I_{DSS} = 100 \mu A \text{ (max) (V}_{DS} = 30 \text{ V)}$
- Enhancement mode: V_{th} = 1.3 to 2.3 V (V_{DS} = 10 V, I_D = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unit	
Drain-source volta	ge	V _{DSS}	30	V	
Drain-gate voltage	(R _{GS} = 20 kΩ)	V_{DGR}	30	V	
Gate-source voltage		V _{GSS}	±20	V	
Drain augrant	DC (Note 1)	I _D	17		
Drain current	Pulsed (Note 1)	I _{DP}	VDSS 30 VDGR 30 VGSS ±20 ID 17 IDP 68 PD 1.9 PD 1.0 EAS 188 IAR 17 EAR 0.108 Tch 150	Α	
Drain power dissipation (t = 10 s) (Note 2a)		PD	1.9	C w	
Drain power dissipation (t = 10 s) (Note 2b)		P _D	1.0	W	
Single-pulse avalanche energy (Note 3)		E _{AS}	188	mJ	
Avalanche current		I _{AR}	17	Α	
Repetitive avalanche energy (Tc = 25°C) (Note 4)		E _{AR}	0.108	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55 to 150	°C	

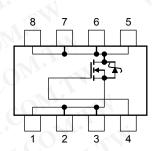
1, 2, 3 SOURCE 4 GATE 5, 6, 7, 8 DRAIN

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2-6J1B

Weight: 0.085g (typ.)

Circuit Configuration



Note: For Notes 1 to 4, refer to the next page.

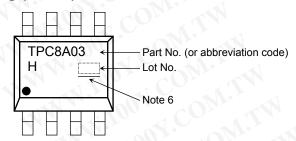
Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc.)

This transistor is an electrostatic-sensitive device. Handle with care.

Thermal Characteristics

Characteristic	Symbol	Max	Unit
Thermal resistance, channel to ambient (t = 10 s) (Note 2a)	R _{th (ch-a)}	65.8	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2b)	R _{th (ch-a)}	125	°C/W

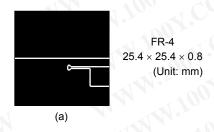
Marking (Note 5)

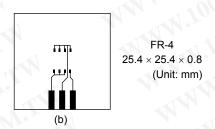


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Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: (a) Device mounted on a glass-epoxy board (a) (b) Device mounted on a glass-epoxy board (b)





Note 3: $V_{DD} = 24 \text{ V}$, $T_{ch} = 25^{\circ}\text{C}$ (initial), L = 0.5 mH, $R_G = 25 \Omega$, $I_{AR} = 17 \text{ A}$

Note 4: Repetitive rating: pulse width limited by maximum channel temperature

Note 5:

* Weekly code: (Three digits)

Week of manufacture
(01 for first week of year, continuing up to 52 or 53)

Year of manufacture

(The last digit of the calendar year)

Note 6: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV

Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

Electrical Characteristics (Ta = 25°C)

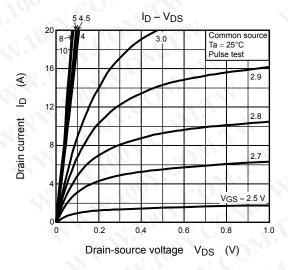
Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$			±100	nA
Drain cut-off curr	ent	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V		_	100	μА
Drain-source breakdown voltage		V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	30		1	V
Drain-source bre	akdown voltage	V (BR) DSX	$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$	15	/		V
Gate threshold ve	oltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	1.3		2.3	V
Drain-source ON-resistance Forward transfer admittance		D	$V_{GS} = 4.5 \text{ V}, I_D = 8.5 \text{ A}$	+	5.1	7.0	mΩ
		R _{DS} (ON)	V _{GS} = 10 V, I _D = 8.5 A		4.1	5.6	
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 8.5 A	27	54		S
Input capacitance		C _{iss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz		2640	3430	
Reverse transfer capacitance		C _{rss}		14	100	150	pF
Output capacitance		Coss		_	610	_(
Gate resistance	100	rg	V _{DS} = 10 V, V _{GS} = 0 V, f = 5 MHz	1.0 1.5		1.5	Ω
Switching time	Rise time	tr	$V_{GS} \stackrel{10 \text{ V}}{\longrightarrow} I_{D} = 8.5 \text{ A}$ $V_{DD} \approx 15 \text{ V}$ $V_{DD} \approx 15 \text{ V}$ $V_{DU} \approx 15 \text{ V}$ $V_{DU} \approx 15 \text{ V}$		3.6		ns
	Turn-on time	ton			11.0	440	
	Fall time	tf			7.2	-10	
	Turn-off time	t _{off}			42		
Total gate charge (gate-source plus gate-drain)		1 000	$V_{DD} \approx 24 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 17 \text{ A}$		36		. 40
		Q_g	$V_{DD} \approx 24 \text{ V}, V_{GS} = 5 \text{ V}, I_{D} = 17 \text{ A}$		19		nC
Gate-source charge 1		Q _{gs1}	$V_{DD} \approx 24 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 17 \text{ A}$		7.6	NZ.	
Gate-drain ("Miller") charge		Q _{gd}		_	5.0	_	
Gate switch charge		Q _{SW}		<u> </u>	8.4	1	

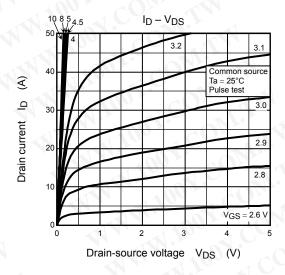
Source-Drain Ratings and Characteristics (Ta = 25°C)

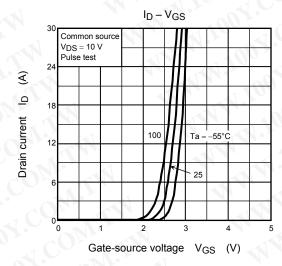
Character	ristic	-1	Symbol	Test Condition	Min	Тур.	Max	Unit
Drain reverse current	Pulse	(Note 1)	I _{DRP}	M_{\perp} , $^{1}\theta_{0}$, 1 CO ₂	_ <		68	Α
Forward voltage (diode)			V_{DSF}	I _{DR} = 1 A, V _{GS} = 0 V	4	- 0.4	- 0.6	V
				I _{DR} = 17 A, V _{GS} = 0 V			- 1.2	V

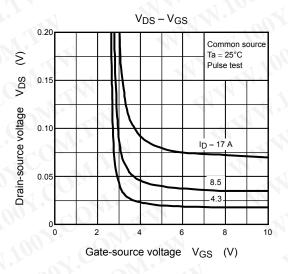
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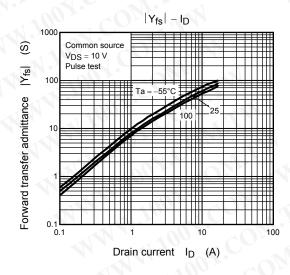
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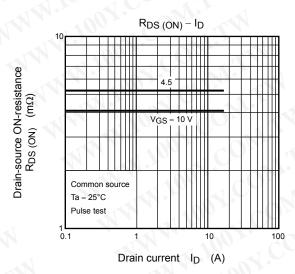








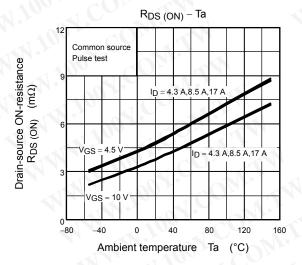


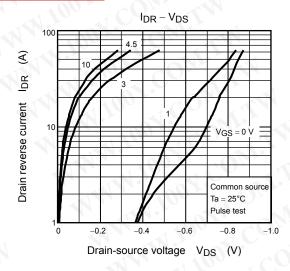


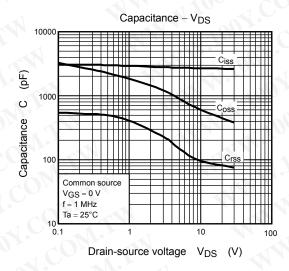
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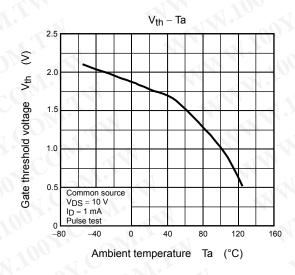
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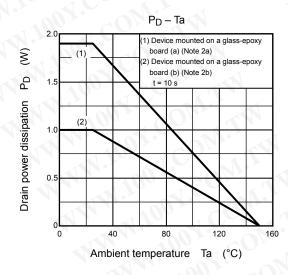
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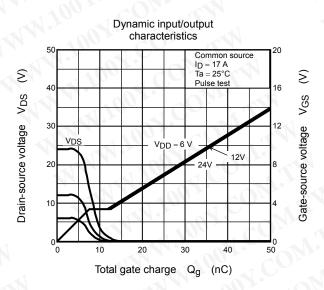


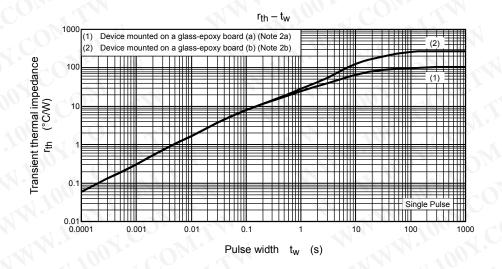


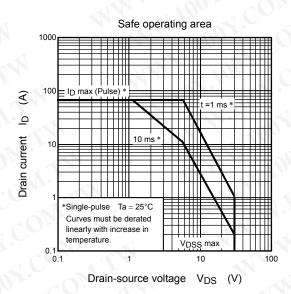






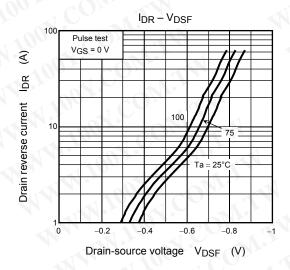


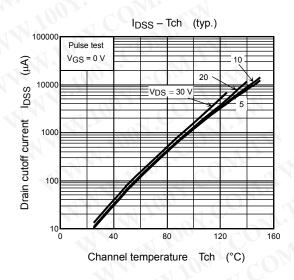


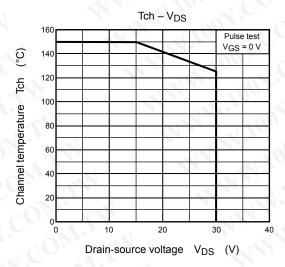


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