

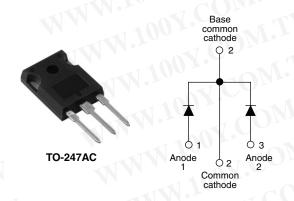
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60CPQ150PbF

ROHS

Vishay High Power Products

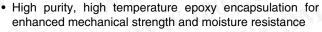
Schottky Rectifier, 2 x 30 A



PRODUCT SUMMARY	WWW.C
I _{F(AV)}	2 x 30 A
V_R	150 V

FEATURES

- 175 °C T_J operation
- Center tap TO-247 package
- Low forward voltage drop
- · High frequency operation



- Guard ring for enhanced ruggedness and long term reliability
- · Lead (Pb)-free ("PbF" suffix)
- · Designed and qualified for industrial leve

DESCRIPTION

The 60CPQ150PbF center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	60	Α		
V _{RRM}	1007.	150	V		
I _{FSM}	$t_p = 5 \mu s sine$	2300	Α		
V _F	30 Apk, T _J = 125 °C (per leg)	0.67	V		
TJ	Range	- 55 to 175	°C		

VOLTAGE RATINGS			
PARAMETER	SYMBOL	60CPQ150PbF	UNITS
Maximum DC reverse voltage	V _R	150	V
Maximum working peak reverse voltage	V _{RWM}	150	V

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST COND	ITIONS	VALUES	UNITS		
Maximum average per leg	I _{F(AV)}	50 % duty cycle at T _C = 151 °C	rectangular waveform	30			
See fig. 5 per device	'F(AV)	50 % duty cycle at T _C = 151 °C, rectangular waveform		60			
Maximum peak one cycle non-repetitive		5 μs sine or 3 μs rect. pulse	Following any rated load	2300	A		
surge current per leg See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	condition and with rated V _{RRM} applied	510			
Non-repetitive avalanche energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 1 A, L = 1 mH		0.5	mJ		
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1	Α		

^{*} Pb containing terminations are not RoHS compliant, exemptions may apply

60CPQ150PbF

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ELECTRICAL SPECIFICATIO	NS					
PARAMETER	SYMBOL	TEST C	ONDITIONS	TYP.	MAX.	UNITS
TIW. It	- CO	30 A	T 05 °C	0.80	0.83	
Maximum forward voltage drop per leg	V (1)	60 A	$T_J = 25 ^{\circ}\text{C}$	0.93	0.99	V
See fig. 1	V _{FM} ⁽¹⁾	30 A	T 105 °C	0.64	0.67	
		60 A	T _J = 125 °C	0.74	0.77	
Maximum reverse leakage current per leg	I _{RM}	T _J = 25 °C	V Datad V	10	100	μA
See fig. 2		T _J = 125 °C	V_R = Rated V_R	12	25	mA
Typical junction capacitance per leg	C _T	V _R = 5 V _{DC} (test signal range 100 kHz to 1 MHz) 25 °C		10-	820	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		- T 1	7.5	nH
Maximum voltage rate of change	dV/dt	Rated V _R		11-0-	10 000	V/µs

Note

 $^{^{(1)}}$ Pulse width < 300 μ s, duty cycle < 2 %

THERMAL - MEC	HANICAL S	PECIFIC	ATIONS		
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T _J , T _{Stg}	OOX. OWITH	- 55 to 175	√°C \
Maximum thermal resistance, junction to case per leg		D	DC operation See fig. 4	0.8	w 10
Maximum thermal resistance, junction to case per package		- R _{thJC}	DC operation	0.4	°C/W
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.25	
Anarovimota waight			W.100	6	g
Approximate weight				0.21	oz.
Maunting taxque	minimum		TIN TOO	6 (5)	kgf · cm
Mounting torque	maximum		N. O. T.	12 (10)	(lbf · in)
Marking device			Case style TO-247AC (JEDEC)	60CP	Q150

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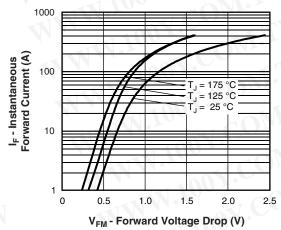


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

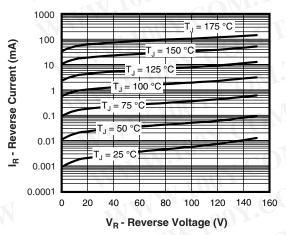


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

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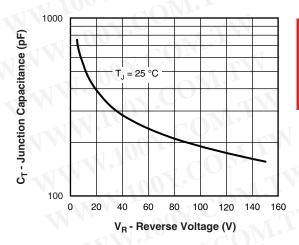


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

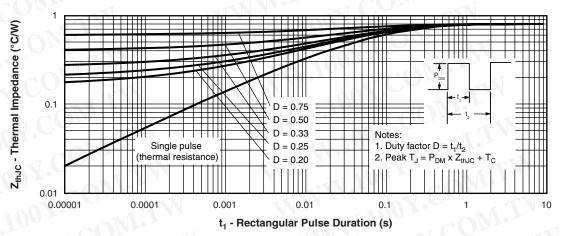


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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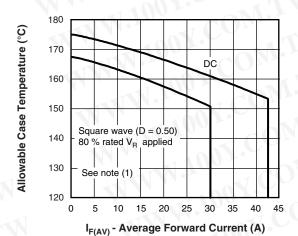


Fig. 5 - Maximum Allowable Case Temperature vs Average Forward Current (Per Leg)

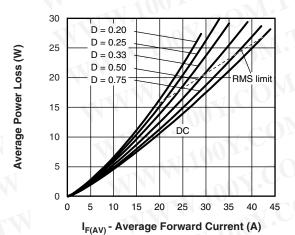


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

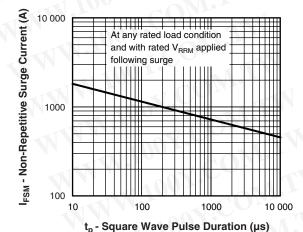


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

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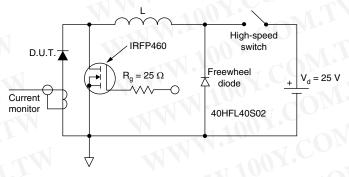


Fig. 8 - Unclamped Inductive Test Circuit

Note

(1) Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; Pd = Forward power loss = $I_{F(AV)} \times V_{FM}$ at ($I_{F(AV)}/D$) (see fig. 6); Pd_{REV} = Inverse power loss = $V_{R1} \times I_{R}$ (1 - D); I_{R} at V_{R1} = 80 % rated V_{R}

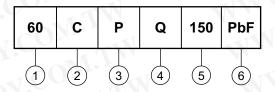
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ORDERING INFORMATION TABLE





- 1 Current rating (60 = 60 A)
- 2 Circuit configuration:

C = Common cathode

- Package:
 - P = TO-247
- 4 Schottky "Q" series
- 5 Voltage code (150 = 150 V)
- None = Standard production
 - PbF = Lead (Pb)-free

Tube standard pack quantity: 25 pieces

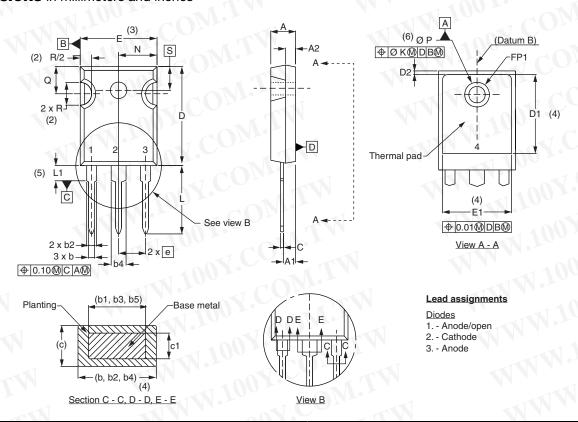
LINKS TO RELATED DOCUMENTS				
Dimensions	http://www.vishay.com/doc?95223			
Part marking information	http://www.vishay.com/doc?95226			

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Vishay Semiconductors

DIMENSIONS in millimeters and inches



SYMBOL	MILLIN	IETERS	INCHES		NOTES
STIMBUL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209	-7.1
A1	2.21	2.59	0.087	0.102	
A2	1.50	2.49	0.059	0.098	
р	0.99	1.40	0.039	0.055	- 1
- b1	0.99	1.35	0.039	0.053	1 44.
b2	1.65	2.39	0.065	0.094	_ 1
b3	1.65	2.37	0.065	0.094	
b4	2.59	3.43	0.102	0.135	
b5	2.59	3.38	0.102	0.133	
C	0.38	0.86	0.015	0.034	
c1	0.38	0.76	0.015	0.030	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
	MIN.	MAX.	MIN.	MAX.	NOTES
D2	0.51	1.30	0.020	0.051	- 11
E	15.29	15.87	0.602	0.625	3
E1	13.72	N -	0.540		
е	5.46	BSC	0.215	5 BSC	-111
7 FK	2.	54	0.0	010	
7.	14.20	16.10	0.559	0.634	
L1	3.71	4.29	0.146	0.169	
N	7.62	BSC	0	.3	
ΦР	3.56	3.66	0.14	0.144	- 1
ФР1	V.	6.98	-	0.275	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	1.78	0.216	-11
S	5.51	BSC	0.217	7 BSC	

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- $^{(6)}$ Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC outline TO-247 with exception of dimension c

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