

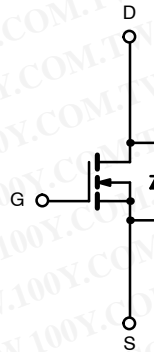
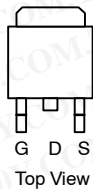


## N-Channel 150-V (D-S) 175°C MOSFET

### PRODUCT SUMMARY

$V_{(BR)DSS}$ (V)	$r_{DS(on)}$ ( $\Omega$ )	$I_D$ (A)
150	0.019 @ $V_{GS} = 10$ V	85 <sup>a</sup>

TO-263



N-Channel MOSFET

Ordering Information: SUM85N15-19  
SUM85N15-19-E3 (Lead Free)

### FEATURES

- TrenchFET® Power MOSFET
- 175°C Junction Temperature
- New Low Thermal Resistance Package
- 100%  $R_g$  Tested

### APPLICATIONS

- Primary Side Switch
- Automotive
  - 42-V EPS and ABS
  - DC/DC Conversion
  - Motor Drives

### ABSOLUTE MAXIMUM RATINGS ( $T_C = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	150	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current ( $T_J = 175^\circ\text{C}$ )	$I_D$	$T_C = 25^\circ\text{C}$	85 <sup>a</sup>
		$T_C = 125^\circ\text{C}$	50 <sup>a</sup>
Pulsed Drain Current	$I_{DM}$	180	A
Avalanche Current	$I_{AR}$	50	
Repetitive Avalanche Energy <sup>b</sup>	$E_{AR}$	125	mJ
Maximum Power Dissipation <sup>b</sup>	$P_D$	$T_C = 25^\circ\text{C}$	375 <sup>c</sup>
		$T_A = 25^\circ\text{C}^d$	3.75
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 to 175	$^\circ\text{C}$

### THERMAL RESISTANCE RATINGS

Parameter	Symbol	Limit	Unit
Junction-to-Ambient	$R_{thJA}$	40	$^\circ\text{C/W}$
Junction-to-Case (Drain)	$R_{thJC}$	0.4	

Notes

- Package limited.
- Duty cycle  $\leq 1\%$ .
- See SOA curve for voltage derating.
- When mounted on 1" square PCB (FR-4 material).

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**SPECIFICATIONS (T<sub>J</sub> = 25 °C UNLESS OTHERWISE NOTED)**

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>DS</sub> = 0 V, I <sub>D</sub> = 250 μA	150			V
Gate-Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA	2		4	
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ±20 V			±100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 150 V, V <sub>GS</sub> = 0 V			1	μA
		V <sub>DS</sub> = 150 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C			50	
		V <sub>DS</sub> = 150 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 175 °C			250	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> ≥ 5 V, V <sub>GS</sub> = 10 V	120			A
Drain-Source On-State Resistance <sup>a</sup>	r <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A		0.015	0.019	Ω
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A, T <sub>J</sub> = 125 °C			0.038	
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A, T <sub>J</sub> = 175 °C			0.050	
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 30 A	25			S
<b>Dynamic<sup>b</sup></b>						
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 25 V, f = 1 MHz		4750		pF
Output Capacitance	C <sub>oss</sub>			530		
Reverse Transfer Capacitance	C <sub>rss</sub>			220		
Total Gate Charge <sup>c</sup>	Q <sub>g</sub>	V <sub>DS</sub> = 75 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 85 A		76	110	nC
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>			21		
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			26		
Gate Resistance	R <sub>g</sub>		0.5	1.8	3.0	Ω
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>	V <sub>DD</sub> = 75 V, R <sub>L</sub> = 0.9 Ω I <sub>D</sub> = 85 A, V <sub>GEN</sub> = 10 V, R <sub>g</sub> = 2.5 Ω		22	35	ns
Rise Time <sup>c</sup>	t <sub>r</sub>			170	250	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			40	60	
Fall Time <sup>c</sup>	t <sub>f</sub>			170	250	
<b>Source-Drain Diode Ratings and Characteristics (T<sub>C</sub> = 25 °C)<sup>b</sup></b>						
Continuous Current	I <sub>S</sub>				85	A
Pulsed Current	I <sub>SM</sub>				180	
Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>F</sub> = 85 A, V <sub>GS</sub> = 0 V		1.0	1.5	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 50 A, di/dt = 100 A/μs		130	200	ns
Peak Reverse Recovery Current	I <sub>RM(REC)</sub>			8	12	A
Reverse Recovery Charge	Q <sub>rr</sub>			0.52	1.2	μC

**Notes**

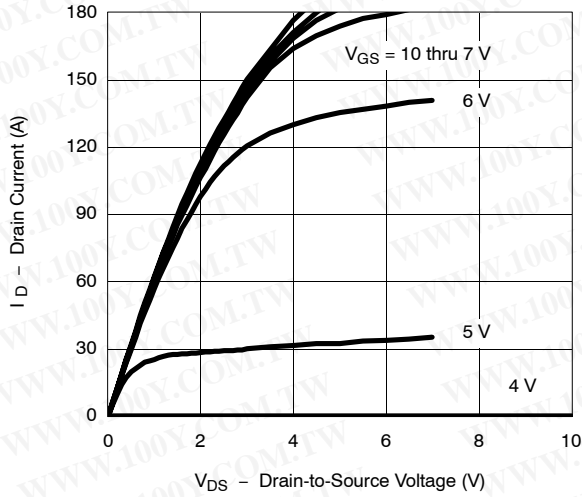
- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.  
b. Guaranteed by design, not subject to production testing.  
c. Independent of operating temperature.

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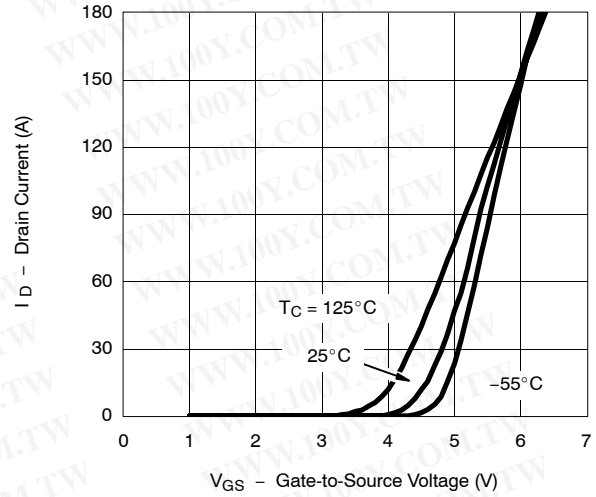


**TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)**

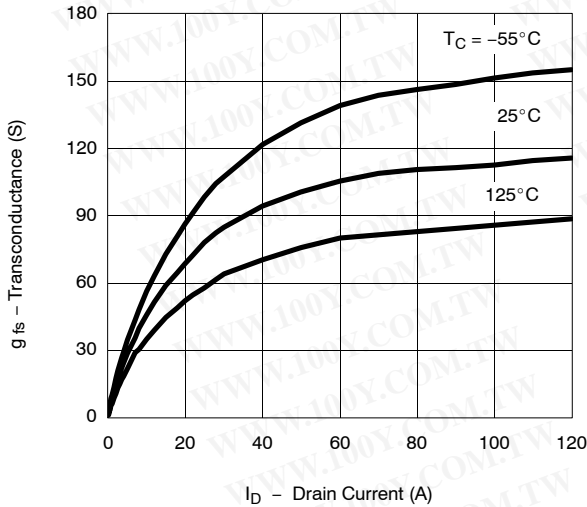
Output Characteristics



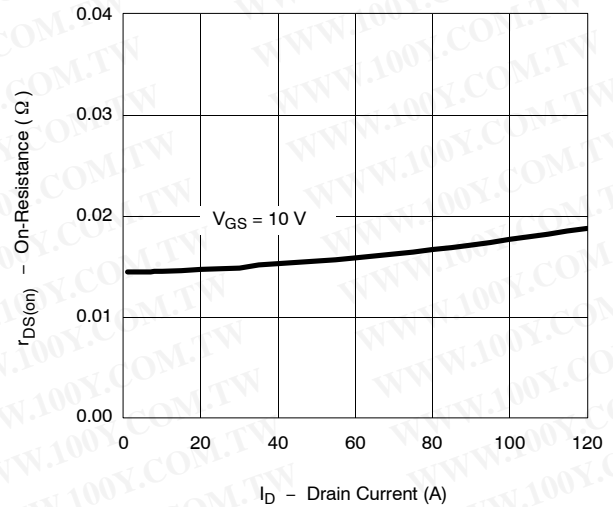
Transfer Characteristics



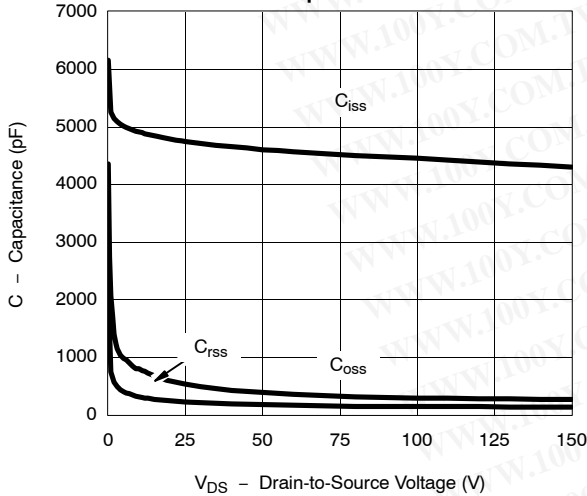
Transconductance



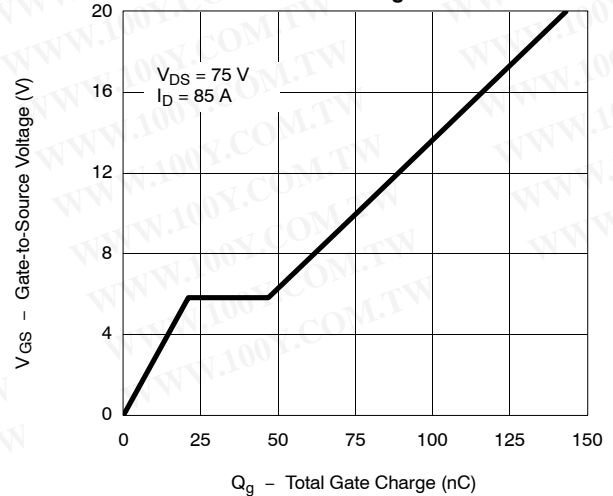
On-Resistance vs. Drain Current



Capacitance

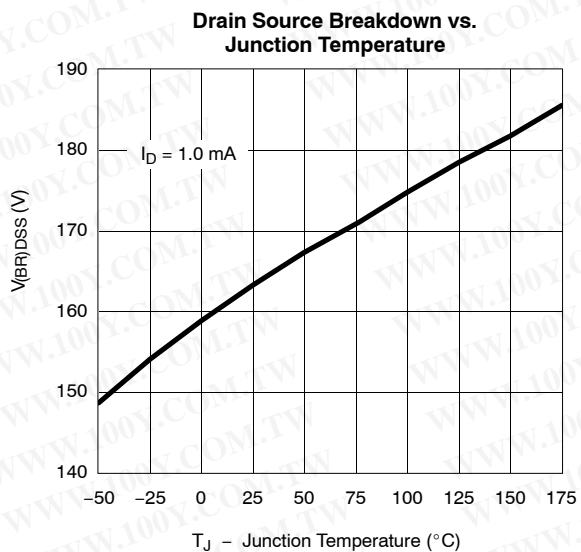
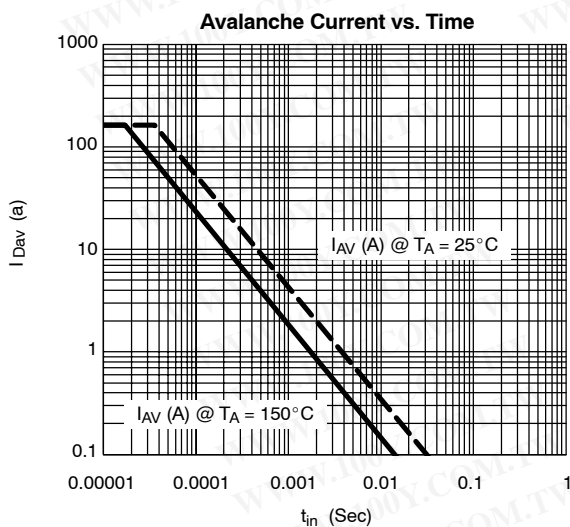
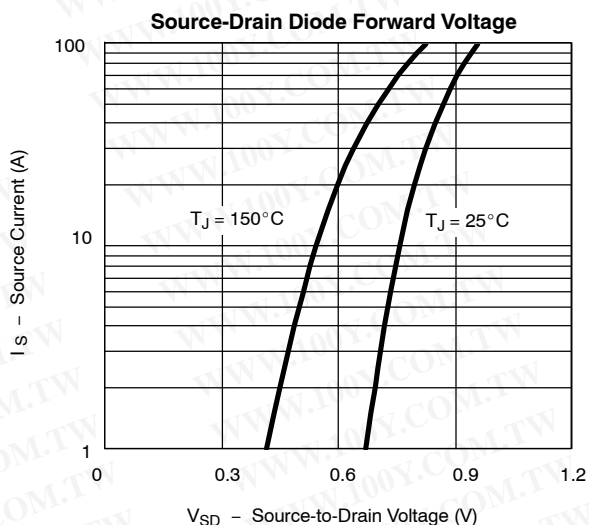
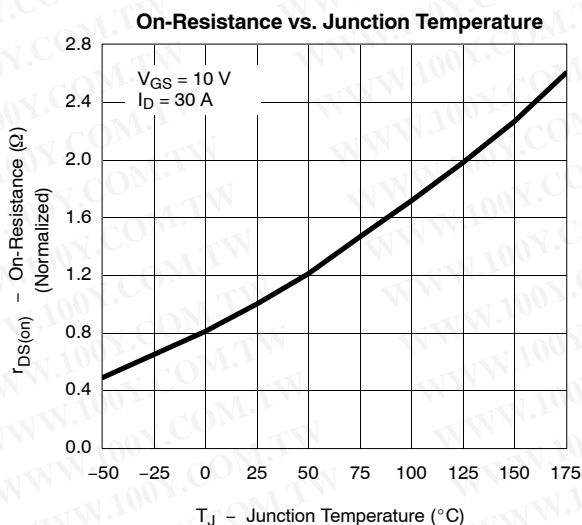


Gate Charge





**TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)**



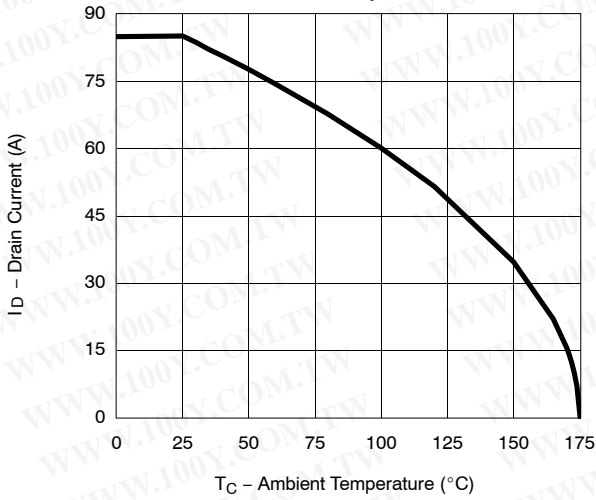
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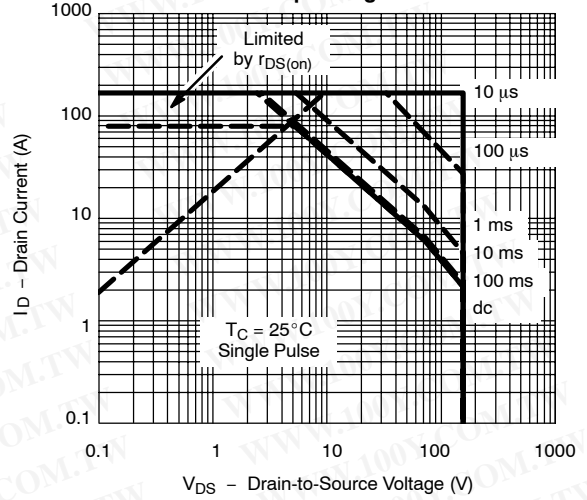


**THERMAL RATINGS**

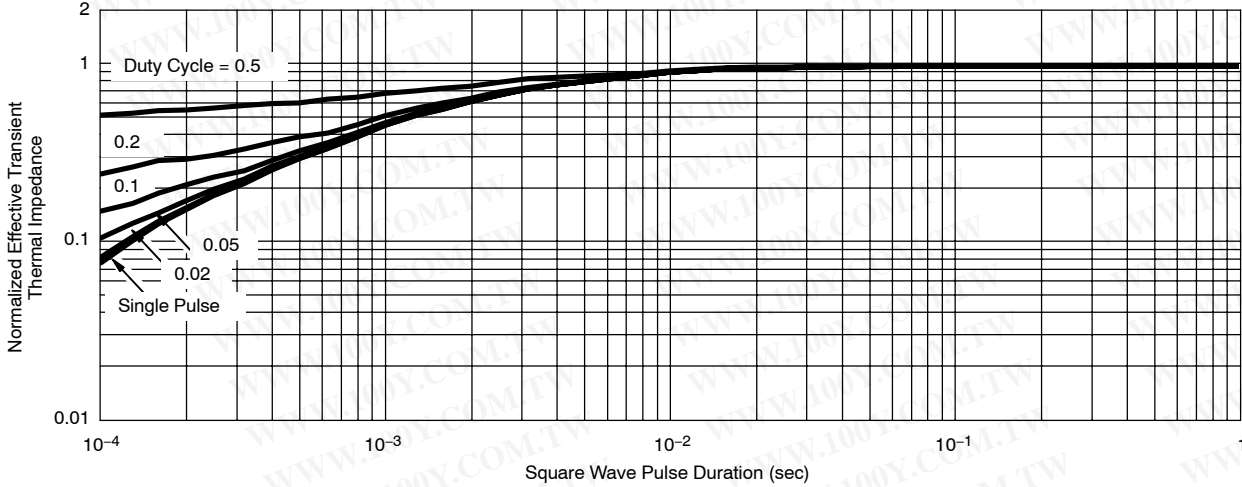
**Maximum Avalanche and Drain Current vs. Case Temperature**



**Safe Operating Area**



**Normalized Thermal Transient Impedance, Junction-to-Case**



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