



FQPF8N60CF

N-Channel QFET® FRFET® MOSFET

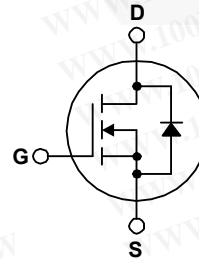
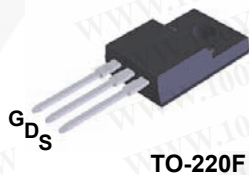
600 V, 6.26 A, 1.5 Ω

Description

This N-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, active power factor correction (PFC), and electronic lamp ballasts.

Features

- 6.26 A, 600 V, $R_{DS(on)} = 1.5 \Omega$ (Max.) @ $V_{GS} = 10 V$, $I_D = 3.13 A$
- Low Gate Charge (Typ. 28 nC)
- Low Crss (Typ. 12 pF)
- 100% Avalanche Tested



Absolute Maximum Ratings $T_C = 25^\circ C$ unless otherwise noted.

Symbol	Parameter	FQPF8N60CFT	Unit
V_{DSS}	Drain-Source Voltage	600	V
I_D	Drain Current - Continuous ($T_C = 25^\circ C$)	6.26*	A
	Drain Current - Continuous ($T_C = 100^\circ C$)	3.96*	A
I_{DM}	Drain Current - Pulsed (Note 1)	25*	A
V_{GSS}	Gate-Source Voltage	± 30	V
E_{AS}	Single Pulsed Avalanche Energy (Note 2)	160	mJ
I_{AR}	Avalanche Current (Note 1)	6.26	A
E_{AR}	Repetitive Avalanche Energy (Note 1)	14.7	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)	4.5	V/ns
P_D	Power Dissipation ($T_C = 25^\circ C$)	48	W
	- Derate above $25^\circ C$	0.38	W/ $^\circ C$
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to +150	$^\circ C$
T_L	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds	300	$^\circ C$

* Drain current limited by maximum junction temperature

Thermal Characteristics

Symbol	Parameter	FQPF8N60CFT	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	2.6	$^\circ C/W$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max.	62.5	

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Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FQPF8N60CFT	FQPF8N60CFT	TO-220F	Tube	N/A	N/A	50 units

Electrical Characteristics T_C = 25°C unless otherwise noted.

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit	
Off Characteristics							
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _D = 250 μA	600	--	--	V	
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient	I _D = 250 μA, Referenced to 25°C	--	0.7	--	V/°C	
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 600 V, V _{GS} = 0 V	--	--	10	μA	
		V _{DS} = 480 V, T _C = 125°C	--	--	100	μA	
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 30 V, V _{DS} = 0 V	--	--	100	nA	
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -30 V, V _{DS} = 0 V	--	--	-100	nA	
On Characteristics							
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250 μA	2.0	--	4.0	V	
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10 V, I _D = 3.13 A	--	1.25	1.5	Ω	
g _{FS}	Forward Transconductance	V _{DS} = 40 V, I _D = 3.13 A	--	8.7	--	S	
Dynamic Characteristics							
C _{iss}	Input Capacitance	V _{DS} = 25 V, V _{GS} = 0 V, f = 1.0 MHz	--	965	1255	pF	
C _{oss}	Output Capacitance		--	105	135	pF	
C _{rss}	Reverse Transfer Capacitance		--	12	16	pF	
Switching Characteristics							
t _{d(on)}	Turn-On Delay Time	V _{DD} = 300 V, I _D = 6.26A, R _G = 25 Ω	--	16.5	45	ns	
t _r	Turn-On Rise Time		--	60.5	130	ns	
t _{d(off)}	Turn-Off Delay Time		(Note 4)	--	81	170	ns
t _f	Turn-Off Fall Time		--	64.5	140	ns	
Q _g	Total Gate Charge	V _{DS} = 480 V, I _D = 6.26A, V _{GS} = 10 V	--	28	36	nC	
Q _{gs}	Gate-Source Charge		--	4.5	--	nC	
Q _{gd}	Gate-Drain Charge		(Note 4)	--	12	--	nC
Drain-Source Diode Characteristics and Maximum Ratings							
I _S	Maximum Continuous Drain-Source Diode Forward Current		--	--	6.26	A	
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current		--	--	25	A	
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0 V, I _S = 6.26 A	--	--	1.4	V	
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _S = 6.26 A, di _F / dt = 100 A/μs	--	82	--	ns	
Q _{rr}	Reverse Recovery Charge		--	242	--	nC	

NOTES:

1. Repetitive rating : pulse-width limited by maximum junction temperature.
2. L = 7.3 mH, I_{AS} = 6.26 A, V_{DD} = 50 V, R_G = 25 Ω, starting T_J = 25°C.
3. I_{SD} ≤ 6.26 A, di/dt ≤ 200 A/μs, V_{DD} ≤ BV_{DSS}, Starting T_J = 25°C.
4. Essentially independent of operating temperature.

Typical Performance Characteristics

Figure 1. On-Region Characteristics

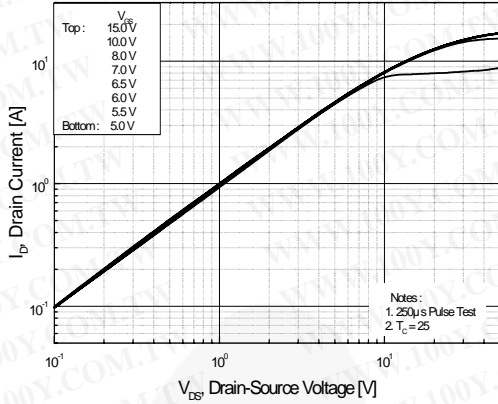


Figure 2. Transfer Characteristics

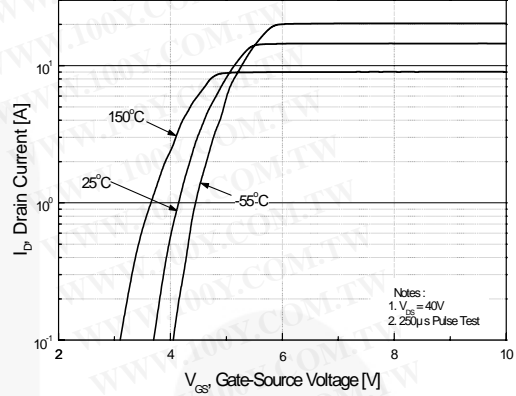


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

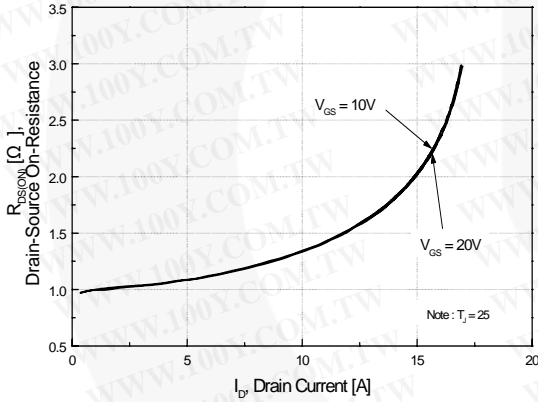


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

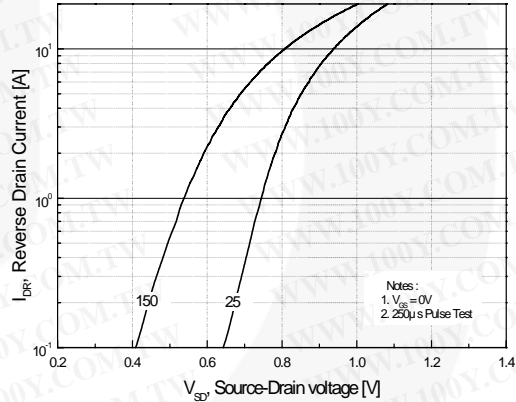


Figure 5. Capacitance Characteristics

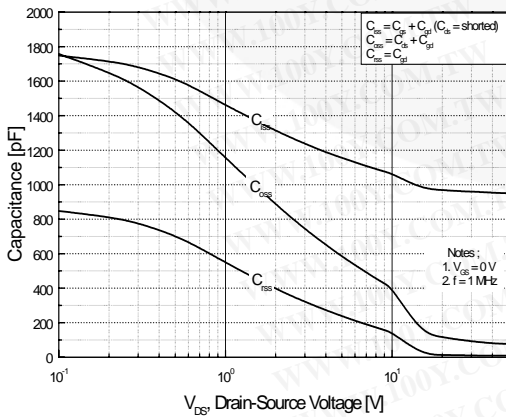
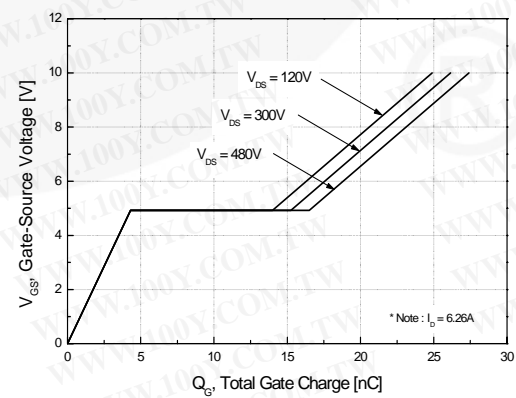


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

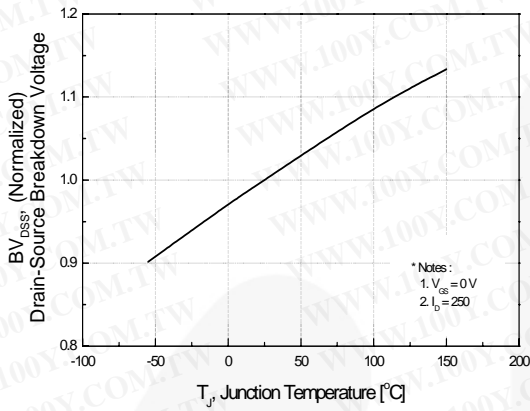


Figure 8. On-Resistance Variation vs. Temperature

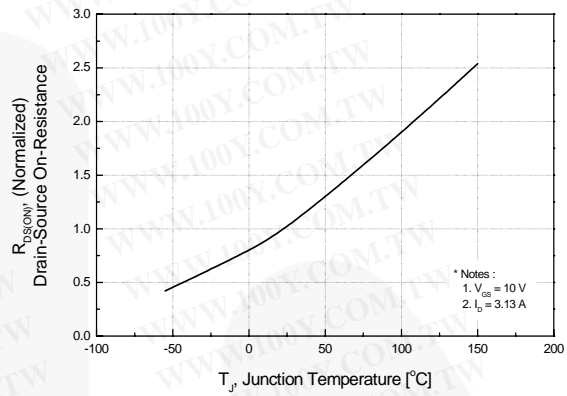


Figure 9. Maximum Safe Operating Area

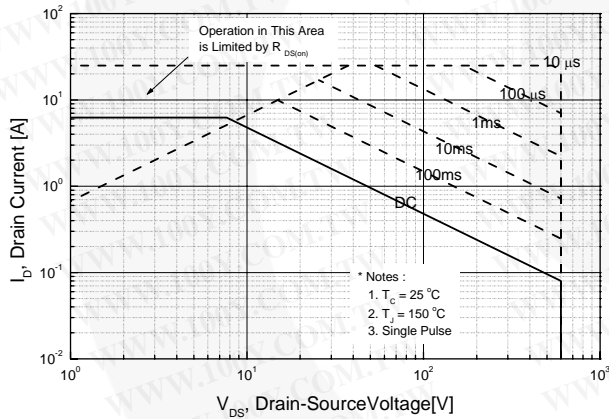


Figure 10. Maximum Drain Current vs. Case Temperature

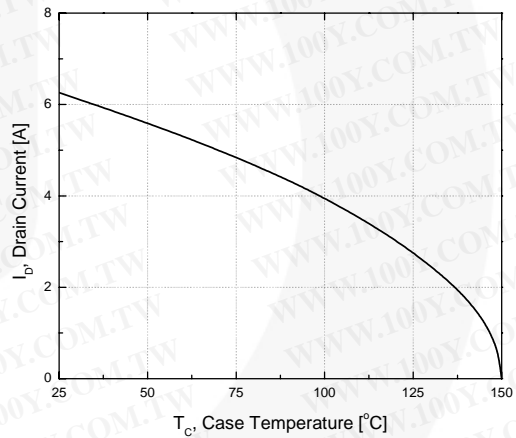
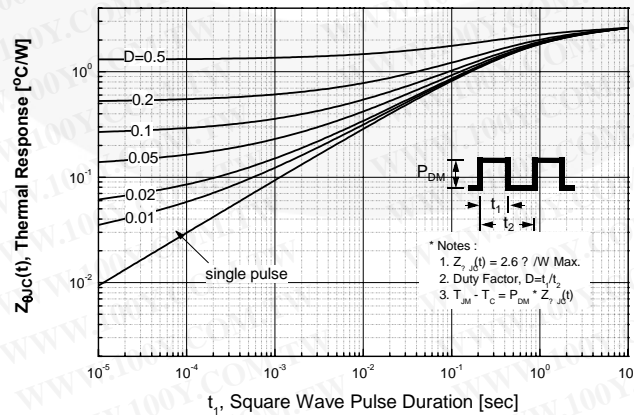


Figure 11. Transient Thermal Response Curve



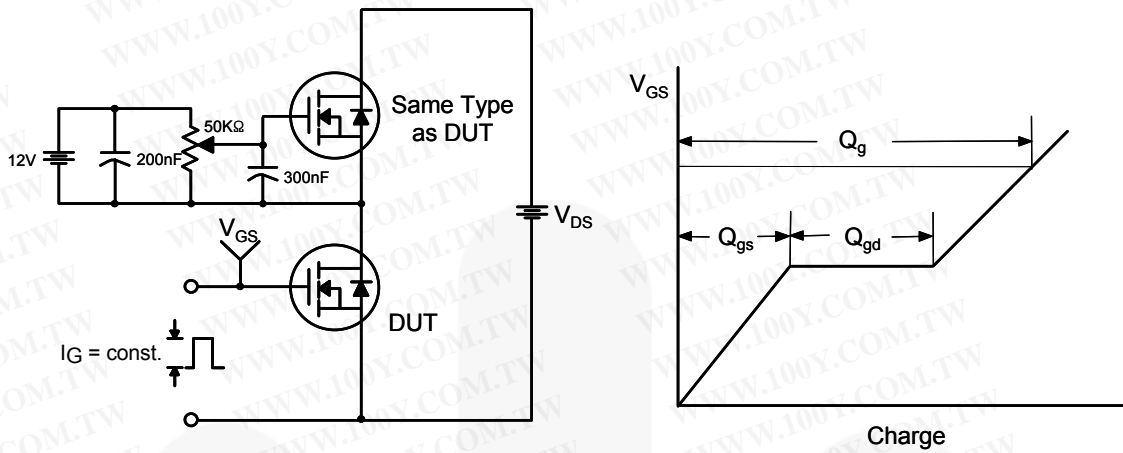


Figure 12. Gate Charge Test Circuit & Waveform

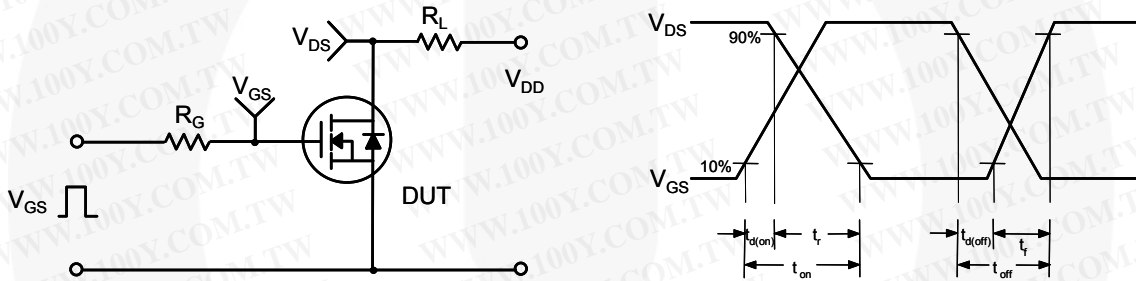


Figure 13. Resistive Switching Test Circuit & Waveforms

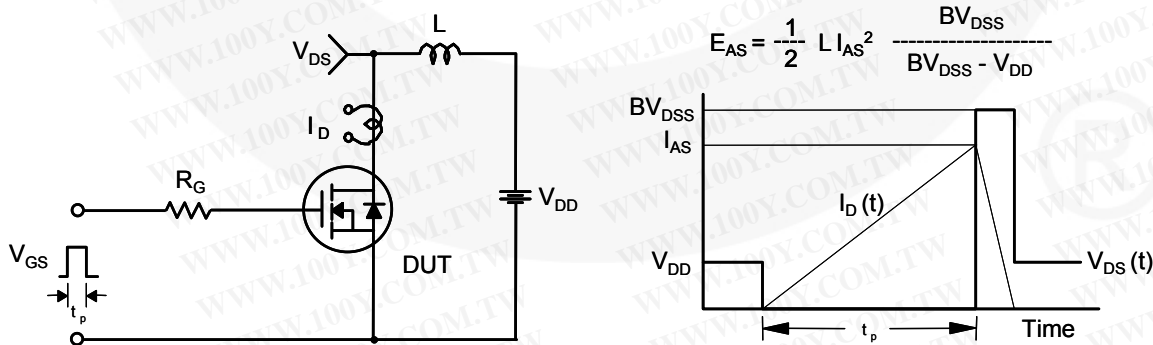


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms

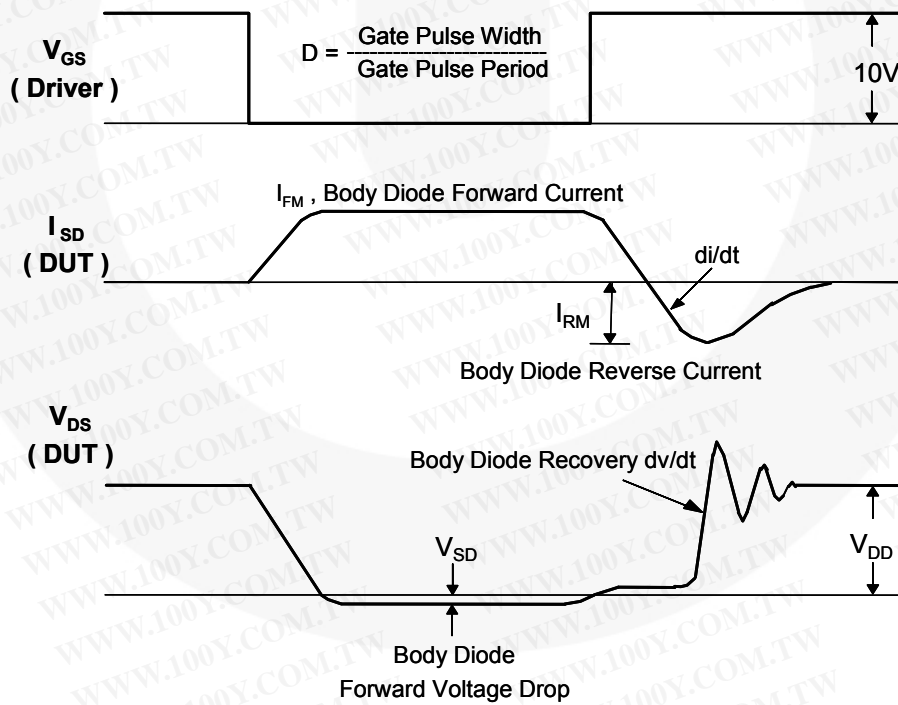
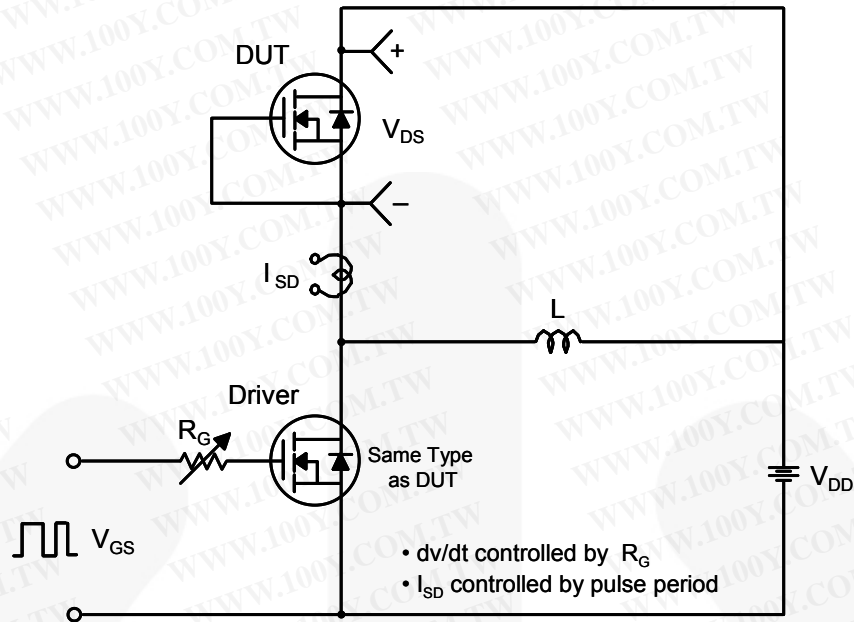
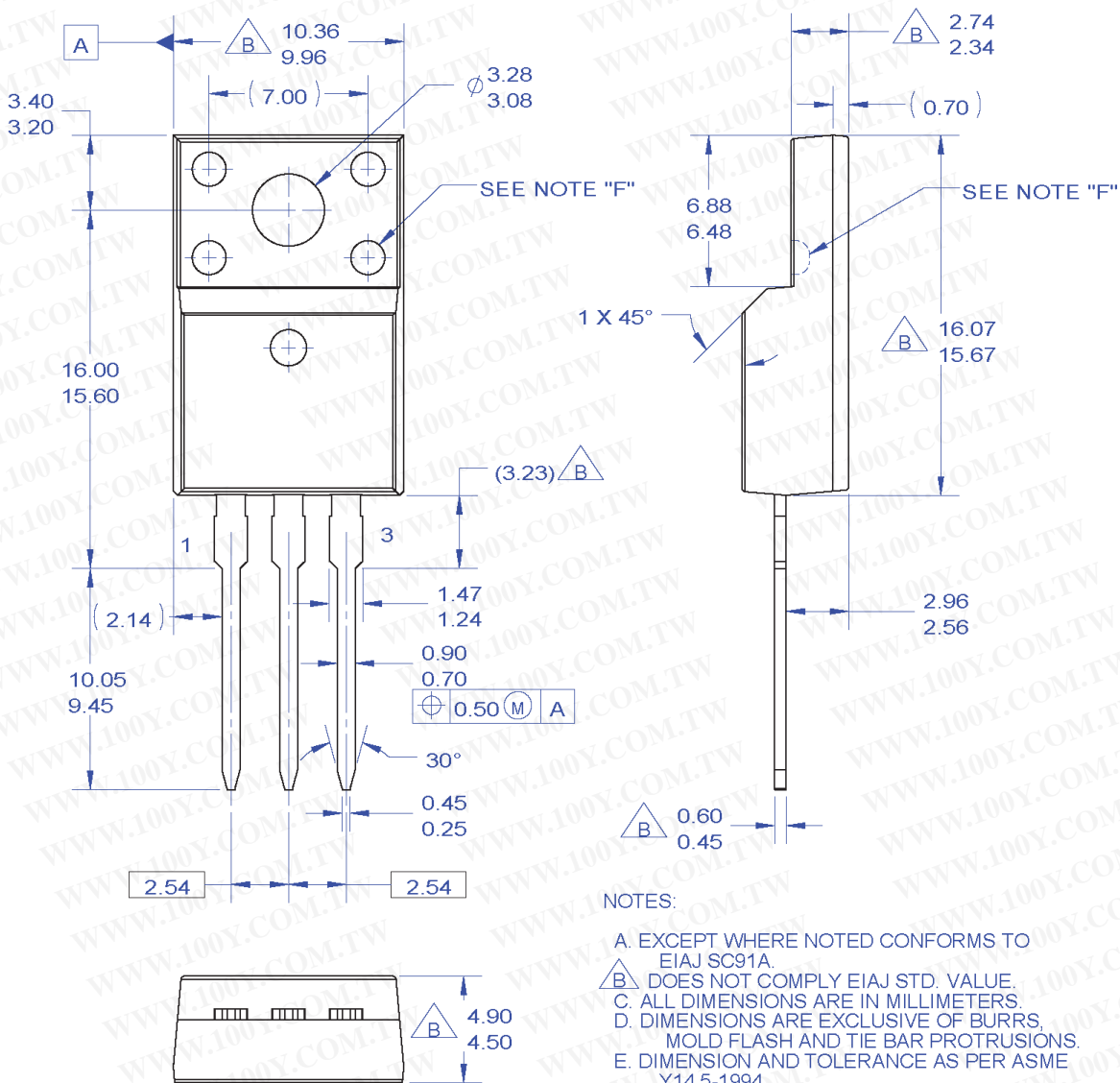


Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms

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Mechanical Dimensions



NOTES:

- A. EXCEPT WHERE NOTED CONFORMS TO EIAJ SC91A.
- B. DOES NOT COMPLY EIAJ STD. VALUE.
- C. ALL DIMENSIONS ARE IN MILLIMETERS.
- D. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUSIONS.
- E. DIMENSION AND TOLERANCE AS PER ASME Y14.5-1994.
- F. OPTION 1 - WITH SUPPORT PIN HOLE.
OPTION 2 - NO SUPPORT PIN HOLE.
- G. DRAWING FILE NAME: TO220M03REV3

Figure 16. TO220, Molded, 3-Lead, Full Pack, EIAJ SC91, Straight Lead

Package drawings are provided as a service to customers considering Fairchild components. Drawings may change in any manner without notice. Please note the revision and/or date on the drawing and contact a Fairchild Semiconductor representative to verify or obtain the most recent revision. Package specifications do not expand the terms of Fairchild's worldwide terms and conditions, specifically the warranty therein, which covers Fairchild products.

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