# DATA SHEET



# MOS FIELD EFFECT TRANSISTOR

勝特力電材超市-龍山店 886-3-5773766 勝特力電材超市-光復店 886-3-5729570 胜特力电子(上海) 86-21-34970699 胜特力电子(深圳) 86-755-83298787 http://www.100y.com.tw

2SK2141

#### SWITCHING

# N-CHANNEL POWER MOSFET INDUSTRIAL USE

#### DESCRIPTION

The 2SK2141 is N-channel Power M 0S Field Effect Transistor designed for high voltage switching applications.

#### FEATURES

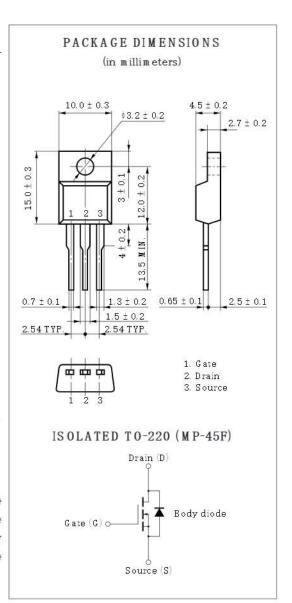
- Low On-state Resistance  $R_{DS(on)} = 1.1 \Omega MAX. (V_{GS} = 10 V, I_{D} = 3.0 A)$
- Low Ciss Ciss = 1150 pF TYP.
- High Avalanche Capability Ratings
- · Isolated TO-220 (MP-45F) Package

#### ABSOLUTE MAXIMUM RATINGS (TA = 25 °C)

Drain to Source Voltage	VDSS	600	V
Gate to Source Voltage	Vess	±30	V
Drain Current (DC)	ID (DC)	±6.0	A
Drain Current (pulse)	ID (pulse)*	±24	A
Total Power Dissipation (Tc = 25 °C)	Ртı	35	W
Total Power Dissipation ( $T_a = 25 \text{ °C}$ )	Рта	2.0	$\mathbb{W}$
Storage Temperature	Tstg -5	5 to +150	°C
Channel Temperature	T ch	150	°C
Single Avalanche Current	las**	6.0	A
Single Avalanche Energy	Eas**	12	шJ
*P W ≤ 10 αs, Duty Cycle ≤ 1%			

The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device is actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

\*\*Starting T<sub>ch</sub> = 25 °C, R<sub>G</sub> = 25  $\Omega$ , V<sub>GS</sub> = 20 V  $\rightarrow$  0



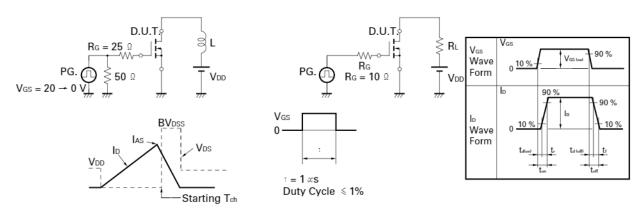


## ELECTRICAL CHARACTERISTICS (TA = 25 °C)

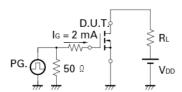
CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Drain to Source On-state Resistance	RDS(on)		0.8	1.1	Ω	Vgs = 10 V, ID = 3.0 A
Gate to Source Cutoff Voltage	V <sub>GS(off)</sub>	2.5		3.5	V	VDS = 10 V, ID = 1 mA
Forward Transfer Admittance	□ <b>y</b> fs □	2.0			S	VDS = 10 V, ID = 3.0 A
Drain Leakage Current	IDSS			100	αA	VDS = 600V, VGS = 0
Gate to Source Leakage Current	Igss			±100	nA	Vgs = ±30 V, Vps = 0
Input Capacitance	Ciss		1150		pF	V <sub>DS</sub> = 10 V
Output Capacitance	Coss		260		pF	Vgs = 0
Reverse Transfer Capacitance	Crss		60		pF	f = 1 MHz
Turn-On Delay Time	td(on)		15		ns	Vgs = 10 V
Rise Time	tr		15		ns	V <sub>DD</sub> = 150 V
Turn-Off Delay Time	td(off)		75		ns	ID = 3.0 A, Rg = 10 Ω
Fall Time	<b>t</b> f		13		ns	R <sub>L</sub> = 37.5 Ω
Total Gate Charge	Qg		40		nC	Vgs = 10 V
Gate to Source Charge	Qgs		6.0		nC	ID = 6.0 A
Gate to Drain Charge	QgD		20		nC	V <sub>DD</sub> = 480 V
Diode Forward Voltage	VF(S-D)		1.0		V	IF = 6.0 A, VGS = 0
Reverse Recovery Time	trr		370		ns	IF = 6.0 A
Reverse Recovery Charge	Qrr		1.5		αC	di/dt = 50 A/∝s

## **Test Circuit 1: Avalanche Capability**

**Test Circuit 2: Switching Time** 



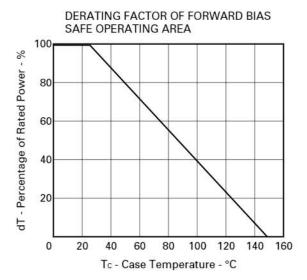
# Test Circuit 3: Gate Charge



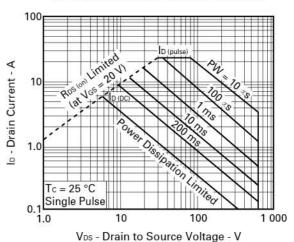
The application circuits and their parameters are for references only and are not intended for use in actual design-in's.



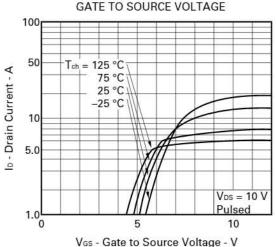
#### TYPICAL CHARACTERISTICS (TA = 25 °C)

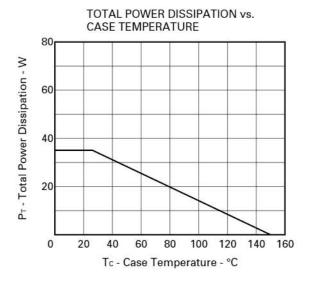


#### FORWARD BIAS SAFE OPERATING AREA

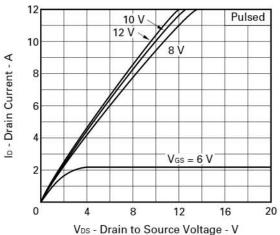


DRAIN CURRENT vs. GATE TO SOURCE VOLTAGE

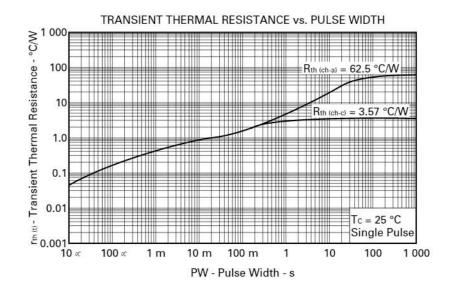


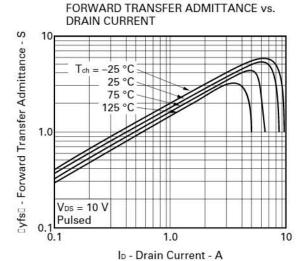


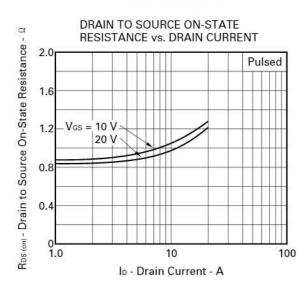
DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE

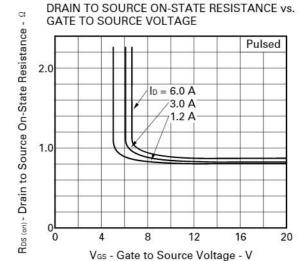


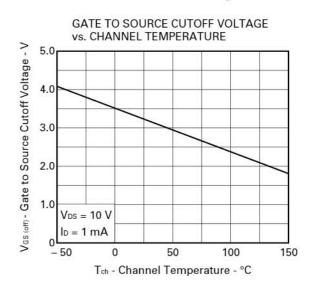




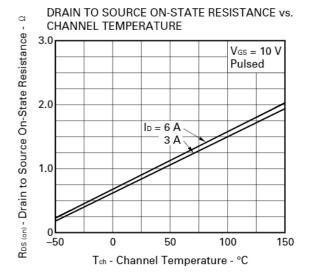


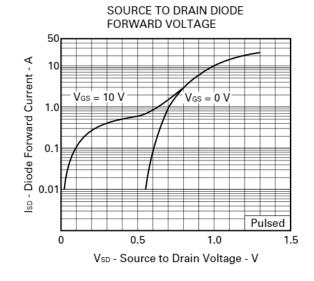


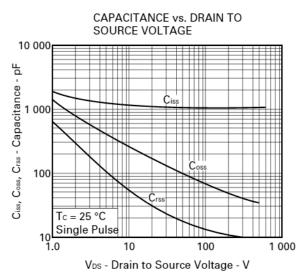


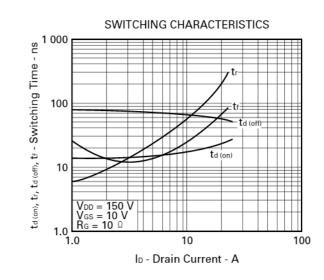


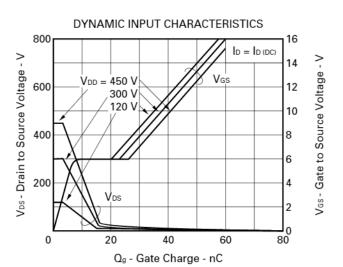


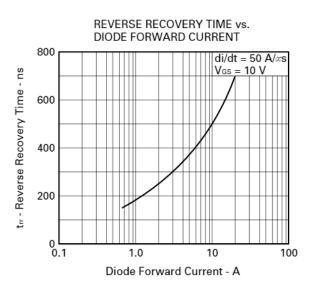








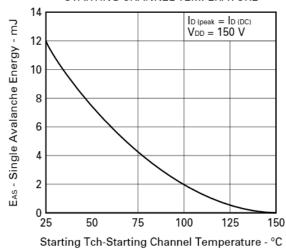






# SINGLE AVALANCHE CURRENT vs. INDUCTIVE LOAD The state of the state o

# SINGLE AVALANCHE ENERGY vs. STARTING CHANNEL TEMPERATURE





## REFERENCE

Document Name	Document No.
NEC semiconductor device reliability/quality control system.	TEI-1202
Quality grade on NEC semiconductor devices.	IEI-1209
Semiconductor device mounting technology manual.	IEI-1207
Semiconductor device package manual.	IEI-1213
Guide to quality assurance for semiconductor devices.	MEI-1202
Semiconductor selection guide.	MF-1134
Power MOS FET features and application switching power supply.	TEA-1034
Application circuits using Power MOS FET.	TEA-1035
Safe operating area of Power MOS FET.	TEA-1037

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Anti-radioactive design is not implemented in this product.

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