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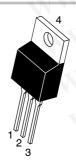
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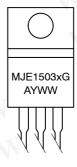
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# 4.0 AMPERES **POWER TRANSISTORS** COMPLEMENTARY SILICON 350 VOLTS, 50 WATTS



TO-220AB CASE 221A STYLE 1

#### **MARKING DIAGRAM**



MJE1503x **Device Code** x = 4 or 5Location Code

Year Work Week Pb-Free Package

### **ORDERING INFORMATION**

Device	Package	Shipping
MJE15034	TO-220AB	50 Units / Rail
MJE15034G	TO-220AB (Pb-Free)	50 Units / Rail
MJE15035	TO-220AB	50 Units / Rail
MJE15035G	TO-220AB (Pb-Free)	50 Units / Rail

# **Complementary Silicon Plastic Power Transistors**

# TO-220, NPN & PNP Devices

Complementary silicon plastic power transistors are designed for use as high-frequency drivers in audio amplifiers.

#### **Features**

- $h_{FE} = 100 \text{ (Min)} @ I_C = 0.5 \text{ Adc}$  $= 10 \text{ (Min)} @ I_{\text{C}} = 2.0 \text{ Adc}$
- Collector–Emitter Sustaining Voltage -

 $V_{CEO(sus)} = 350 \text{ Vdc (Min)} - \text{MJE}15034, \text{MJE}15035$ 

High Current Gain - Bandwidth Product

 $f_T = 30 \text{ MHz (Min)} @ I_C = 500 \text{ mAdc}$ 

- TO-220AB Compact Package
- Epoxy meets UL 94 V-0 @ 0.125 in
- ESD Ratings: Machine Model: C Human Body Model: 3B
- Pb-Free Packages are Available\*

#### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V <sub>CEO</sub>	350	Vdc
Collector-Base Voltage	V <sub>CB</sub>	350	Vdc
Emitter-Base Voltage	V <sub>EB</sub>	5.0	Vdc
Collector Current - Continuous - Peak	lc	4.0 8.0	Adc
Base Current	I <sub>B</sub>	1.0	Adc
Total Power Dissipation @ T <sub>C</sub> = 25°C Derate above 25°C	P <sub>D</sub>	50 0.40	W W/°C
Total Power Dissipation @ T <sub>A</sub> = 25°C Derate above 25°C	P <sub>D</sub>	2.0 0.016	W W/°C
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-65 to +150	°C

### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	2.5	°C/W
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	62.5	°C/W

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

### **ELECTRICAL CHARACTERISTICS** (T<sub>C</sub> = 25°C unless otherwise noted)

Characteristic		Symbol	Min	Max	Unit
OFF CHARACTERISTICS	TV V		. 003		
Collector-Emitter Sustaining Voltage (Note 1)	$(I_C = 10 \text{ mAdc}, I_B = 0)$	V <sub>CEO(sus)</sub>	350	-70	Vdc
Collector Cutoff Current	(V <sub>CB</sub> = 350 Vdc, I <sub>E</sub> = 0)	I <sub>CBO</sub>		10	μAdc
Emitter Cutoff Current	$(V_{BE} = 5.0 \text{ Vdc}, I_{C} = 0)$	I <sub>EBO</sub>	1.10.	10	μAdc
ON CHARACTERISTICS (Note 1)			- 10	U.Y.	
DC Current Gain	$ \begin{array}{l} (I_{C}=0.1 \; \text{Adc},  \text{V}_{CE}=5.0 \; \text{Vdc}) \\ (I_{C}=0.5 \; \text{Adc},  \text{V}_{CE}=5.0 \; \text{Vdc}) \\ (I_{C}=1.0 \; \text{Adc},  \text{V}_{CE}=5.0 \; \text{Vdc}) \\ (I_{C}=2.0 \; \text{Adc},  \text{V}_{CE}=5.0 \; \text{Vdc}) \end{array} $	h <sub>FE</sub>	100 100 50 10	00 <del>.</del> Y.	'C <sub>O</sub>
Collector-Emitter Saturation Voltage	(I <sub>C</sub> = 1.0 Adc, I <sub>B</sub> = 0.1 Adc)	V <sub>CE(sat)</sub>	- T ( )	0.5	Vdc
Base-Emitter On Voltage	$(I_C = 1.0 \text{ Adc}, V_{CE} = 5.0 \text{ Vdc})$	V <sub>BE(on)</sub>	11.	1.0	Vdc
DYNAMIC CHARACTERISTICS	COM		-33/	N.To.	-1
Current Gain – Bandwidth Product (Note 2) (I <sub>C</sub> = 500 mAdc, V <sub>CE</sub> = 10 Vdc, f <sub>test</sub> = 1.0 MHz)	T. Control	f <sub>T</sub>	30	- T 10	MHz

<sup>1.</sup> Pulse Test: Pulse Width  $\leq$  300  $\mu$ s, Duty Cycle  $\leq$  2.0%.

<sup>2.</sup>  $f_T = |h_{fe}| \cdot f_{test}$ .

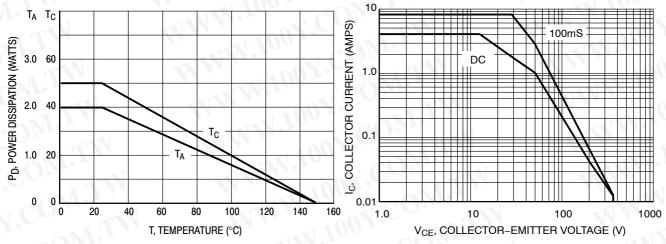


Figure 1. Power Derating

Figure 2. Active Region Safe Operating Area

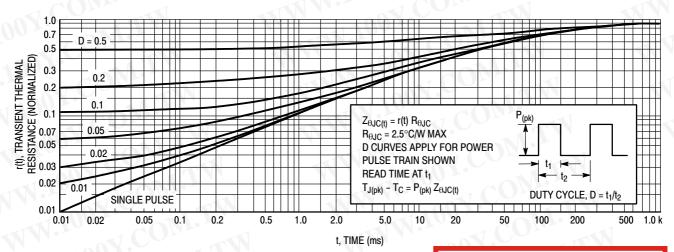


Figure 3. Thermal Response

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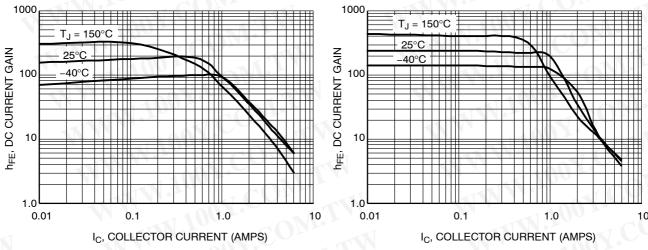


Figure 4. DC Current Gain, V<sub>CE</sub> = 5.0 V NPN MJE15034

Figure 5. DC Current Gain, V<sub>CE</sub> = 5.0 V PNP MJE15035

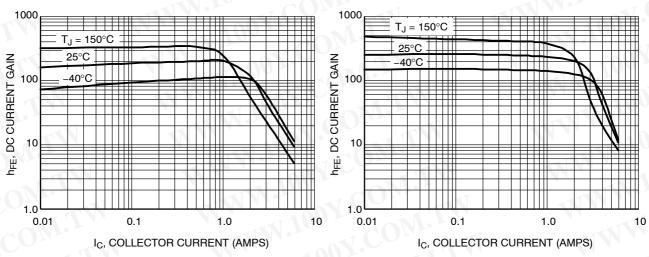


Figure 6. DC Current Gain, V<sub>CE</sub> = 20 V NPN MJE15034

Figure 7. DC Current Gain, V<sub>CE</sub> = 20 V PNP MJE15035

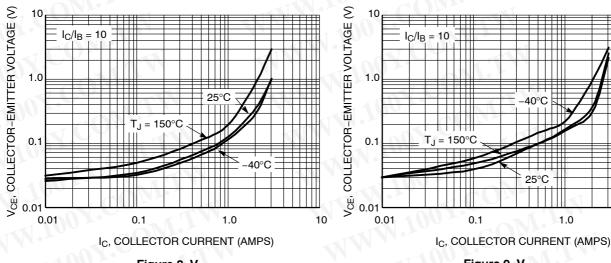


Figure 8. V<sub>CE(sat)</sub> NPN MJE15034

Figure 9. V<sub>CE(sat)</sub> PNP MJE15035

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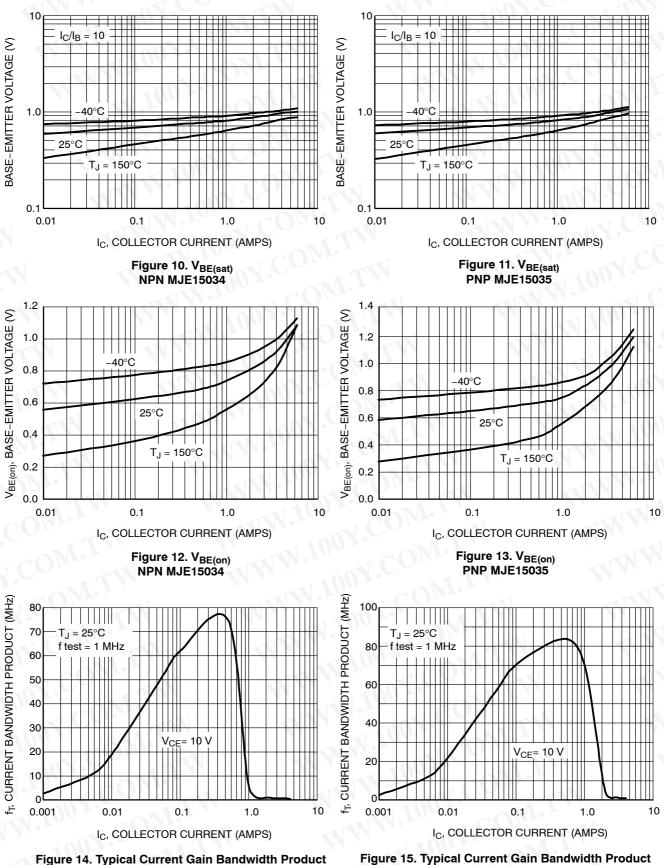


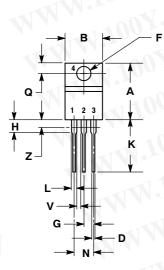
Figure 15. Typical Current Gain Bandwidth Product **PNP MJE15035** 

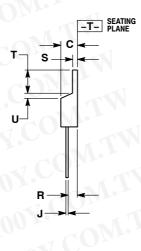
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**NPN MJE15034** 

#### PACKAGE DIMENSIONS

TO-220 CASE 221A-09 **ISSUE AG** 





#### NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: INCH.
- DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED

	INCHES		INCHES MILLIMETE		
DIM	MIN	MAX	MIN	MAX	]
Α	0.570	0.620	14.48	15.75	]
В	0.380	0.405	9.66	10.28	1
C	0.160	0.190	4.07	4.82	
D	0.025	0.036	0.64	0.91	]
F	0.142	0.161	3.61	4.09	]
G	0.095	0.105	2.42	2.66	]
Н	0.110	0.161	2.80	4.10	]
7	0.014	0.025	0.36	0.64	
K	0.500	0.562	12.70	14.27	]
L	0.045	0.060	1.15	1.52	]
N	0.190	0.210	4.83	5.33	7
Q	0.100	0.120	2.54	3.04	
R	0.080	0.110	2.04	2.79	
S	0.045	0.055	1.15	1.39	]
T	0.235	0.255	5.97	6.47	1
J	0.000	0.050	0.00	1.27	
٧	0.045		1.15	VV	]
Z		0.080	<u> </u>	2.04	]

STYLE 1: PIN 1. BASE

2.

COLLECTOR

**EMITTER** 

COLLECTOR

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