

# KA4558

## Dual Operational Amplifier

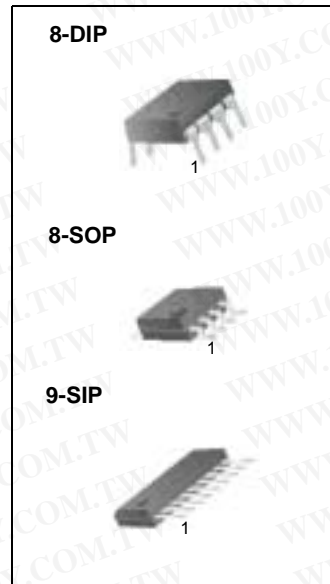
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### Features

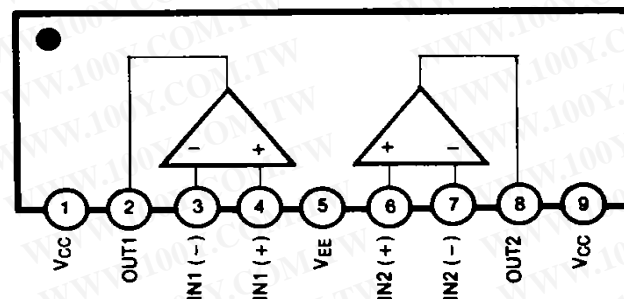
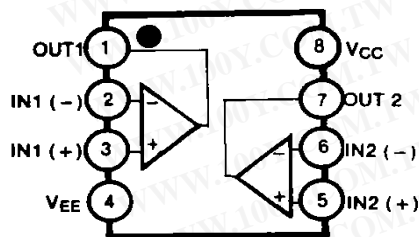
- No frequency compensation required.
- No latch up.
- Large common mode and differential voltage range.
- Parameter tracking over temperature range.
- Gain and phase match between amplifiers.
- Internally frequency compensated.
- Low noise input transistors.

### Descriptions

The KA4558 is a monolithic integrated circuit designed for dual operational amplifier.

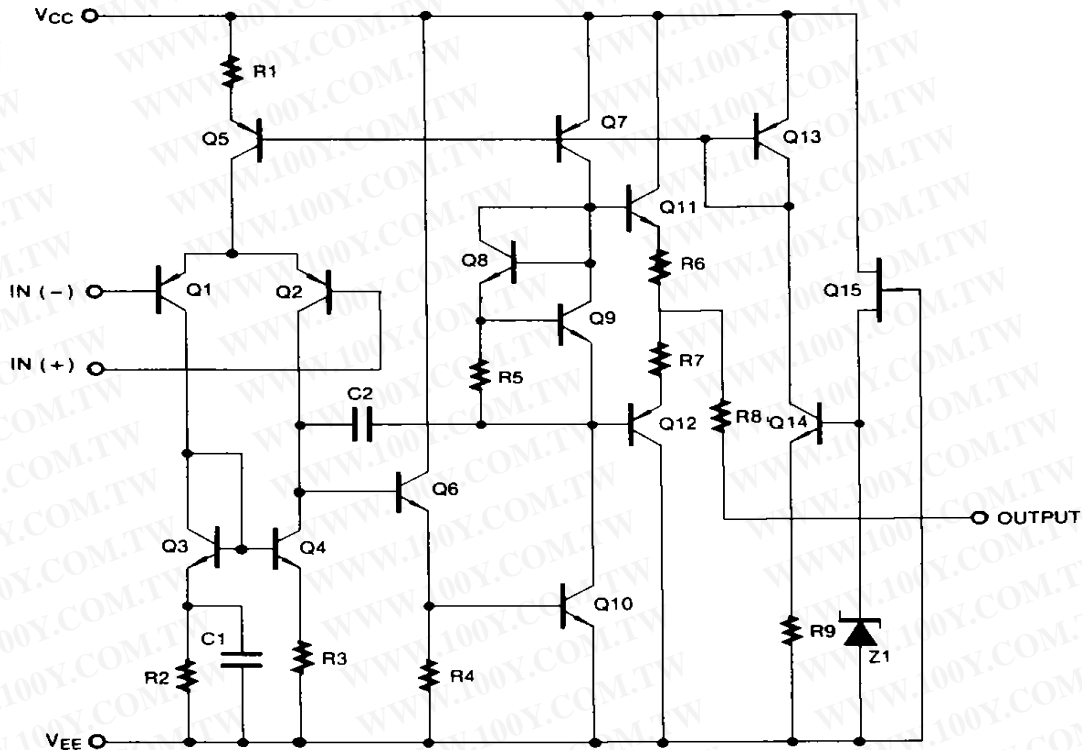


### Internal Block Diagram



## Schematic Diagram

(One Section Only)



## Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Supply Voltage	VCC	±22	V
Differential Input Voltage	VI(DIFF)	30	V
Input Voltage	VI	±15	V
Power Dissipation	PD	400	mW
Operating Temperature Range KA4558 KA4558I	TOPR	0 ~ 70 -40 ~ 85	°C
Storage Temperature Range	TSTG	-65 ~ 150	°C

## Electrical Characteristics

(VCC = 15V, VEE = - 15V, TA = 25 °C unless otherwise specified)

Parameter	Symbol	Conditions	KA4558/KA4558I			Unit
			Min	Typ	Max	
Input Offset Voltage	V <sub>IO</sub>	RS≤10KΩ	-	2	6	mV
		Note 1	-	-	7.5	
Input Offset Current	I <sub>IO</sub>		-	5	200	nA
		T <sub>A</sub> =T <sub>A</sub> (MAX)	-	-	300	
		T <sub>A</sub> =T <sub>A</sub> (MIN)	-	-	300	
Input Bias Current	I <sub>BIAS</sub>		-	30	500	nA
		T <sub>A</sub> =T <sub>A</sub> (MAX)	-	-	800	
		T <sub>A</sub> =T <sub>A</sub> (MIN)	-	-	800	
Large Signal Voltage Gain	G <sub>V</sub>	V <sub>O</sub> (P-P)= ±10V, R <sub>L</sub> ≤2KΩ	20	200	-	V/mV
		Note 1	-	-	-	
Common Mode Input Voltage Range	V <sub>I(R)</sub>		±12	±13	-	V
		Note 1	-	-	-	
Common Mode Rejection Ratio	CMRR	RS≤10KΩ	70	90	-	dB
		Note 1	-	-	-	
Supply Voltage Rejection Ratio	PSRR	RS≤10KΩ	76	90	-	dB
		Note 1	76	90	-	
Output Voltage Swing	V <sub>O</sub> (P-P)	R <sub>L</sub> ≥10KΩ	±12	±14	-	V
		R <sub>L</sub> ≥2KΩ	±10	±13	-	
Supply Current (Both Amplifiers)	I <sub>CC</sub>		-	3.5	5.8	mA
		T <sub>A</sub> =T <sub>A</sub> (MAX)	-	-	5.0	
		T <sub>A</sub> =T <sub>A</sub> (MIN)	-	-	6.7	
Power Consumption (Both Amplifiers)	P <sub>C</sub>		-	70	170	mW
		T <sub>A</sub> =T <sub>A</sub> (MAX)	-	-	150	
		T <sub>A</sub> = T <sub>A</sub> (MIN)	-	-	200	
Slew Rate (Note2)	SR	V <sub>I</sub> =10V, R <sub>L</sub> ≥2KΩ C <sub>I</sub> ≤100pF	1.2	-	-	V/μs
Rise Time (Note2)	T <sub>R</sub>	V <sub>I</sub> =20mV, R <sub>L</sub> ≥2KΩ C <sub>I</sub> ≤100pF	-	0.3	-	μs
Overshoot (Note2)	OS	V <sub>I</sub> =20mV, R <sub>L</sub> ≥2KΩ C <sub>I</sub> ≤100pF	-	15	-	%

### Note :

- KA4558 : T<sub>A</sub>(MIN) ≤T<sub>A</sub>≤T<sub>A</sub>(MAX) = 0≤T<sub>A</sub>≤ 70 °C , KA4558I : T<sub>A</sub>(MIN) ≤T<sub>A</sub>≤T<sub>A</sub>(MAX) = -40≤T<sub>A</sub>≤ +85 °C
- Guaranteed by design.

# Typical Performance Characteristics

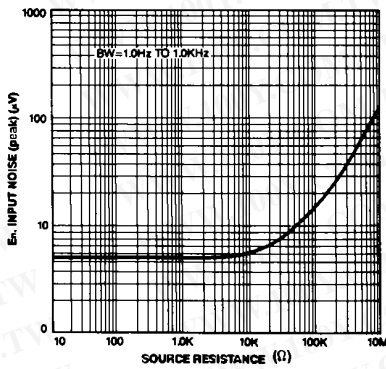


Figure 1. Burst Noise vs Source Resistance

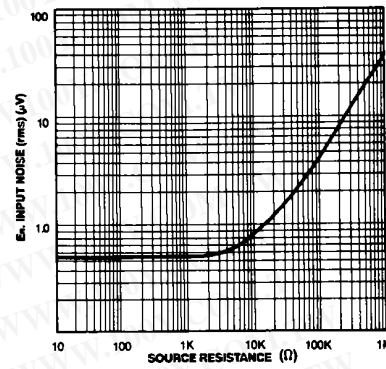


Figure 2. RMS Noise vs Source Resistance

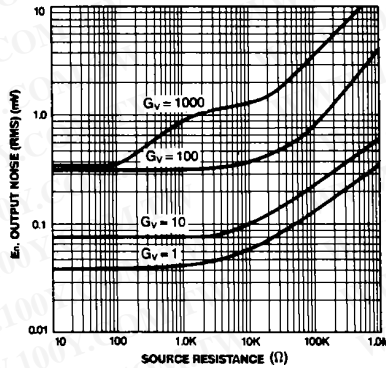


Figure 3. Output Noise vs Source Resistance

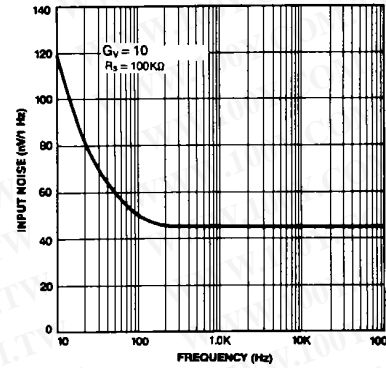


Figure 4. Spectral Noise Density

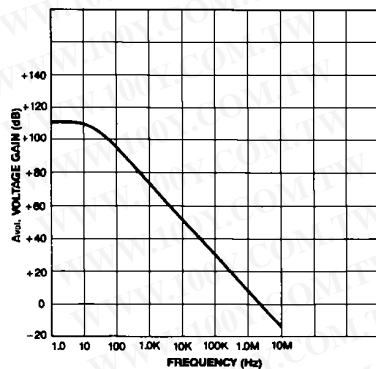


Figure 5. Open Loop Frequency Response

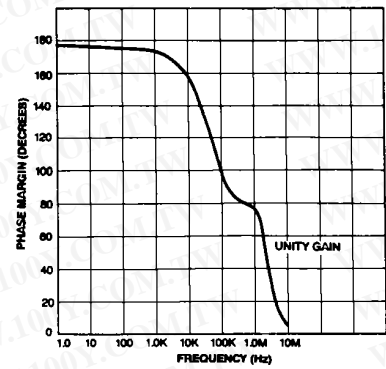


Figure 6. Phase Margin vs Frequency

## Typical Performance Characteristics (continued)

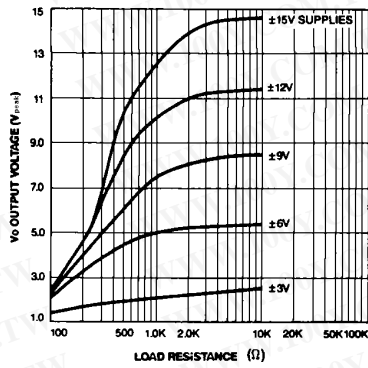


Figure 7. Positive Output Voltage Swing vs Load Resistance

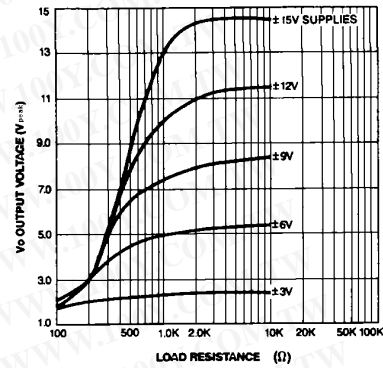


Figure 8. Negative Output Voltage Swing vs Load Resistance

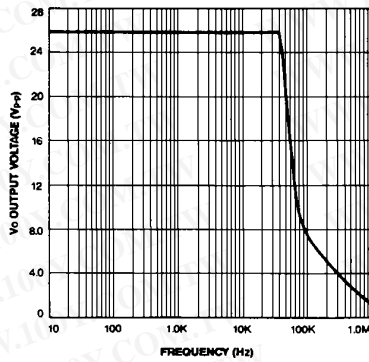
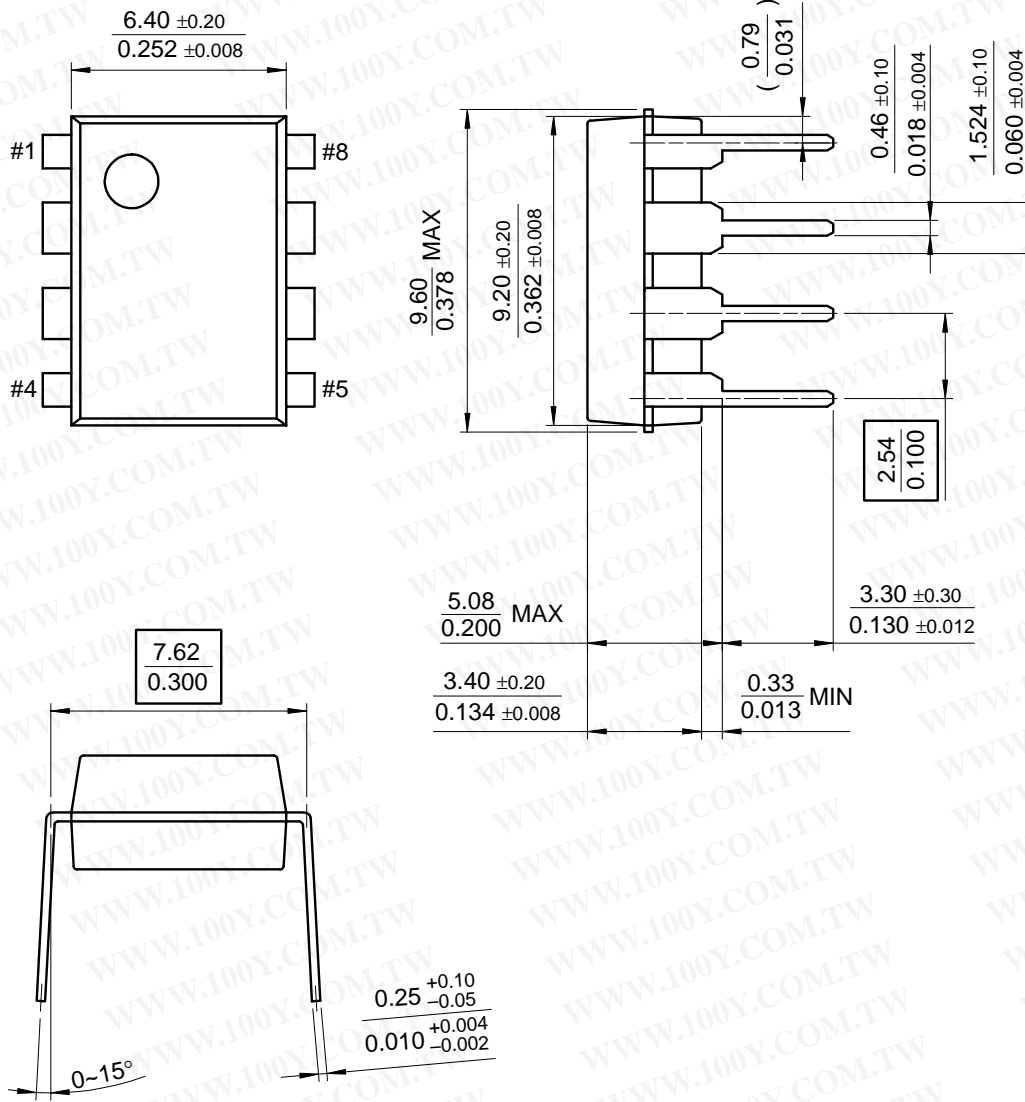


Figure 9. Power Bandwidth  
(Large Signal Output Swing vs Frequency)

# Mechanical Dimensions

## Package

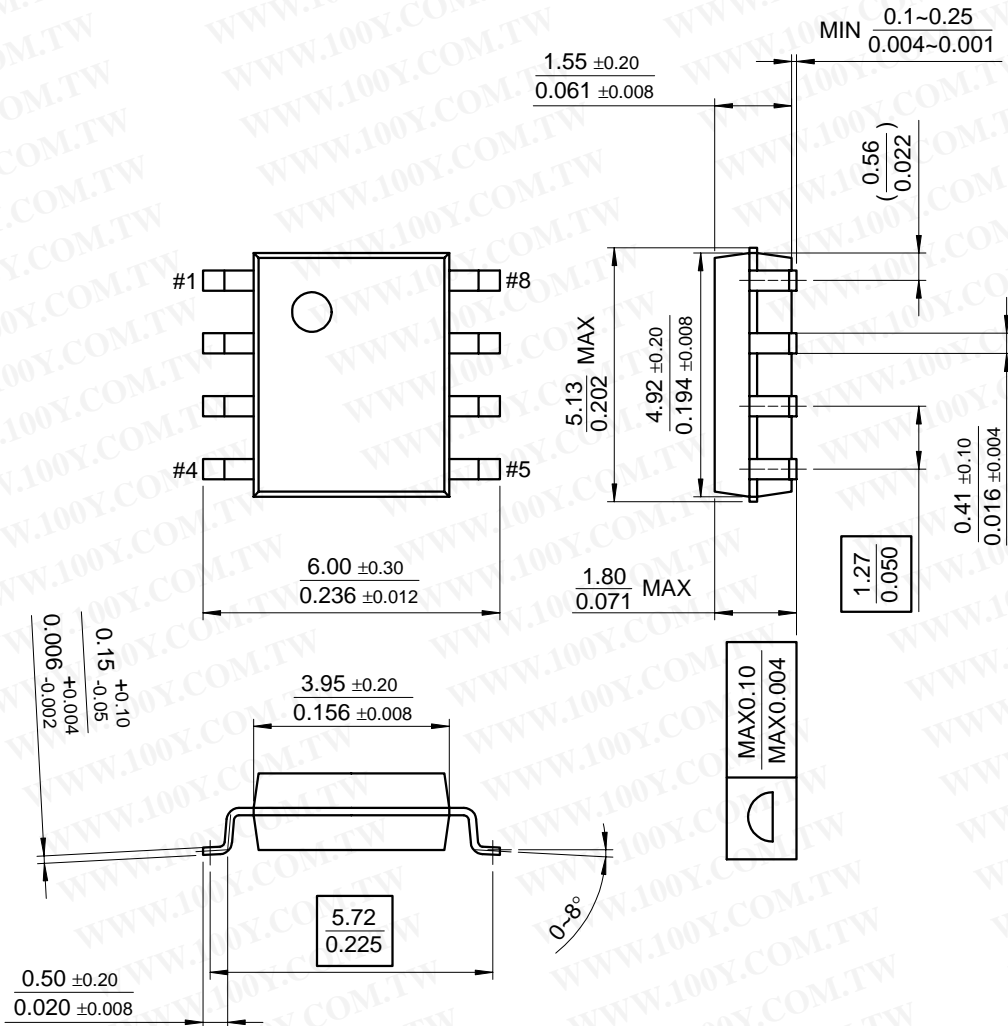
### 8-DIP



Mechanical Dimensions (Continued)

Package

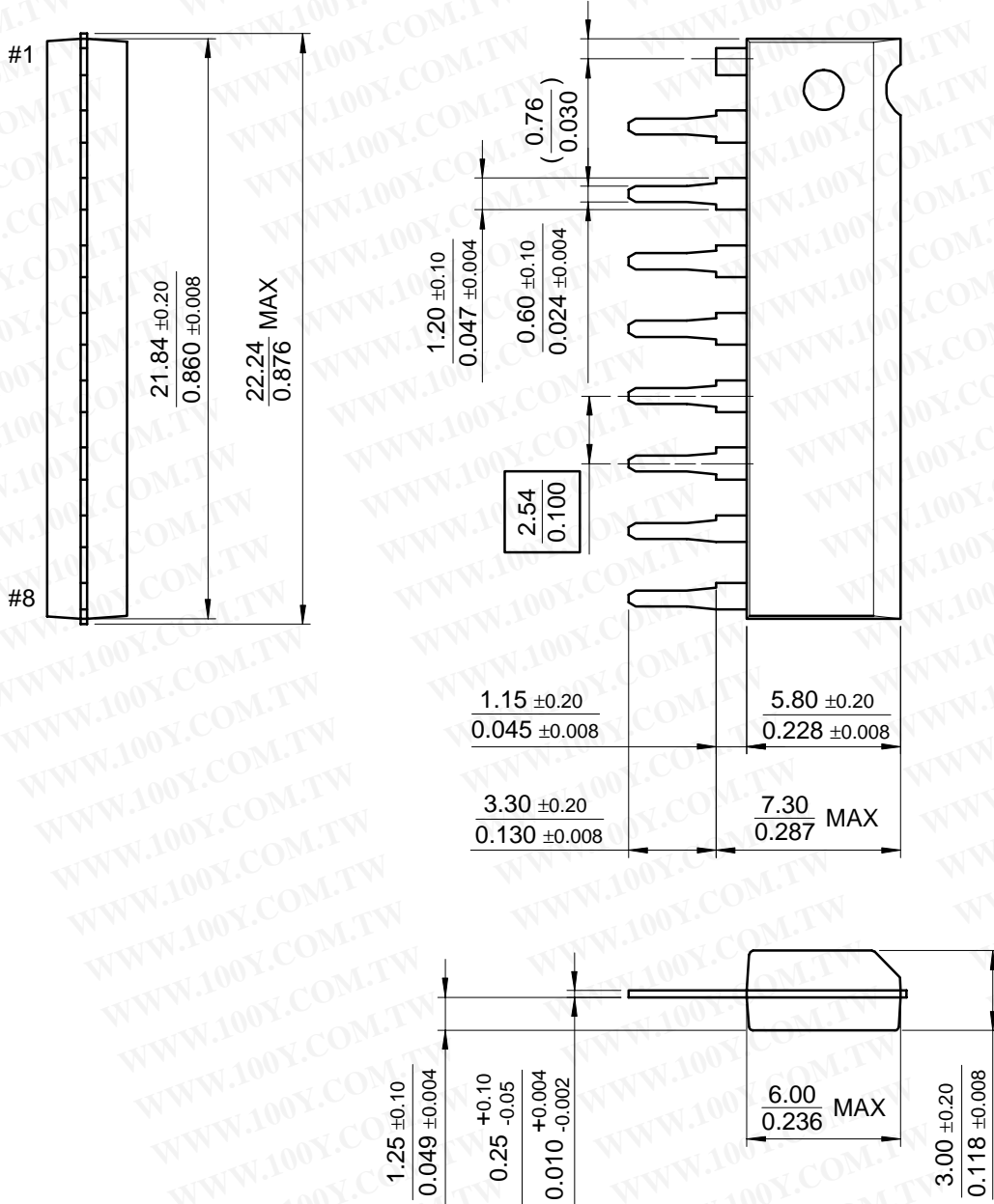
8-SOP



**Mechanical Dimensions** (Continued)

**Package**

**9-SIP**





**Ordering Information**

Product Number	Package	Operating Temperature
KA4558	8-DIP	0 ~ + 70°C
KA4558D	8-SOP	
KA4558S	9-SIP	
KA4558I	8-DIP	-40 ~ + 85°C

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