WWW.100Y.COM.TW COM.TW LM4250 Y.COM.TW

NW.100Y.COM.TW OOY.COM.TW LCOM.TW LM4250 Programmable Operational Amplifier

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LM4250

Programmable Operational Amplifier

General Description

The LM4250 and LM4250C are extremely versatile programmable monolithic operational amplifiers. A single external master bias current setting resistor programs the input bias current, input offset current, quiescent power consumption, slew rate, input noise, and the gain-bandwidth product. The device is a truly general purpose operational amplifier.

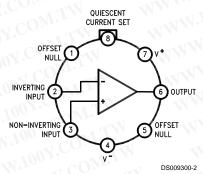
The LM4250C is identical to the LM4250 except that the LM4250C has its performance guaranteed over a 0°C to +70°C temperature range instead of the -55°C to +125°C temperature range of the LM4250.

Features

- ±1V to ±18V power supply operation
- 3 nA input offset current
- Standby power consumption as low as 500 nW
- No frequency compensation required
- Programmable electrical characteristics
- Offset voltage nulling capability
- Can be powered by two flashlight batteries
- Short circuit protection

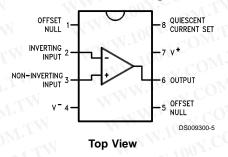
Connection Diagrams

Metal Can Package

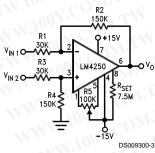


Top View

Dual-In-Line Package

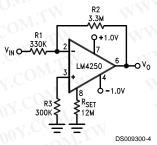


X5 Difference Amplifier



Quiescent $P_D = 0.6 \text{ mW}$

500 Nano-Watt X10 Amplifier



Quiescent P_D = 500 nW

Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

	LM4250	LM4250C
Supply Voltage	±18V	±18V
Operating Temp. Range	–55°C ≤ T _A ≤ +125°C	$0^{\circ}\text{C} \le \text{T}_{\text{A}} \le +70^{\circ}\text{C}$
Differential Input Voltage	±30V	±30V
Input Voltage (Note 2)	±15V	±15V
I _{SET} Current	150 nA	150 nA
Output Short Circuit Duration	Continuous	Continuous
T_{JMAX}		
H-Package	150°C	100°C
N-Package		100°C
J-Package	150°C	100°C
M-Package		100°C
Power Dissipation at T _A = 25°C		
H-Package (Still Air)	500 mW	300 mW
(400 LF/Min Air Flow)	1200 mW	1200 mW
N-Package		500 mW
J-Package	1000 mW	600 mW
M-Package		350 mW
Thermal Resistance (Typical) θ_{JA}		
H-Package (Still Air)	165°C/W	165°C/W
(400 LF/Min Air Flow)	65°C/W	65°C/W
N-Package		130°C/W
J-Package	108°C/W	108°C/W
M-Package		190°C/W
(Typical) θ_{JC}		
H-Package	21°C/W	21°C/W
Storage Temperature Range	-65°C to +150°C	−65°C to +150°C
WW 1007.Com.TW		
Soldering Information		
Dual-In-Line Package	360°C	
Soldering (10 seconds) Small Outline Package	260°C	
Vapor Phase (60 seconds)	215°C	
1 3 7 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1		

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Infrared (15 seconds)

See AN-450 "Surface Mounting Methods and Their Effect on Product Reliability" for other methods of colders surface mount devices.

ESD tolerance (Note 4)

Note 1: "Absolute Maximum Ratings" indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not guarantee specific performance limits.

Note 2: For supply voltages less than ±15V, the absolute maximum input voltage is equal to the supply voltage.

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Note 3: Refer to RETS4250X for military specifications. Note 4: Human body model, 1.5 k Ω in series with 100 pF.

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Resistor Biasing Set Current Setting Resistor to V-

		Is	SET		
Vs	0.1 μΑ	0.5 μΑ	1.0 μΑ	5 μΑ	10 μΑ
1.5V	25.6 MΩ	5.04 MΩ	2.5 MΩ	492 kΩ	244 kΩ
3.0V	55.6 MΩ	11.0 MΩ	5.5 MΩ	1.09 MΩ	544 kΩ
6.0V	116 MΩ	23.0 MΩ	11.5 MΩ	2.29 MΩ	1.14 ΜΩ
9.0V	176 MΩ	35.0 MΩ	17.5 MΩ	3.49 MΩ	1.74 MΩ
12.0V	236 ΜΩ	47.0 MΩ	23.5 MΩ	4.69 MΩ	2.34 MΩ
15.0V	296 ΜΩ	59.0 MΩ	29.5 MΩ	5.89 MΩ	2.94 MΩ

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Electrical Characteristics
LM4250 (-55°C < T. < +125°C LM4250 (-55°C \leq T_A \leq +125°C unless otherwise specified.) T_A = T_J

	M.M. CO.	V _s =		: ±1.5V	
Parameter	Conditions	I _{SET} = 1 μA		I _{SET} = 10 μA	
100Y. CM.TW	W. W. 100X. CM.TW	Min	Max	Min	Max
Vos	$R_S \le 100 \text{ k}\Omega, T_A = 25^{\circ}\text{C}$	1	3 mV	1.0	5 mV
los COMP	$T_A = 25^{\circ}C$	N -	3 nA	M.Com	10 nA
l _{bias}	$T_A = 25^{\circ}C$	_ 1	7.5 nA	ST CONT	50 nA
Large Signal Voltage	$R_L = 100 \text{ k}\Omega, T_A = 25^{\circ}\text{C}$	40k	WW.	ONECON	CIN
Gain	$V_O = \pm 0.6V$, $R_L = 10 \text{ k}\Omega$			50k	00 1
Supply Current	$T_A = 25^{\circ}C$	TVI	7.5 µA	1001	80 μΑ
Power Consumption	$T_A = 25^{\circ}C$	11	23 µW	V. TC	240 µW
Vos	$R_S \le 100 \text{ k}\Omega$	M.T.W	4 mV	W.100 1.	6 mV
l _{os}	$T_A = +125^{\circ}C$	WILL	5 nA	1007.	10 nA
	$T_A = -55^{\circ}C$	OM.	3 nA	W. John	10 nA
l _{bias}	W. 100 P.	COMIT	7.5 nA	MW.100	50 nA
Input Voltage Range	TW 100%	±0.6V		±0.6V	Mos
Large Signal Voltage Gain	$V_O = \pm 0.5 V$, $R_L = 100 \text{ k}\Omega$ $R_L = 10 \text{ k}\Omega$	30k	N v	30k	N.CON
Output Voltage Swing	$R_{L} = 100 \text{ k}\Omega$ $R_{L} = 10 \text{ k}\Omega$	±0.6V	TW	±0.6V	100X.CO
Common Mode Rejection Ratio	$R_S \le 10 \text{ k}\Omega$	70 dB	WT.	70 dB	100 Y.C
Supply Voltage Rejection Ratio	$R_S \le 10 \text{ k}\Omega$	76 dB	1. ·	76 dB	.10
Supply Current	W.T.	1002.	8 μΑ	-11	90 μA

	W.T.W	W.100 r.	V _s =	±15V	
Parameter	Conditions	I _{SET} = 1 μA		I _{SET} = 10 μA	
	CONT.	Min	Max	Min	Max
Vos	$R_S \le 100 \text{ k}\Omega, T_A = 25^{\circ}\text{C}$	WW.Inc	3 mV	1	5 mV
I _{os}	$T_A = 25^{\circ}C$	100	3 nA		10 nA
I _{bias}	$T_A = 25^{\circ}C$	1111	7.5 nA		50 nA
Large Signal Voltage	$R_L = 100 \text{ k}\Omega, T_A = 25^{\circ}\text{C}$	100k	ON CON	TV	MM.
Gain	$V_O = \pm 10V$, $R_L = 10 \text{ k}\Omega$	W.11	COM	100k	
Supply Current	$T_A = 25^{\circ}C$	11	10 μΑ	1.1.11	90 μΑ
Power Consumption	$T_A = 25^{\circ}C$	MM	300 µW	WILL	2.7 mW
Vos	R _S ≤ 100 kΩ	WWW	4 mV	TW	6 mV
l _{os}	$T_A = +125^{\circ}C$		25 nA	M. I	25 nA
	$T_A = -55^{\circ}C$	MAI	3 nA	WIIN	10 nA
l _{bias}	M. COM	WW	7.5 nA	WT	50 nA
Input Voltage Range	MM.In. COM.	±13.5V	W.Io	±13.5V	

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Electrical Characteristics (Continued)

	WWW.I		V _s =	s = ±15V	
Parameter	Conditions	I _{SET} = 1 μA		I _{SET} = 10 μA	
	MITH WWW. III	Min	Max	Min	Max
Large Signal Voltage	$V_{O} = \pm 10V, R_{L} = 100 \text{ k}\Omega$	50k	TW		
Gain	$R_L = 10 \text{ k}\Omega$			50k	
Output Voltage Swing	$R_L = 100 \text{ k}\Omega$	±12V	1.1		
	$R_L = 10 \text{ k}\Omega$		WIIM	±12V	
Common Mode Rejection Ratio	$R_S \le 10 \text{ k}\Omega$	70 dB	WT	70 dB	
Supply Voltage Rejection Ratio	R _S ≤ 10 kΩ	76 dB	OM	76 dB	
Supply Current	T. COMITINI	W.100 r.	11 µA		100 µA
Power Consumption	W. W.	1003	330 µW		3 mW

THE WAY	1001. COM: IV		$V_S = \pm 1.5V$		
Parameter	Conditions	I _{SET} = 1 μA		I _{SET} = 10 μA	
	N. TO COM.	Min	Max	Min	Max
Vos	$R_S \le 100 \text{ k}\Omega, T_A = 25^{\circ}\text{C}$	WW	5 mV	VI.	6 mV
los	$T_A = 25^{\circ}C$	111	6 nA	Mil	20 nA
bias W	$T_A = 25^{\circ}C$	MM.	10 nA	WILL	75 nA
Large Signal Voltage Gain	$R_L = 100 \text{ k}\Omega, T_A = 25^{\circ}\text{C}$ $V_O = \pm 0.6\text{V}, R_L = 10 \text{ k}\Omega$	25k	W.100Y.C	25k	ſ
Supply Current	$T_A = 25^{\circ}C$		8 μΑ	COM.II	90 µA
Power Consumption	$T_A = 25^{\circ}C$	W W	24 µW	TIME	270 μV
Vos	$R_S \le 10 \text{ k}\Omega$	di 🔻	6.5 mV	V.COB	7.5 mV
os	M. IOO. COM.		8 nA	COM.	25 nA
bias	WW 31007.0		10 nA	MO.	80 nA
Input Voltage Range	MAN	±0.6V	MM	±0.6V	NT.
Large Signal Voltage Gain	$V_O = \pm 0.5 V$, $R_L = 100 \text{ k}\Omega$ $R_L = 10 \text{ k}\Omega$	25k	MMM	25k	M.TW
Output Voltage Swing	$R_{L} = 100 \text{ k}\Omega$ $R_{L} = 10 \text{ k}\Omega$	±0.6V	MM	±0.6V	OMITY
Common Mode Rejection Ratio	$R_S \le 10 \text{ k}\Omega$	70 dB	TAT VI	70 dB	COM
Supply Voltage Rejection Ratio	$R_S \le 10 \text{ k}\Omega$	74 dB	77	74 dB	COM.
Supply Current	LA MA 100X	WILL	8 μΑ	1007	90 µA
Power Consumption	WWW.	COM.	24 μW 🦪	MM	270 μV

	WWW.I	V.CO	±15V		
Parameter	Conditions	I _{SET} = 1 μA		I _{SET} = 10 μA	
	ONITH WY	Min	Max	Min	Max
Vos	$R_S \le 100 \text{ k}\Omega, T_A = 25^{\circ}\text{C}$	100 Y.	5 mV	M. M.	6 mV
los	$T_A = 25^{\circ}C$	· CO	6 nA	MM	20 nA
I _{bias}	$T_A = 25^{\circ}C$	1.100 TCC	10 nA		75 nA
Large Signal Voltage	$R_L = 100 \text{ k}\Omega, T_A = 25^{\circ}\text{C}$	60k	M_{JJM}	V	W.100 .
Gain	$V_{O} = \pm 10V, R_{L} = 10 \text{ k}\Omega$	100X.C	WILL	60k	
Supply Current	$T_A = 25^{\circ}C$	W. L	11 µA	W	100 µA
Power Consumption	$T_A = 25^{\circ}C$	M.100	330 µW		3 mW
Vos	R _S ≤ 100 kΩ	1007	6.5 mV		7.5 mV
los	NY.CON TW	100	8 nA		25 nA
I _{bias}	TONI.	TIWW.I	10 nA		80 nA

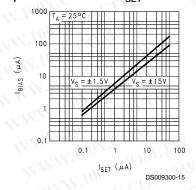
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Electrical Characteristics (Continued)

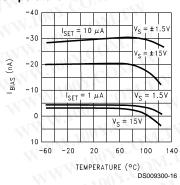
	COM.	$V_s = \pm 15V$			
Parameter (1997)	Conditions	I _{SET} = 1 μA		I _{SET} = 10 μA	
	V.COMITY W	Min Max	Min	Max	
Input Voltage Range	V.COM	±13.5V	I.Co.	±13.5V	
Large Signal Voltage	$V_{O} = \pm 10V, R_{L} = 100 \text{ k}\Omega$	50k	A COM.	CVN	
Gain	$R_L = 10 \text{ k}\Omega$	W .10	OM	50k	
Output Voltage Swing	$R_L = 100 \text{ k}\Omega$	±12V	001.	TIM	
	$R_L = 10 \text{ k}\Omega$	MMM.	OUN.COL	±12V	
Common Mode Rejection Ratio	$R_S \le 10 \text{ k}\Omega$	70 dB	TO CO	70 dB	
Supply Voltage Rejection Ratio	$R_S \le 10 \text{ k}\Omega$	74 dB	1.100 .	74 dB	
Supply Current	TY	MA	11 µA	TILL	100 μΑ
Power Consumption	IVI. CONTRA	WW	330 µW	TW	3 mW

Typical Performance Characteristics

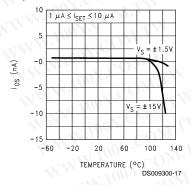
Input Bias Current vs I_{SET}



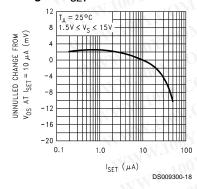
Input Bias Current vs Temperature



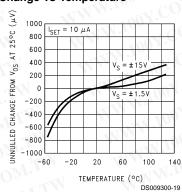
Input Offset Current vs Temperature



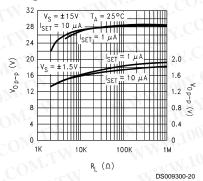
Unnulled Input Offset Voltage Change vs I_{SET}



Unnulled Input Offset Voltage Change vs Temperature

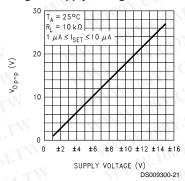


Peak to Peak Output Voltage Swing vs Load Resistance

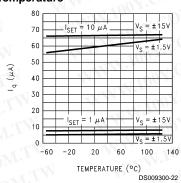


Typical Performance Characteristics (Continued)

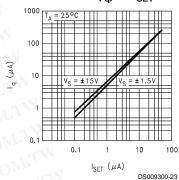
Peak to Peak Output Voltage Swing vs Supply Voltage



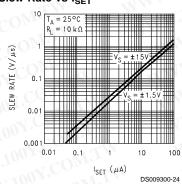
Quiescent Current (I_q) vs Temperature



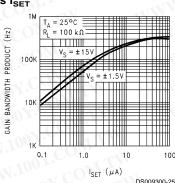
Quiescent Current (Iq) vs I_{SET}



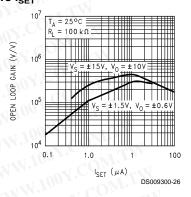
Slew Rate vs I_{SET}



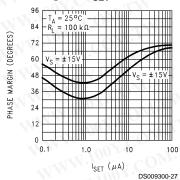
Gain Bandwidth Product vs I_{SET}



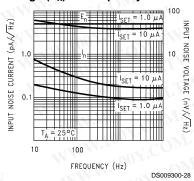
Open Loop Voltage Gain vs I_{SET}



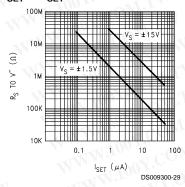
Phase Margin vs I_{SET}



Input Noise Current (I_n) and Voltage (E_n) vs Frequency



R_{SET} vs I_{SET}



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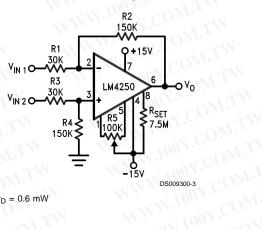
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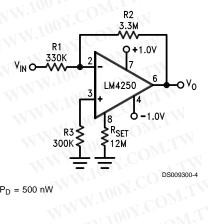
Typical Applications

X5 Difference Amplifier



500 Nano-Watt X10 Amplifier

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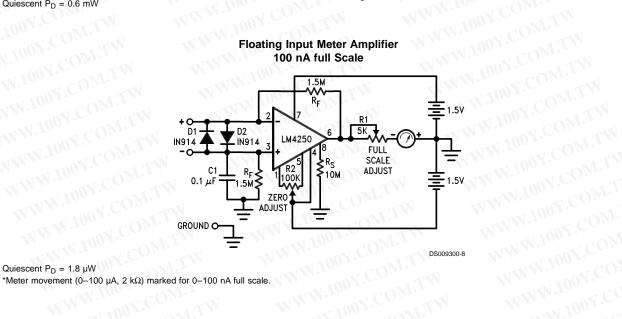
Quiescent $P_D = 0.6 \text{ mW}$ W.100Y.COM.TW

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Quiescent P_D = 500 nW

WWW.100X

WW.100Y.COM. Floating Input Meter Amplifier 100 nA full Scale



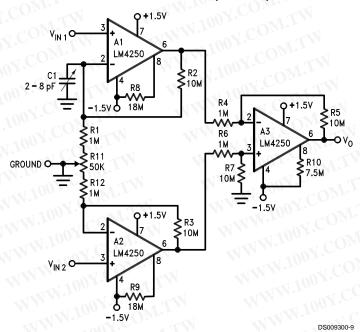
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Quiescent P_D = 1.8 μW WWW.100Y.COM

Typical Applications (Continued)

X100 Instrumentation Amplifier 10 μW

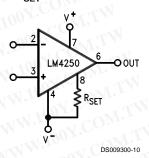


Note 5: Quiescent $P_D = 10 \mu W$.

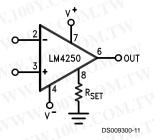
Note 6: R2, R3, R4, R5, R6 and R7 are 1% resistors.

Note 7: R11 and C1 are for DC and AC common mode rejection adjustments.

R_{SET} Connected to V



R_{SET} Connected to Ground

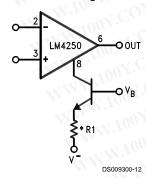


I_{SET} Equations:

$$I_{SET} pprox rac{V^+ - 0.5}{R_{SET}}$$
 where R_{SET} is connected to ground.

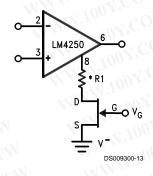
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Transistor Current Sourcing Biasing

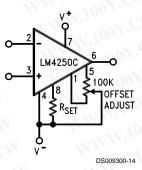


*R1 limits I_{SET} maximum

FET Current Sourcing Biasing



Offset Null Circuit



Schematic Diagram

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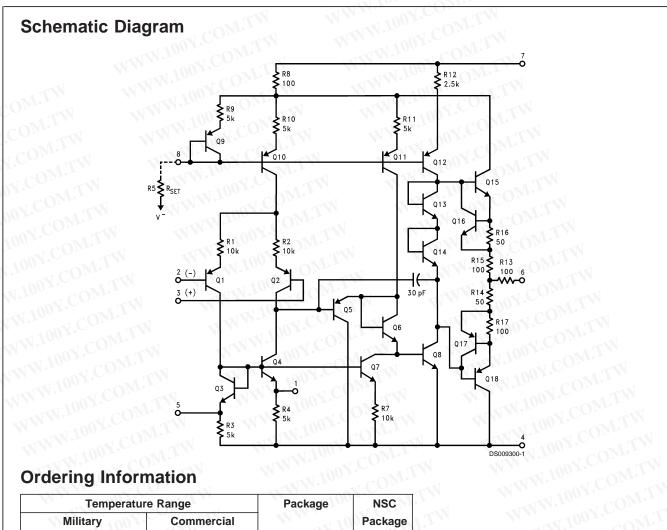
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Ordering Information

Temperatur	e Range	Package	NSC
Military -55°C ≤ T _A ≤ +125°C	Commercial $0^{\circ}C \le T_{A} \le +70^{\circ}C$	MMM.10	Package Number
WWW.100	LM4250CN	8-Pin Molded DIP	N08E
MMM.10	LM4250CM LM4250CMX	8-Pin Surface Mount	M08A
LM4250J-MIL	100X.COM.TV	8-Pin Ceramic DIP	J08E
MMA	LM4250CH	8-Pin Metal Can	H08C

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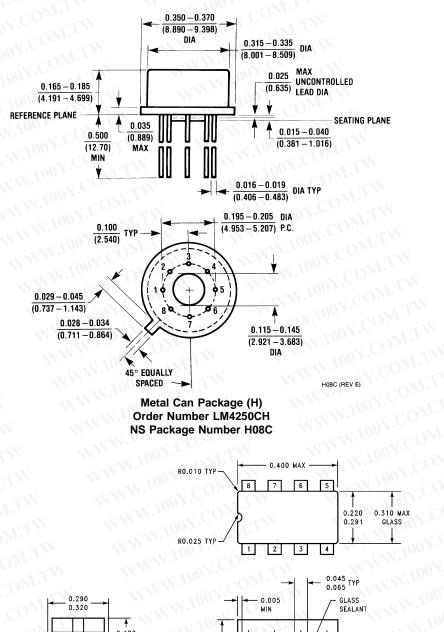
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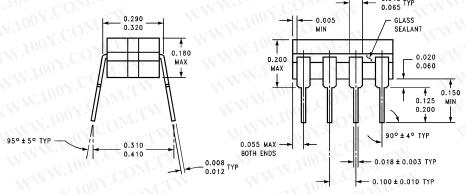
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WWW.100Y.COM.

Physical Dimensions inches (millimeters) unless otherwise noted

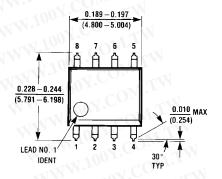


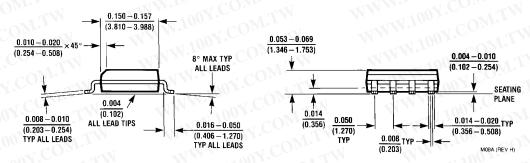


Ceramic Dual-In-Line Package (J)
Order Number LM4250J-MIL
NS Package Number J08A

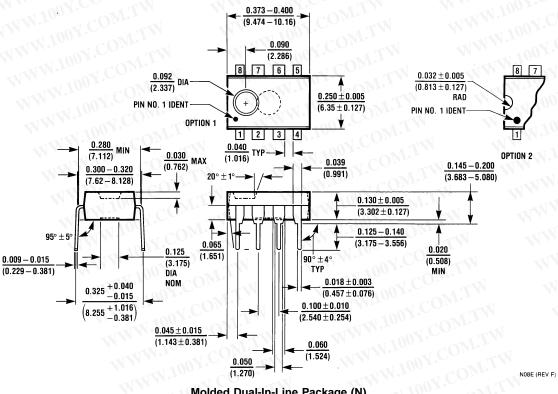
JOSA (REV K)

Physical Dimensions inches (millimeters) unless otherwise noted (Continued)





Small Outline Package (M)
Order Number LM4250CM or LM4250CMX
NS Package Number M08A



Molded Dual-In-Line Package (N)
Order Number LM4250CN
NS Package Number N08E

Notes

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