

OPA134
OPA2134
OPA4134

Sound_{PLUS}™ High Performance AUDIO OPERATIONAL AMPLIFIERS

FEATURES

- SUPERIOR SOUND QUALITY
- ULTRA LOW DISTORTION: 0.00008%
- LOW NOISE: $8\text{nV}/\sqrt{\text{Hz}}$
- TRUE FET-INPUT: $I_b = 5\text{pA}$
- HIGH SPEED:
 - SLEW RATE: $20\text{V}/\mu\text{s}$
 - BANDWIDTH: 8MHz
- HIGH OPEN-LOOP GAIN: 120dB (600Ω)
- WIDE SUPPLY RANGE: $\pm 2.5\text{V}$ to $\pm 18\text{V}$
- SINGLE, DUAL, AND QUAD VERSIONS

APPLICATIONS

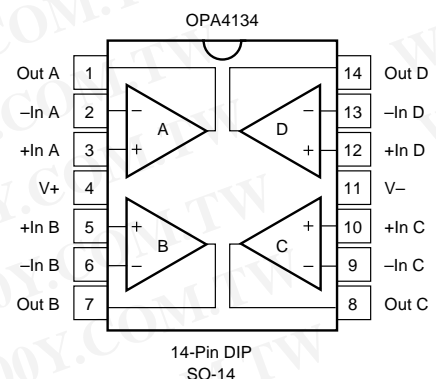
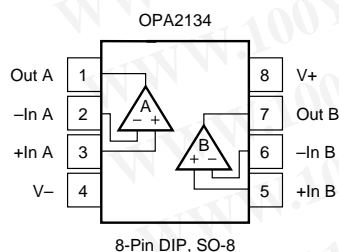
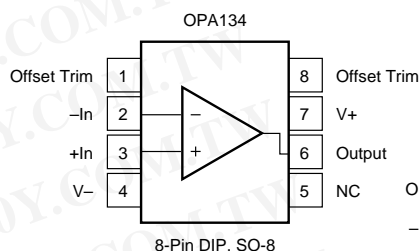
- PROFESSIONAL AUDIO AND MUSIC
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- LINE RECEIVERS
- MULTIMEDIA AUDIO
- ACTIVE FILTERS
- PREAMPLIFIERS
- INTEGRATORS
- CROSSOVER NETWORKS

DESCRIPTION

The OPA134 series are ultra-low distortion, low noise operational amplifiers fully specified for audio applications. A true FET input stage was incorporated to provide superior sound quality and speed for exceptional audio performance. This in combination with high output drive capability and excellent dc performance allows use in a wide variety of demanding applications. In addition, the OPA134's wide output swing, to within 1V of the rails, allows increased headroom making it ideal for use in any audio circuit.

OPA134 op amps are easy to use and free from phase inversion and overload problems often found in common FET-input op amps. They can be operated from $\pm 2.5\text{V}$ to $\pm 18\text{V}$ power supplies. Input cascode circuitry provides excellent common-mode rejection and maintains low input bias current over its wide input voltage range, minimizing distortion. OPA134 series op amps are unity-gain stable and provide excellent dynamic behavior over a wide range of load conditions, including high load capacitance. The dual and quad versions feature completely independent circuitry for lowest crosstalk and freedom from interaction, even when overdriven or overloaded.

Single and dual versions are available in 8-pin DIP and SO-8 surface-mount packages in standard configurations. The quad is available in 14-pin DIP and SO-14 surface mount packages. All are specified for -40°C to $+85^\circ\text{C}$ operation. A SPICE macromodel is available for design analysis.



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 Internet: <http://www.burr-brown.com/> • FAXLine: (800) 548-6133 (US/Canada Only) • Cable: BBRCORP • Telex: 066-6491 • FAX: (520) 889-1510 • Immediate Product Info: (800) 548-6132

SPECIFICATIONS

At $T_A = +25^{\circ}\text{C}$, $V_S = \pm 15\text{V}$, unless otherwise noted.

PARAMETER	CONDITION	OPA134PA, UA OPA2134PA, UA OPA4134PA, UA			UNITS
		MIN	TYP	MAX	
AUDIO PERFORMANCE Total Harmonic Distortion + Noise Intermodulation Distortion Headroom ⁽¹⁾	$G = 1$, $f = 1\text{kHz}$, $V_O = 3\text{Vrms}$ $R_L = 2\text{k}\Omega$ $R_L = 600\Omega$ $G = 1$, $f = 1\text{kHz}$, $V_O = 1\text{Vp-p}$ $\text{THD} < 0.01\%$, $R_L = 2\text{k}\Omega$, $V_S = \pm 18\text{V}$		0.00008 0.00015 -98 23.6		% % dB dBu
FREQUENCY RESPONSE Gain-Bandwidth Product Slew Rate ⁽²⁾ Full Power Bandwidth Settling Time 0.1% 0.01% Overload Recovery Time	$G = 1$, 10V Step, $C_L = 100\text{pF}$ $G = 1$, 10V Step, $C_L = 100\text{pF}$ $(V_{IN}) \cdot (\text{Gain}) = V_S$	± 15	8 ± 20 1.3 0.7 1 0.5		MHz V/ μs MHz μs μs μs
NOISE Input Voltage Noise Noise Voltage, $f = 20\text{Hz}$ to 20kHz Noise Density, $f = 1\text{kHz}$ Current Noise Density, $f = 1\text{kHz}$			1.2 8 3		μVrms $\text{nV}/\sqrt{\text{Hz}}$ $\text{fA}/\sqrt{\text{Hz}}$
OFFSET VOLTAGE Input Offset Voltage vs Temperature vs Power Supply (PSRR) Channel Separation (Dual, Quad)	$T_A = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$ $T_A = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$ $V_S = \pm 2.5\text{V}$ to $\pm 18\text{V}$ dc, $R_L = 2\text{k}\Omega$ $f = 20\text{kHz}$, $R_L = 2\text{k}\Omega$	90	± 0.5 ± 1 ± 2 106 135 130	± 2 $\pm 3^{(3)}$	mV mV $\mu\text{V}/^{\circ}\text{C}$ dB dB dB
INPUT BIAS CURRENT Input Bias Current ⁽⁴⁾ vs Temperature ⁽³⁾ Input Offset Current ⁽⁴⁾	$V_{CM} = 0\text{V}$ $V_{CM} = 0\text{V}$		+5 See Typical Curve ± 2	± 100 ± 5 ± 50	pA nA pA
INPUT VOLTAGE RANGE Common-Mode Voltage Range Common-Mode Rejection	$V_{CM} = -12.5\text{V}$ to $+12.5\text{V}$ $T_A = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$	(V-)+2.5 86	± 13 100 90	(V+)-2.5	V dB dB
INPUT IMPEDANCE Differential Common-Mode	$V_{CM} = -12.5\text{V}$ to $+12.5\text{V}$		$10^{13} \parallel 2$ $10^{13} \parallel 5$		$\Omega \parallel \text{pF}$ $\Omega \parallel \text{pF}$
OPEN-LOOP GAIN Open-Loop Voltage Gain	$R_L = 10\text{k}\Omega$, $V_O = -14.5\text{V}$ to $+13.8\text{V}$ $R_L = 2\text{k}\Omega$, $V_O = -13.8\text{V}$ to $+13.5\text{V}$ $R_L = 600\Omega$, $V_O = -12.8\text{V}$ to $+12.5\text{V}$	104 104 104	120 120 120		dB dB dB
OUTPUT Voltage Output Output Current Output Impedance, Closed-Loop ⁽⁵⁾ Open-Loop Short-Circuit Current Capacitive Load Drive (Stable Operation)	$R_L = 10\text{k}\Omega$ $R_L = 2\text{k}\Omega$ $R_L = 600\Omega$ $f = 10\text{kHz}$ $f = 10\text{kHz}$	(V-)+0.5 (V-)+1.2 (V-)+2.2	± 35 0.01 10 ± 40 See Typical Curve	(V+)-1.2 (V+)-1.5 (V+)-2.5	V V V mA Ω Ω mA
POWER SUPPLY Specified Operating Voltage Operating Voltage Range Quiescent Current (per amplifier)	$I_O = 0$	± 2.5	± 15 4	± 18 5	V V mA
TEMPERATURE RANGE Specified Range Operating Range Storage Thermal Resistance, θ_{JA} 8-Pin DIP SO-8 Surface-Mount 14-Pin DIP SO-14 Surface-Mount		-40 -55 -55		+85 +125 +125	$^{\circ}\text{C}$ $^{\circ}\text{C}$ $^{\circ}\text{C}$ $^{\circ}\text{C}/\text{W}$ $^{\circ}\text{C}/\text{W}$ $^{\circ}\text{C}/\text{W}$ $^{\circ}\text{C}/\text{W}$

NOTES: (1) $\text{dBu} = 20 \cdot \log(V_{\text{rms}}/0.7746)$ where V_{rms} is the maximum output voltage for which $\text{THD} + \text{Noise}$ is less than 0.01%. See $\text{THD} + \text{Noise}$ text. (2) Guaranteed by design. (3) Guaranteed by wafer-level test to 95% confidence level. (4) High-speed test at $T_J = 25^{\circ}\text{C}$. (5) See "Closed-Loop Output Impedance vs Frequency" typical curve.

ABSOLUTE MAXIMUM RATINGS⁽¹⁾

Supply Voltage, V_+ to V_-	36V
Input Voltage	(V_-) $-0.7V$ to (V_+) $+0.7V$
Output Short-Circuit ⁽²⁾	Continuous
Operating Temperature	-40°C to $+125^\circ\text{C}$
Storage Temperature	-55°C to $+125^\circ\text{C}$
Junction Temperature	150°C
Lead Temperature (soldering, 10s)	300°C

NOTES: (1) Stresses above these ratings may cause permanent damage.
(2) Short-circuit to ground, one amplifier per package.

PACKAGE/ORDERING INFORMATION

PRODUCT	PACKAGE	PACKAGE DRAWING NUMBER ⁽¹⁾	TEMPERATURE RANGE
Single OPA134PA OPA134UA	8-Pin Plastic DIP	006	-40°C to $+85^\circ\text{C}$
	SO-8 Surface-Mount	182	-40°C to $+85^\circ\text{C}$
Dual OPA2134PA OPA2134UA	8-Pin Plastic DIP	006	-40°C to $+85^\circ\text{C}$
	SO-8 Surface-Mount	182	-40°C to $+85^\circ\text{C}$
Quad OPA4134PA OPA4134UA	14-Pin Plastic DIP	010	-40°C to $+85^\circ\text{C}$
	SO-14 Surface-Mount	235	-40°C to $+85^\circ\text{C}$

NOTE: (1) For detailed drawing and dimension table, please see end of data sheet, or Appendix C of Burr-Brown IC Data Book.



ELECTROSTATIC DISCHARGE SENSITIVITY

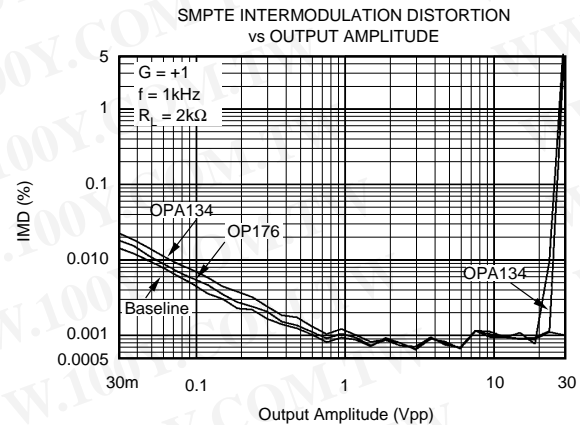
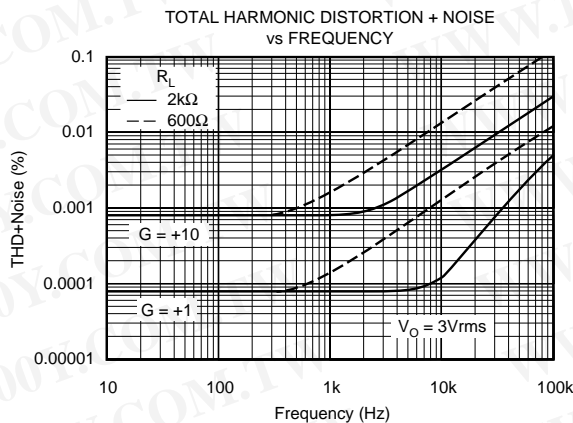
This integrated circuit can be damaged by ESD. Burr-Brown recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

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TYPICAL PERFORMANCE CURVES

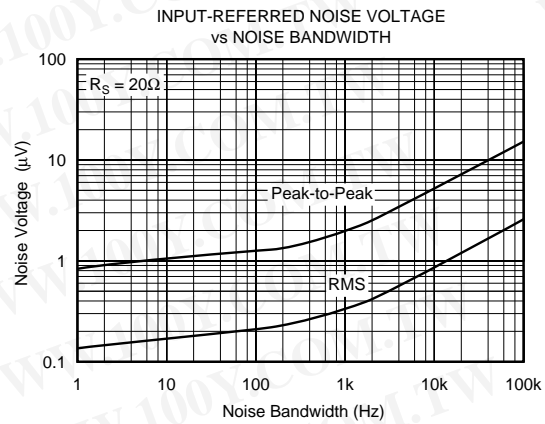
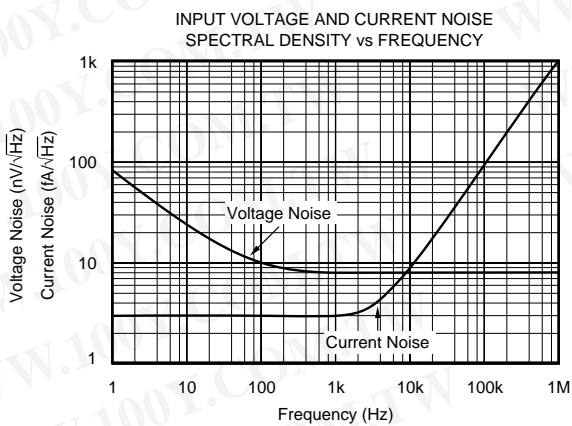
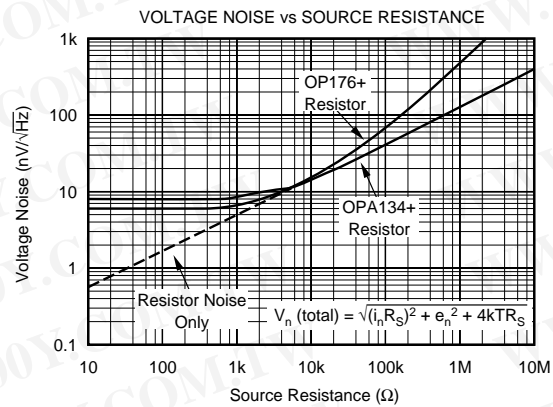
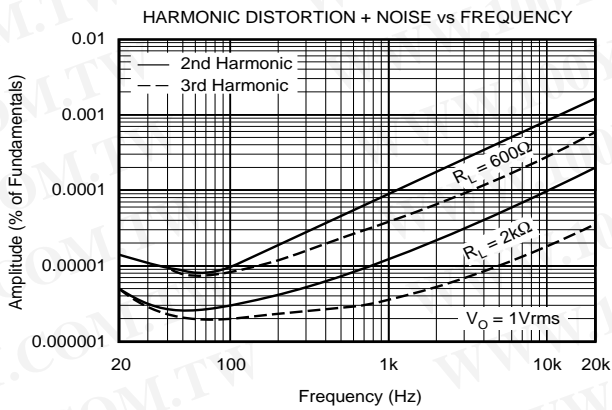
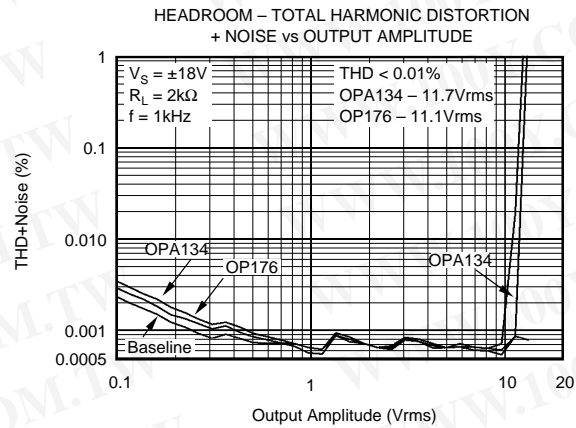
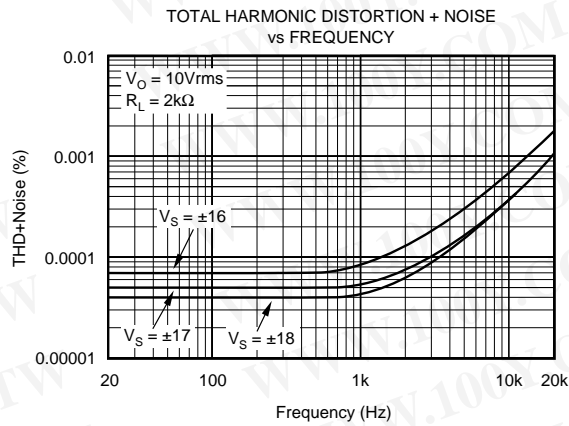
At $T_A = +25^\circ\text{C}$, $V_S = \pm 15V$, $R_L = 2k\Omega$, unless otherwise noted.



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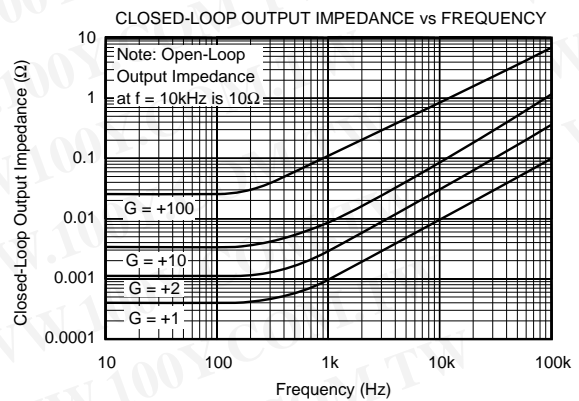
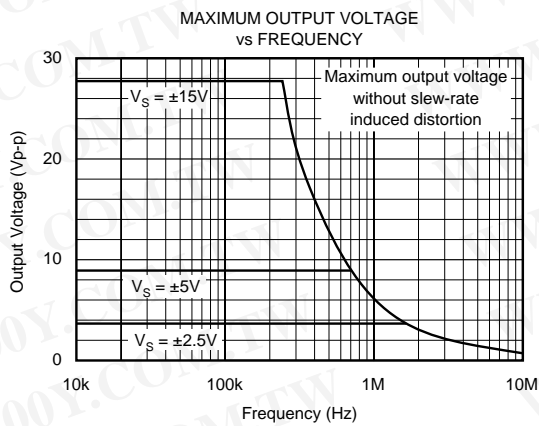
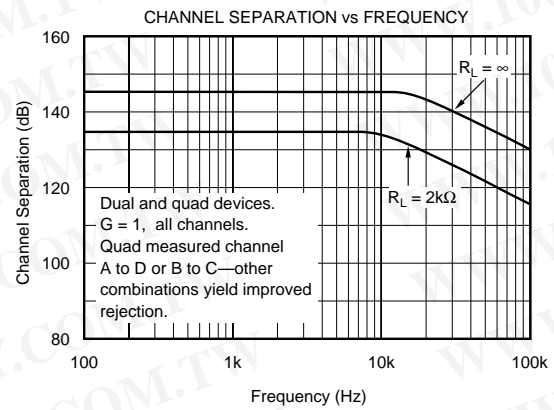
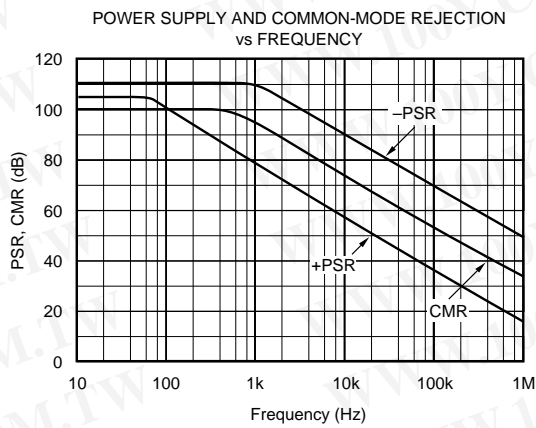
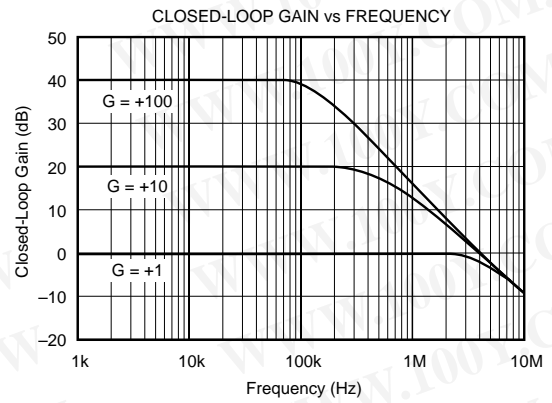
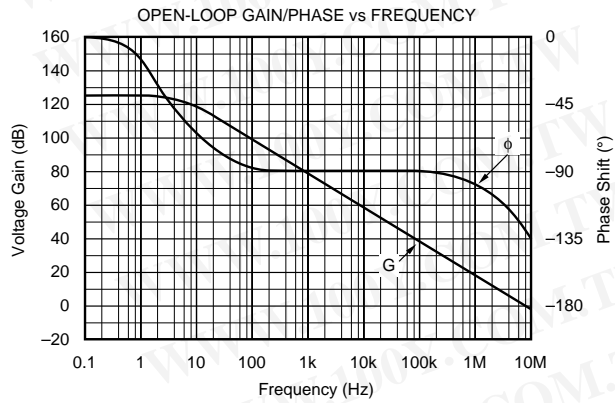


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TYPICAL PERFORMANCE CURVES (CONT)

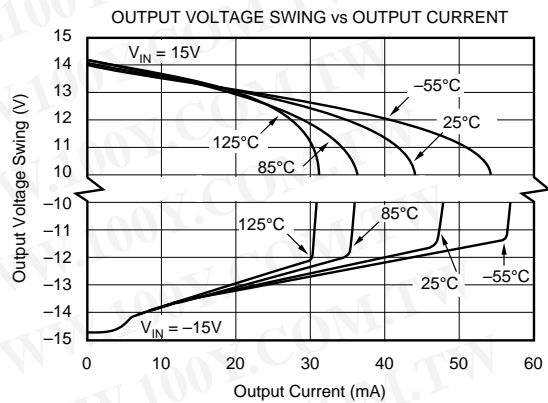
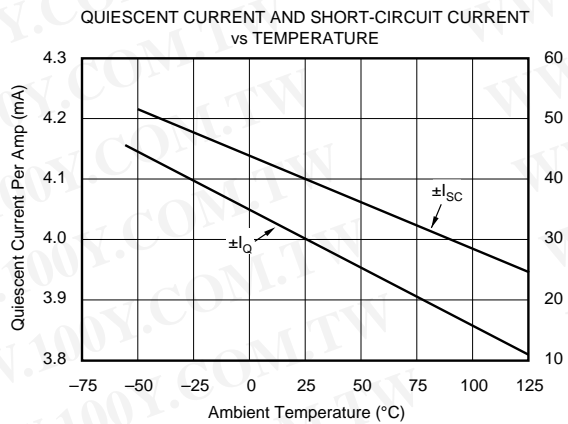
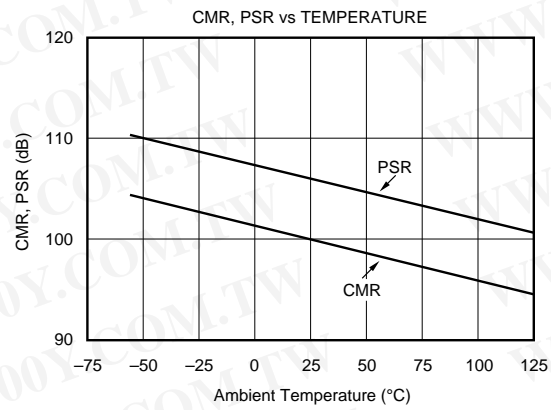
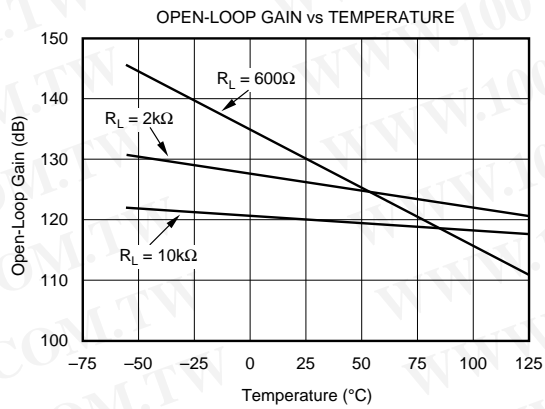
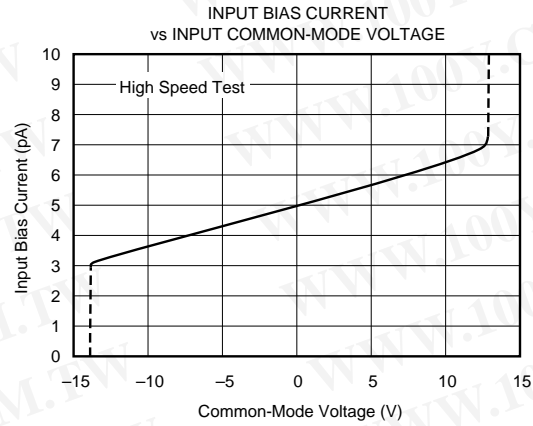
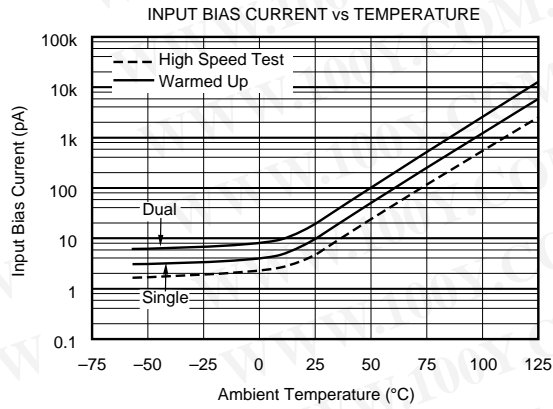
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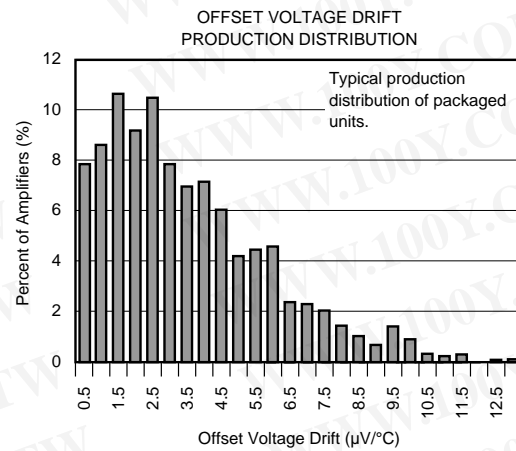
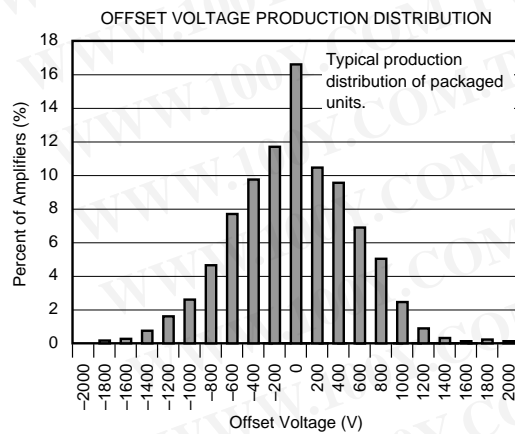


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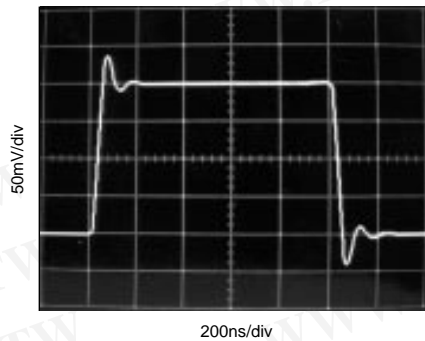
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TYPICAL PERFORMANCE CURVES (CONT)

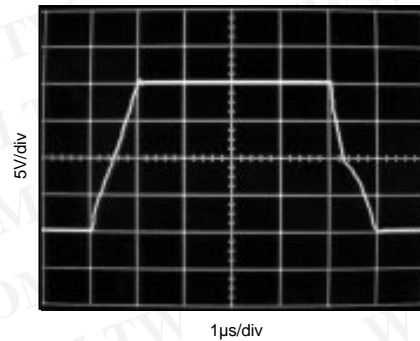
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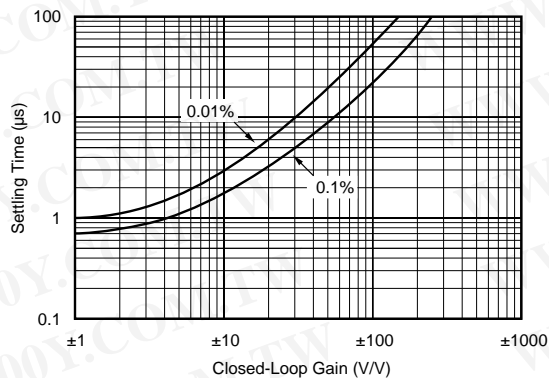
SMALL-SIGNAL STEP RESPONSE
 $G = 1$, $C_L = 100\text{pF}$



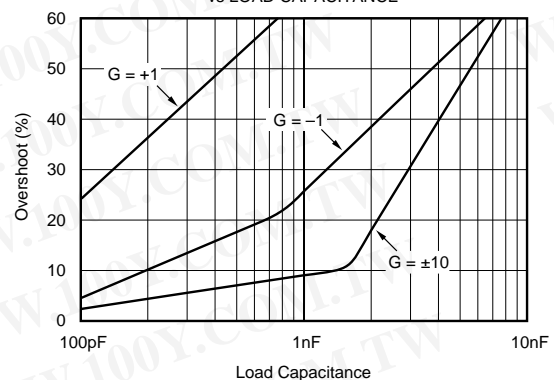
LARGE-SIGNAL STEP RESPONSE
 $G = 1$, $C_L = 100\text{pF}$



SETTLING TIME vs CLOSED-LOOP GAIN



SMALL-SIGNAL OVERSHOOT vs LOAD CAPACITANCE



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APPLICATIONS INFORMATION

OPA134 series op amps are unity-gain stable and suitable for a wide range of audio and general-purpose applications. All circuitry is completely independent in the dual version, assuring normal behavior when one amplifier in a package is overdriven or short-circuited. Power supply pins should be bypassed with 10nF ceramic capacitors or larger to minimize power supply noise.

OPERATING VOLTAGE

OPA134 series op amps operate with power supplies from $\pm 2.5\text{V}$ to $\pm 18\text{V}$ with excellent performance. Although specifications are production tested with $\pm 15\text{V}$ supplies, most behavior remains unchanged throughout the full operating voltage range. Parameters which vary significantly with operating voltage are shown in the typical performance curves.

OFFSET VOLTAGE TRIM

Offset voltage of OPA134 series amplifiers is laser trimmed and usually requires no user adjustment. The OPA134 (single op amp version) provides offset trim connections on pins 1 and 8, identical to 5534 amplifiers. Offset voltage can be adjusted by connecting a potentiometer as shown in Figure 1. This adjustment should be used only to null the offset of the op amp, not to adjust system offset or offset produced by the signal source. Nulling offset could change the offset voltage drift behavior of the op amp. While it is not possible to predict the exact change in drift, the effect is usually small.

TOTAL HARMONIC DISTORTION

OPA134 series op amps have excellent distortion characteristics. THD+Noise is below 0.0004% throughout the audio frequency range, 20Hz to 20kHz, with a $2\text{k}\Omega$ load. In addition, distortion remains relatively flat through its wide output voltage swing range, providing increased headroom compared to other audio amplifiers, including the OPA176/275.

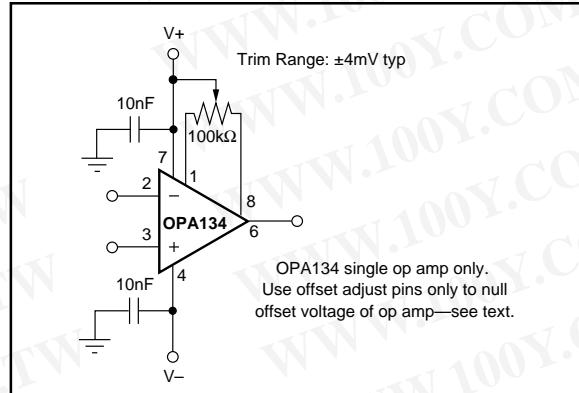


FIGURE 1. OPA134 Offset Voltage Trim Circuit.

In many ways headroom is a subjective measurement. It can be thought of as the maximum output amplitude allowed while still maintaining a very low level of distortion. In an attempt to quantify headroom, we have defined "very low distortion" as 0.01%. Headroom is expressed as a ratio which compares the maximum allowable output voltage level to a standard output level (1mW into 600Ω , or 0.7746Vrms). Therefore, OPA134 series op amps, which have a maximum allowable output voltage level of 11.7Vrms (THD+Noise < 0.01%), have a headroom specification of 23.6dBu. See the typical curve "Headroom - Total Harmonic Distortion + Noise vs Output Amplitude."

DISTORTION MEASUREMENTS

The distortion produced by OPA134 series op amps is below the measurement limit of all known commercially available equipment. However, a special test circuit can be used to extend the measurement capabilities.

Op amp distortion can be considered an internal error source which can be referred to the input. Figure 2 shows a circuit which causes the op amp distortion to be 101 times greater than normally produced by the op amp. The addition of R_3 to the otherwise standard non-inverting amplifier

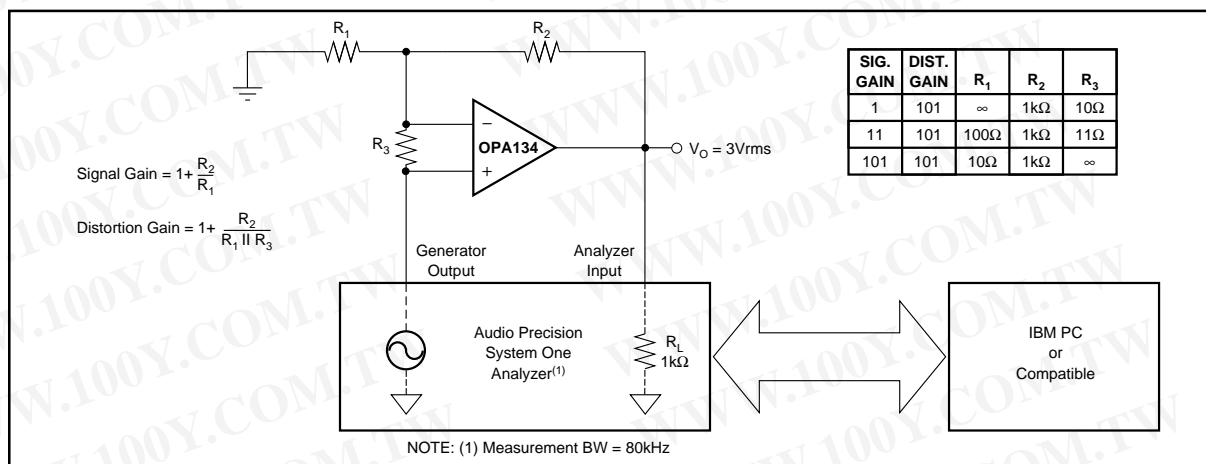


FIGURE 2. Distortion Test Circuit.



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configuration alters the feedback factor or noise gain of the circuit. The closed-loop gain is unchanged, but the feedback available for error correction is reduced by a factor of 101, thus extending the resolution by 101. Note that the input signal and load applied to the op amp are the same as with conventional feedback without R_3 . The value of R_3 should be kept small to minimize its effect on the distortion measurements.

Validity of this technique can be verified by duplicating measurements at high gain and/or high frequency where the distortion is within the measurement capability of the test equipment. Measurements for this data sheet were made with an Audio Precision distortion/noise analyzer which greatly simplifies such repetitive measurements. The measurement technique can, however, be performed with manual distortion measurement instruments.

SOURCE IMPEDANCE AND DISTORTION

For lowest distortion with a source or feedback network which has an impedance greater than $2k\Omega$, the impedance seen by the positive and negative inputs in noninverting applications should be matched. The p-channel JFETs in the FET input stage exhibit a varying input capacitance with applied common-mode input voltage. In inverting configurations the input does not vary with input voltage since the inverting input is held at virtual ground. However, in noninverting applications the inputs do vary, and the gate-to-source voltage is not constant. The effect is increased distortion due to the varying capacitance for unmatched source impedances greater than $2k\Omega$.

To maintain low distortion, match unbalanced source impedance with appropriate values in the feedback network as shown in Figure 3. Of course, the unbalanced impedance may be from gain-setting resistors in the feedback path. If the parallel combination of R_1 and R_2 is greater than $2k\Omega$, a matching impedance on the noninverting input should be used. As always, resistor values should be minimized to reduce the effects of thermal noise.

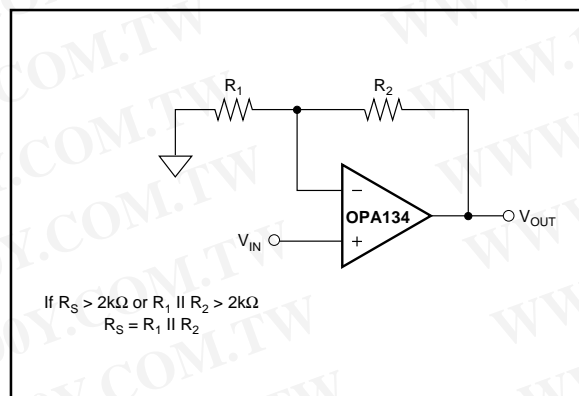


FIGURE 3. Impedance Matching for Maintaining Low Distortion in Non-Inverting Circuits.

NOISE PERFORMANCE

Circuit noise is determined by the thermal noise of external resistors and op amp noise. Op amp noise is described by two parameters—noise voltage and noise current. The total noise is quantified by the equation:

$$V_n(\text{total}) = \sqrt{(i_n R_S)^2 + e_n^2 + 4kTR_S}$$

With low source impedance, the current noise term is insignificant and voltage noise dominates the noise performance. At high source impedance, the current noise term becomes the dominant contributor.

Low noise bipolar op amps such as the OPA27 and OPA37 provide very low voltage noise at the expense of a higher current noise. However, OPA134 series op amps are unique in providing very low voltage noise and very low current noise. This provides optimum noise performance over a wide range of sources, including reactive source impedances, refer to the typical curve, “Voltage Noise vs Source Resistance.” Above $2k\Omega$ source resistance, the op amp contributes little additional noise—the voltage and current terms in the total noise equation become insignificant and the source resistance term dominates. Below $2k\Omega$, op amp voltage noise dominates over the resistor noise, but compares favorably with other audio op amps such as OP176.

PHASE REVERSAL PROTECTION

OPA134 series op amps are free from output phase-reversal problems. Many audio op amps, such as OP176, exhibit phase-reversal of the output when the input common-mode voltage range is exceeded. This can occur in voltage-follower circuits, causing serious problems in control loop applications. OPA134 series op amps are free from this undesirable behavior even with inputs of 10V beyond the input common-mode range.

POWER DISSIPATION

OPA134 series op amps are capable of driving 600Ω loads with power supply voltage up to $\pm 18V$. Internal power dissipation is increased when operating at high supply voltages. Copper leadframe construction used in OPA134 series op amps improves heat dissipation compared to conventional materials. Circuit board layout can also help minimize junction temperature rise. Wide copper traces help dissipate the heat by acting as an additional heat sink. Temperature rise can be further minimized by soldering the devices to the circuit board rather than using a socket.

OUTPUT CURRENT LIMIT

Output current is limited by internal circuitry to approximately $\pm 40mA$ at $25^\circ C$. The limit current decreases with increasing temperature as shown in the typical performance curve “Short-Circuit Current vs Temperature.”

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23-Aug-2010

PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	Eco Plan ⁽²⁾	Lead/ Ball Finish	MSL Peak Temp ⁽³⁾	Samples (Requires Login)
OPA134PA	ACTIVE	PDIP	P	8	50	Green (RoHS & no Sb/Br)	CU NIPDAU	N / A for Pkg Type	Contact TI Distributor or Sales Office
OPA134PA3	OBSOLETE	PDIP	P	8		TBD	Call TI	Call TI	Samples Not Available
OPA134PAG4	ACTIVE	PDIP	P	8	50	Green (RoHS & no Sb/Br)	CU NIPDAU	N / A for Pkg Type	Contact TI Distributor or Sales Office
OPA134UA	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-3-260C-168 HR	Contact TI Distributor or Sales Office
OPA134UA/2K5	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-3-260C-168 HR	Purchase Samples
OPA134UA/2K5E4	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-3-260C-168 HR	Purchase Samples
OPA134UA3	OBSOLETE	PDIP	P	8		TBD	Call TI	Call TI	Samples Not Available
OPA134UAE4	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-3-260C-168 HR	Contact TI Distributor or Sales Office
OPA134UAG4	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-3-260C-168 HR	Contact TI Distributor or Sales Office
OPA2134PA	ACTIVE	PDIP	P	8	50	Green (RoHS & no Sb/Br)	CU NIPDAU	N / A for Pkg Type	Contact TI Distributor or Sales Office
OPA2134PAG4	ACTIVE	PDIP	P	8	50	Green (RoHS & no Sb/Br)	CU NIPDAU	N / A for Pkg Type	Contact TI Distributor or Sales Office
OPA2134UA	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-3-260C-168 HR	Contact TI Distributor or Sales Office
OPA2134UA/2K5	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-3-260C-168 HR	Purchase Samples
OPA2134UA/2K5E4	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-3-260C-168 HR	Purchase Samples
OPA2134UAE4	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-3-260C-168 HR	Contact TI Distributor or Sales Office
OPA2134UAG4	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-3-260C-168 HR	Contact TI Distributor or Sales Office
OPA4134PA	OBSOLETE	PDIP	N	14		TBD	Call TI	Call TI	Replaced by OPA4134UA
OPA4134UA	ACTIVE	SOIC	D	14	50	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-3-260C-168 HR	Request Free Samples

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	Eco Plan ⁽²⁾	Lead/ Ball Finish	MSL Peak Temp ⁽³⁾	Samples (Requires Login)
OPA4134UA/2K5	ACTIVE	SOIC	D	14	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-3-260C-168 HR	Purchase Samples
OPA4134UA/2K5E4	ACTIVE	SOIC	D	14	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-3-260C-168 HR	Purchase Samples
OPA4134UAE4	ACTIVE	SOIC	D	14	50	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-3-260C-168 HR	Request Free Samples
SN412008DRE4	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-3-260C-168 HR	Purchase Samples

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

⁽²⁾ Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

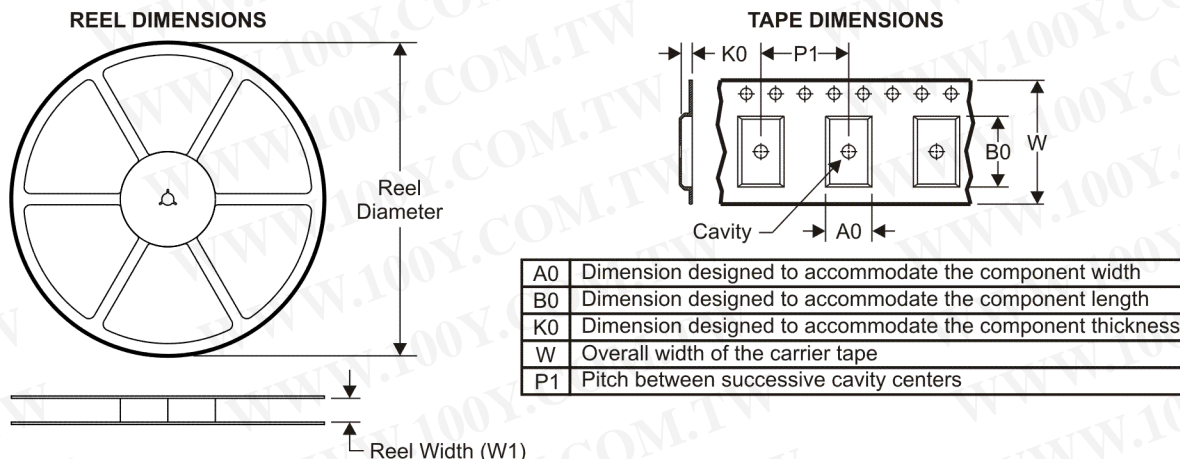
⁽³⁾ MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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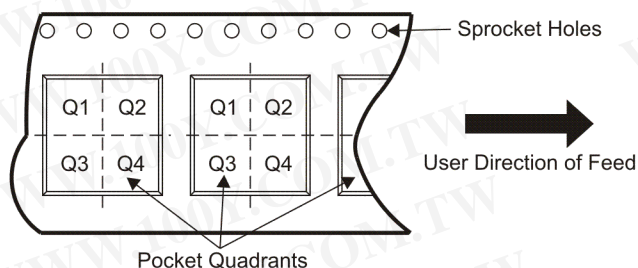
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TAPE AND REEL INFORMATION



QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE

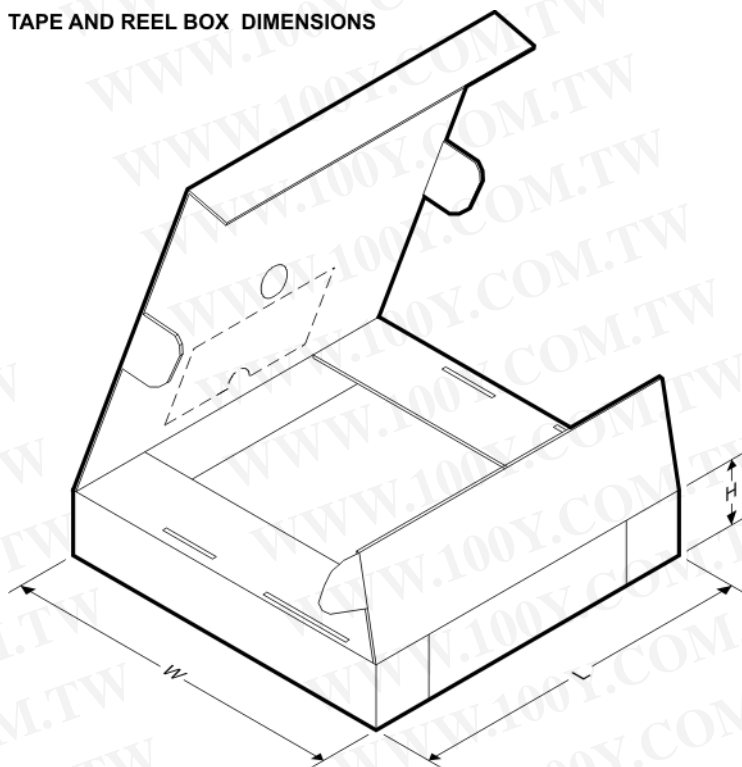


*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
OPA134UA/2K5	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
OPA2134UA/2K5	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
OPA4134UA/2K5	SOIC	D	14	2500	330.0	16.4	6.5	9.0	2.1	8.0	16.0	Q1

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TAPE AND REEL BOX DIMENSIONS



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*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
OPA134UA/2K5	SOIC	D	8	2500	346.0	346.0	29.0
OPA2134UA/2K5	SOIC	D	8	2500	346.0	346.0	29.0
OPA4134UA/2K5	SOIC	D	14	2500	346.0	346.0	33.0

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