勝 特 力 材 料 886-3-5753170 胜特力电子(上海) 86-21-34970699 胜特力电子(深圳) 86-755-83298787 Http://www.100y.com.tw

LED Shunt

The NUD4700 is an electronic shunt which provides a current bypass in the case of a single LED going into open circuit. LEDs are by nature quite fragile when subjected to transients and surge conditions. There are also many cases where high reliability of the LED lighting must be maintained such as headlights, lighthouses, bridges, aircraft, runways and so forth. In these cases the low cost addition of the NUD4700 will provide full assurance that an entire string of LEDs will not extinguish should one LED fail. NUD4700 is also applicable to other loads where circuit continuity is required. This device is designed to be used with 1 W LEDs (nominally 350 mA @ 3 V).

Features

- A Simple Two Terminal Device
- Automatically Resets Itself if the LED Heals Itself or is Replaced
- ON-State Voltage Typically 1 V
- OFF-State Current less than 250 µA
- This is a Pb-Free Device

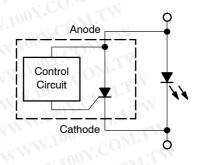
Typical Applications

- LEDs where Preventive Maintenance is Non Practical
- LED Headlights
- LEDs with High Reliability Requirements
- Crowbar Protection for Open Circuit Conditions
- Overvoltage Protection for Sensitive Circuits



ON Semiconductor®

http://onsemi.com





POWERMITE® CASE 457 PLASTIC

MARKING DIAGRAM



M = Date Code
MNH = Device Code
■ Pb-Free Package

PIN FUNCTION DESCRIPTION

Pin	Description
Anode	Positive Input Voltage to the Device
Cathode	Negative Input Voltage to the Device

ORDERING INFORMATION

Device	Package	Shipping [†]
NUD4700SNT1G	POWERMITE (Pb-Free)	3000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

MAXIMUM RATINGS (Maximum ratings are those, that, if exceeded, may cause damage to the device. Electrical Characteristics are not guaranteed over this range)

Rating	Symbol	Value	Unit
Peak Repetitive Off State Voltage (Anode to Cathode)	V _{DM}	-0.3 to 10	V
Average On-State Current, (T _A = 25°C), (Note 1) (Note 2)	I _{T(AVG)}	1.3 0.376	А
Thermal Resistance, Junction-to-Air (Note 1) (Note 2)	Q _{JA}	80 277	°C/W
Thermal Resistance, Junction-to-Lead	Q _{JL}	35	°C/W
Power Dissipation (T _A = 25°C) (Note 1) (Note 2)	P _{MAX}	1.56 0.45	W
Operating Temperature Range	T _J C	-40 to 150	°C
Non-Operating Temperature Range	T _J	150	°C
Lead Temperature, Soldering (10 Sec)	7 J. J. 100 .	260	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

- 1. Mounted onto a 1" x 1" square copper pad.
 - Normally this device would be mounted on the same copper heat sink and adjacent to the LED. If the LED were to go open, then the NUD4700 shunt would now dissipate the power using the same copper heat sink. Since the NUD4700 has a voltage that is nominally 30% of the LED, then the power dissipation would be easily handled by the same heat sink as the LED.
- 2. Device mounted on minimum copper pad.

ELECTRICAL CHARACTERISTICS (Unless otherwise noted: T_A = 25°C)

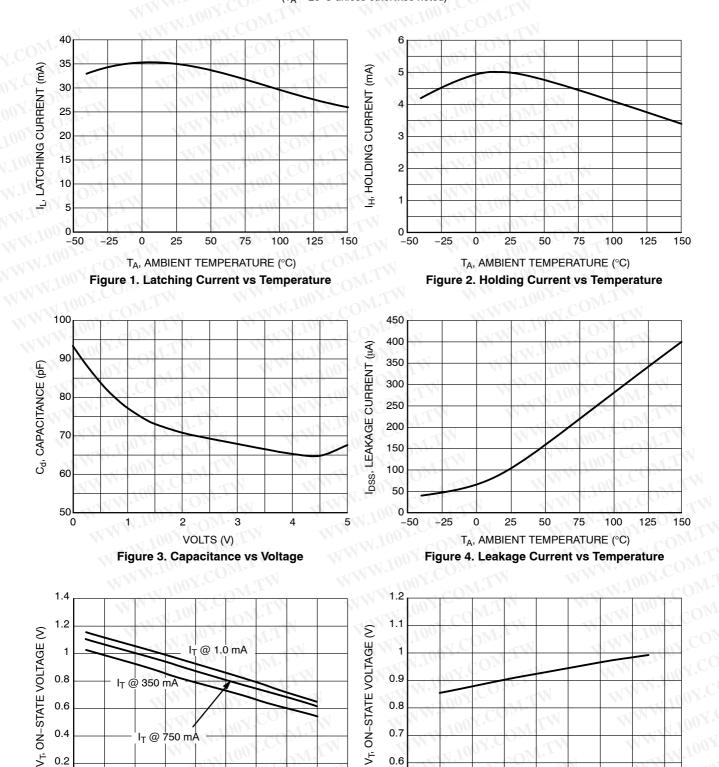
Characteristics	Symbol	Min	Тур	Max	Unit
Off-State Current (V _{Anode} = 5 V)	I _{LEAK}	- 1	100	250	μΑ
Breakdown Voltage (I _{BR} = 1 mA)	V _(BR)	5.5	WWW.	7.5 100 Y.C	ON V
Holding Current (V _{Anode} = 10 V, I _{initial} = 100 mA)	ON CHE	IM-	6.0	12	mA
Latching Current (V _{Anode} = 10 V)	100x.A	TW	35	70	mA
On-State Voltage (I _T = 0.350 A) (I _T = 0.750 A) (I _T = 1.0 A)	V _T CO	M.ZW	1.0 1.0 1.0	1.2	V.V

Critical Rate-of-Rise of Off State Voltage (V _{pk} = Rated V _(BR) , T _J = 125°C, Exponential Method)	dV/dt	250		WWW.	V/μs	
WWW.100Y.COM.TW	MMM.100	I.COM.	W	WWW	N.100X	

WWW.100Y.COM.TW

TYPICAL PERFORMANCE CURVES

(T_A = 25°C unless otherwise noted)



TEMPERATURE (°C) Figure 5. On-State Voltage vs. Temperature

75

100

125

150

50

0

-50

-25 0

Figure 6. On-State Voltage vs. On-State Current (I_T) at 25°C

I_T, ON-STATE CURRNT (A)

0.85

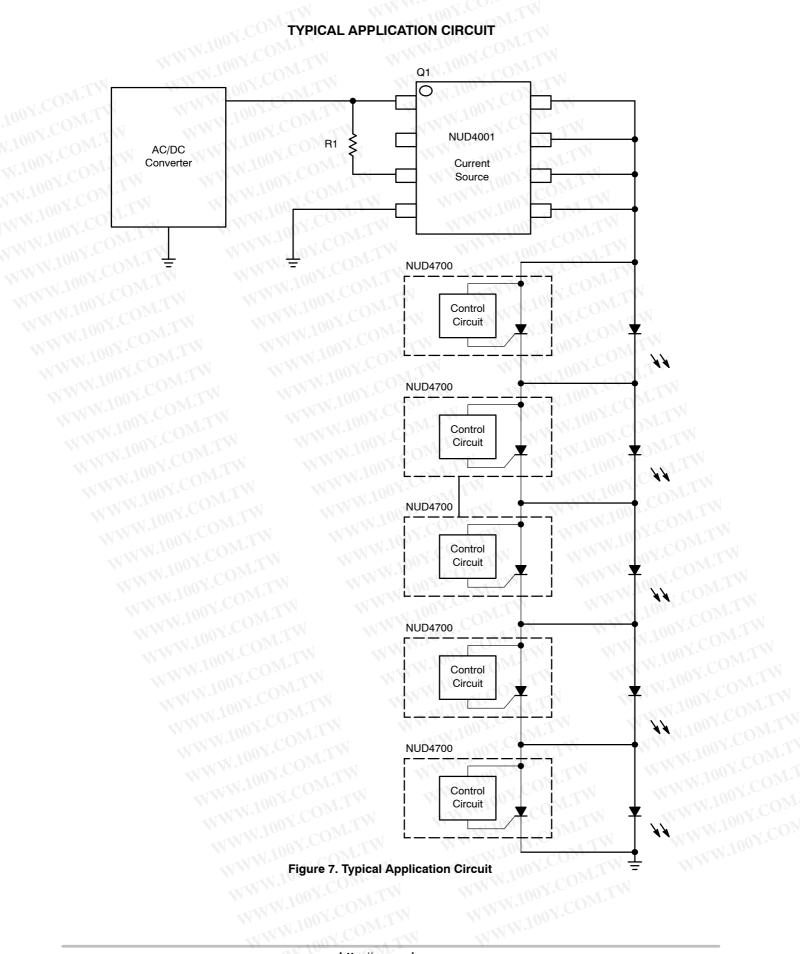
0.95

1.05

0.35 0.45 0.55 0.65 0.75

0.25

OOY.COM.TW



N.COM.TW

TYPICAL OPERATION WAVEFORMS

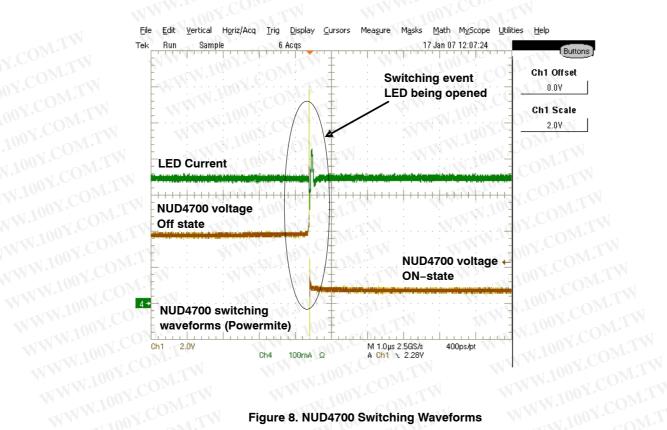


Figure 8. NUD4700 Switching Waveforms

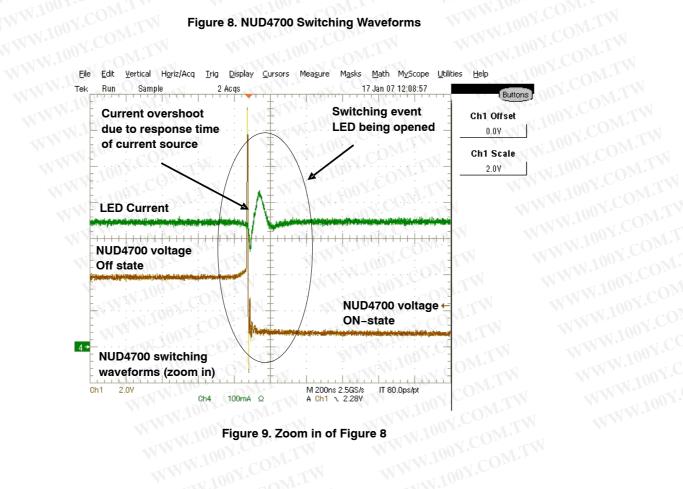


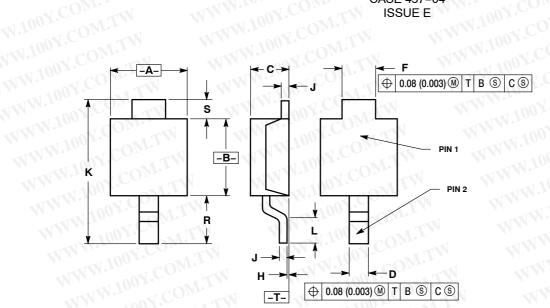
Figure 9. Zoom in of Figure 8 WWW.100Y.COM.TW

EWW.100Y.COM

PACKAGE DIMENSIONS

POWERMITE

CASE 457-04 **ISSUE E**

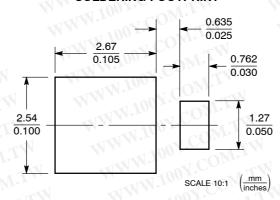


NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: MILLIMETER 2
- DIMENSION A DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS. MOLD FLASH, PROTRUSIONS OR GATE BURRS SHALL NOT EXCEED 0.15 (0.006) PER SIDE.

	MILLIN	INCHES			
DIM	MIN	MAX	MIN	MAX	
Α	1.75	2.05	0.069	0.081	
. Β⁻	1.75	2.18	0.069	0.086	
C	0.85	1.15	0.033	0.045	
D	0.40	0.69	0.016	0.027	
<f th="" ⟨<=""><th>0.70</th><th>1.00</th><th>0.028</th><th>0.039</th></f>	0.70	1.00	0.028	0.039	
H	-0.05	+0.10	-0.002	+0.004	
J	0.10	0.25	0.004	0.010	
K	3.60	3.90	0.142	0.154	
L	0.50	0.80	0.020	0.031	
R	1.20	1.50	0.047	0.059	
S	0.50	REF	0.019	REF	

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

POWERMITE is a registered trademark of and used under a license from Microsemi Corporation. The product described herein (NUD4700), may be covered by one or more U.S. patents. There may be other patents pending

WWW.100Y.CC

TATW 100Y.COM.TW

ON Semiconductor and una are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada

Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada

Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910

Japan Customer Focus Center Phone: 81-3-5773-3850

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative