



DESCRIPTION

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PT6958 is an LED Controller driven on a 1/6 duty factor. Ten segment output lines, 5 grid output lines, one display memory, control circuit, key scan circuit are all incorporated into a single chip to build a highly reliable peripheral device for a single chip microcomputer. Serial data is fed to PT6958 via a three-line serial interface. Housed in a 28-pin SO Package, PT6958's pin assignments and application circuit are optimized for easy PCB Layout and cost saving advantages.

FEATURES

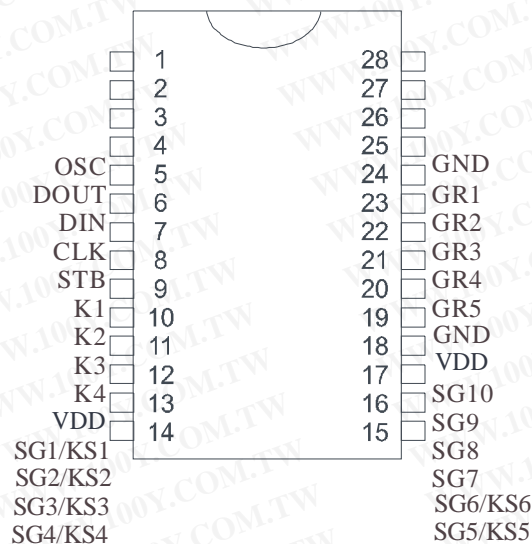
- CMOS Technology
- Low Power Consumption
- Key Scanning (6 x 4 matrix)
- 8-Step Dimming Circuitry
- Serial Interface for Clock, Data Input, Data Output, Strobe Pins
- Available in 28-pin, SO Package

APPLICATION

- Micro-computer Peripheral Device



PIN CONFIGURATION



PIN DESCRIPTION			
Pin Name	I/O	Description	Pin No.
OSC	I	Oscillator Input Pin A resistor is connected to this pin to determine the oscillation frequency	1
DOUT	O	Data Output Pin (N-Channel, Open-Drain) This pin outputs serial data at the falling edge of the shift clock.	2
DIN	I	Data Input Pin This pin inputs serial data at the rising edge of the shift clock (starting from the lower bit)	3
CLK	I	Clock Input Pin This pin reads serial data at the rising edge and outputs data at the falling edge.	4
STB	I	Serial Interface Strobe Pin The data input after the STB has fallen is processed as a command. When this pin is "HIGH", CLK is ignored.	5
K1 to K4	I	Key Data Input Pins The data sent to these pins are latched at the end of the display cycle. (Internal Pull-Low Resistor)	6~9
GND	-	Ground Pin	22,28
SG1/KS1 to SG6/KS6	O	Segment Output Pins (p-channel, open drain) Also acts as the Key Source	11~16
SG7 to SG10	O	Segment Output Pins (p-channel, open drain)	17~20
VDD	-	Power Supply	10,21
GR5 to GR1	O	Grid Output Pins (n-channel, open drain)	23~27

[illegible]