TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

TD62309P,TD62309F

6CH LOW SATURATION HIGH-CURRENT SINK DRIVER

The TD62309P, TD62309F are comprised of six NPN low saturation drivers.

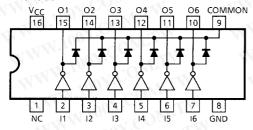
All units feature integral clamp diodes for switching inductive loads. These devices are specifically designed for relay, lamp and LED drive in low voltage systems.

FEATURES

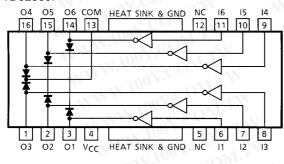
- Low saturation output : VCE (sat) = 0.8 V (Max.)@IOUT = 450 mA
- Output rating (single output) 20 V (Min.) / 700 mA (Max.) Output clamp diodes
- Inputs compatible with TTL and 3~6 V CMOS
- Package type-P: DIP-16 pin Package type-F: PFP-16 pin

PIN CONNECTION (TOP VIEW)

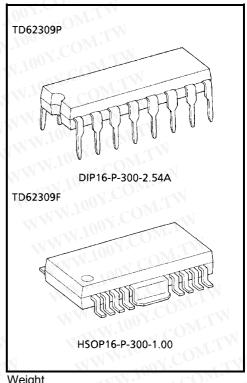
TD62309P



TD62309F



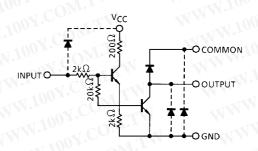
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Weight

DIP16-P-300-2.54A : 1.11 g (Typ.) HSOP16-P-300-1.00: 0.50 g (Typ.)

SCHEMATICS (EACH DRIVER)



The input and output parasitic diodes cannot be used as clamp diodes.

MAXIMUM RATINGS (Ta = 25°C)

Supply Voltage	100 -	Vac	10	V
	·······································	Vcc	10	- C
Output Sustaining Voltag	e 100 -	V _{CE} (SUS)	20	V
Output Current		Гоит	700	mA / ch
Input Voltage	100 Y.C	VIN	10	V
Input Current	1007	lin	10	mA
Clamp Diode Reverse Vo	ltage	V _R	20	V
Clamp Diode Forward Cu	ırrent	COIF	700	mA
Dawer Dissipation	P	OV.CON.	1.47	W
Power Dissipation	TWEN.1	P _D	1.4 (Note)	VV
Operating Temperature	W.	T _{opr}	-40~85	°C
Storage Temperature	M. A.	T _{stg}	-55~150	°C

W.100Y.COM.TW RECOMMENDED OPERATING CONDITIONS ($Ta = -40 \sim 85$ °C)

CHARACTERISTIC		SYMBOL	CONDITION	MIN	TYP.	MAX	UNIT
Supply Voltage		V _{CC}	COM. AM	3	5	7	V
Output Sustaining Voltage	4	V _{CE (SUS)}	V.COM.	M. r.	0. 7 .0	20	V
Output Current COM		M.M.M	DC 1 circuit			- 700 - 200 mA	
Output Current		lout	Tpw = 25 ms, 6 circuits	0	700	200	IIIA
put Voltage		V _{IN}	1001. COM-TW	0	1.700.	V _{CC}	V
Clamp Diode Reverse Voltage	N-F CONSTANT		100X.CONT.TW	W	01.±00	20	V
Clamp Diode Forward Current	rW.	lF	1100Y.CO N.TN	MA	-XT-10	700	mA
Deliver Discipation (V.CO)	P	DWW	TOOY.CO.	41	= 1	0.52	W
Power Dissipation	EN	PD	(Note)	-11	MA.	0.5	COA

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ELECTRICAL CHARACTERISTICS (Ta = 25°C)

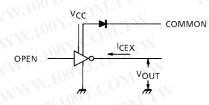
CHARA	CTERISTIC	SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT
Output Leakage Current		I _{CEX}	1	V _{OUT} = 20 V, Ta = 85°C		_	100	μΑ
Output Saturation Voltage		V _{CE} (sat)	2	V _{CC} = 5 V, I _{OUT} = 450 mA	TV	_	0.8	V
				V _{CC} = 5 V, I _{OUT} = 200 mA	A STATE	_	0.45	V
Input Current (Ou	itput On)	I _{IN} (ON)	3	V _{IN} = 3.2V	Dr. F	0.84	1.4	mA
D.C Forward Cur	rent Transfer Ratio	hFE	2	V _{CE} = 4 V, V _{CC} = 6 V I _{OUT} = 300 mA	3000	N -	_	
Supply Current	Output On	I _{CC} (ON)	6	V _{CC} = 7 V, V _{IN} = 3.2 V 6 circuits		120	300	mA
	Output Off	ICC (OFF)	6	V _{CC} = 7 V	NO.	II	10	μΑ
Clamp Diode Rev	verse Current	IR	4	V _R = 20 V	Y.C.	(TY)	100	μΑ
Clamp Diode For	ward Voltage	V _F	5	I _F = 350 mA	UX.Co.		2.7	V
Turn-On Delay		t _{ON}		V _{CC} = 5.0 V, R _L = 36 Ω C _L = 15 pF, V _{OUT} = 20 V	W. CC	0.1	N-	
Turn-Off Delay		toff			~₹C	0.2	CXN	μs

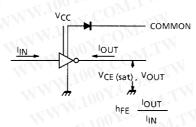
TEST CIRCUIT

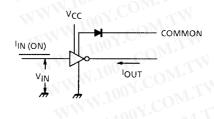
1. ICEX

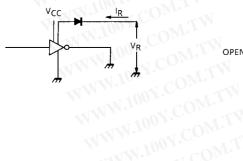
2. hFE, VCE (sat)

WW.100Y.COM.TW 3. I_{IN (ON)}





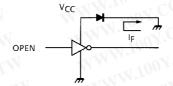


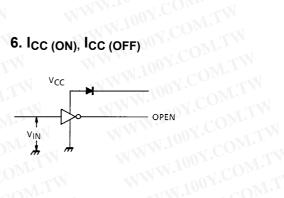




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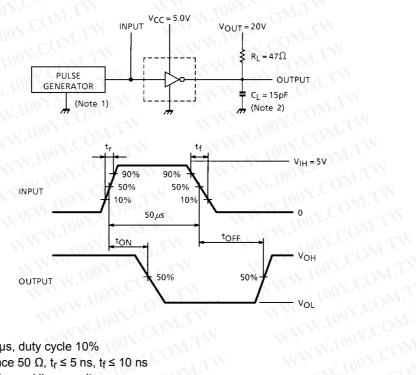




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Note 1: Pulse Width 50 µs, duty cycle 10%

Output impedance 50 Ω , $t_f \le 5$ ns, $t_f \le 10$ ns

Note 2: C_L includes probe and jig capacitance.

PRECAUTIONS for USING

This IC does not include built-in protection circuits for excess current or overvoltage.

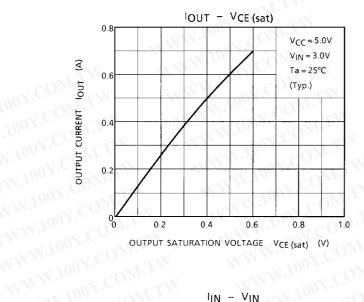
If this IC is subjected to excess current or overvoltage, it may be destroyed.

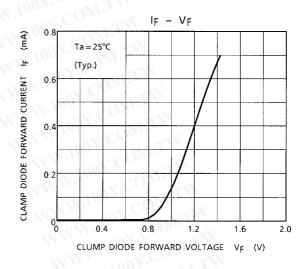
Hence, the utmost care must be taken when systems which incorporate this IC are designed.

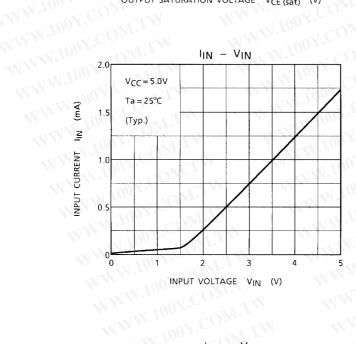
Utmost care is necessary in the design of the output line, VCC, COMMON and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.

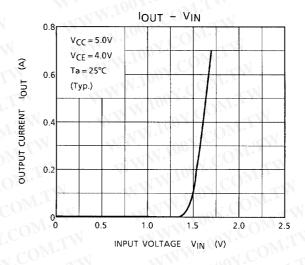
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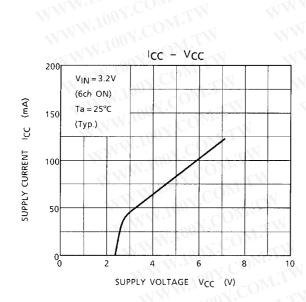
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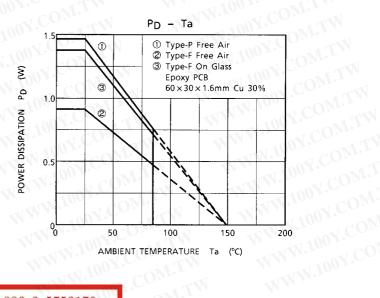












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PACKAGE DIMENSIONS

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WWW.100Y.COM.TW Weight: 1.11 g (Typ.) WWW.100Y.COM.TW

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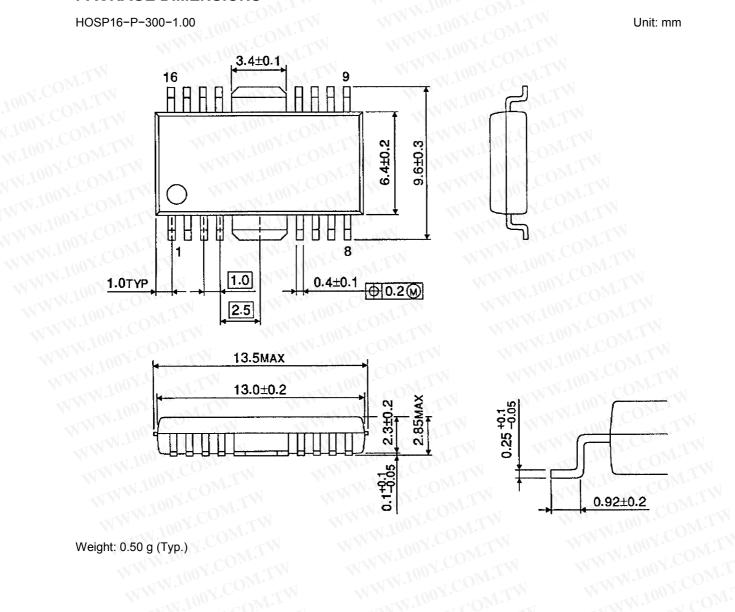
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