

W.100Y.COM.TW VW.100Y.COM.TW OMM74HC4066100Y.COM.TW Quad Analog Switch WW.100Y.COM.TW

WWW.100Y.COM.TW 勝 特 力 材 料 886-3-5753170 胜特力电子(上海) 86-21-34970699 胜特力电子(深圳) 86-755-83298787 Http://www. 100y. com. tw WWW.100Y.CO.

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- Typical switch enable time: 15ns

 Wide analog input value
- Wide analog input voltage range: 0V–12V
 Low "ON" resistance: 00
 - Low "ON" resistance: 30 typ. (MM74HC4066)
 Low quiescent current: 20 ■ Low quiescent current: 80µA maximum (74HC)

 ■ Matched switch observed in the control of the c WWW.100Y.COM.TW
- Matched switch characteristics WWW.100Y.COM.TV
- Individual switch controls WWW.100Y.CO WWW.100Y.COM.TW

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NWW.100Y.COM.TW **General Description**

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The MM74HC4066 devices are digitally controlled analog switches utilizing advanced silicon-gate CMOS technology. These switches have low "ON" resistance and low "OFF" leakages. They are bidirectional switches, thus any analog input may be used as an output and visa-versa. Also the MM74HC4066 switches contain linearization circuitry which lowers the "ON" resistance with digital control signals of the same range. Each switch has its own control input which disables switch when I OW AT WWW.100Y.COM.TV WWW.100Y.COM. WWW.100Y.COM WWW.100Y.C WWW.100Y.COM.TW diodes to V_{CC} and ground. WWW.100Y.COM

Ordering Information

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MM74HC4066MTC MTC14 14-Lead Thin Shrink Small Outline Package (TSSOP), JEDEC MO-153, 4.4mm Wide MM74HC4066N N14A 14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300" Wide	Order Number	Package Number	Package Description
MM74HC4066MTC MTC14 14-Lead Thin Shrink Small Outline Package (TSSOP), JEDEC MO-153, 4.4mm Wide MM74HC4066N N14A 14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300" Wide	MM74HC4066M	M14AN	
4.4mm Wide MM74HC4066N N14A 14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300" Wide	MM74HC4066SJ	M14D	14-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide
MM74HC4066N N14A 14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300" Wide Device also available in Tape and Reel. Specify by appending suffix letter "X" to the ordering number.	MM74HC4066MTC	MTC14	
Device also available in Tape and Reel. Specify by appending suffix letter "X" to the ordering number.	MM74HC4066N	N14A	14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300" Wide
This coult is a contract of the contract of th	Device also available i	n Tape and Reel	. Specify by appending suffix letter "X" to the ordering number.
All packages are lead free per JEDEC: J-STD-020B standard.	All packages are le	ead free per JED	EC: J-STD-020B standard. 1007 COM.TW

Device also available in Tape and Reel. Specify by appending suffix letter "X" to the ordering number. WWW.100Y.COM.TW



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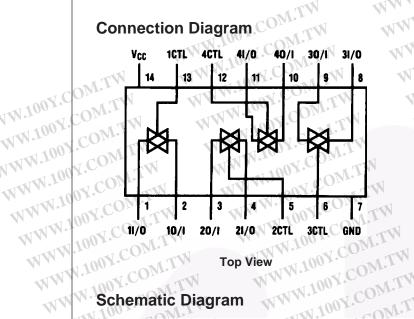
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Top View

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W

V

Input	Switch
CTL	1/0-0/I
Thoy.co	"OFF"
HOY.C	"ON"
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11 1 1 1 1 1 1 1 1	
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-137	at COND

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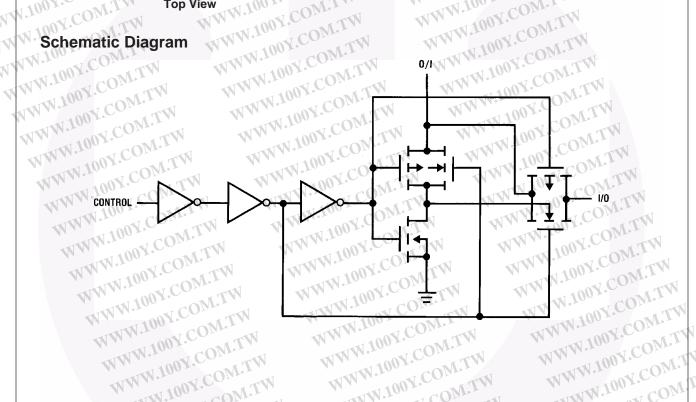
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WWW.100Y.COM.TW **Schematic Diagram**



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WWW.100Y.COM.TW Absolute Maximum Ratings⁽¹⁾
Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. WW

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CO_{J}	Symbol	Parameter	NAMA TOOX.COM	Rating
CC	V _{CC}	Supply Voltage	MAIN. TOOX.CO.	-0.5 to +15V
J C	V_{IN}	DC Control Input Voltage	MMM.TCO	-1.5 to V _{CC} +1.5V
¥.	Vout	DC Switch I/O Voltage	WWW.TooY.C	V_{EE} –0.5 to V_{CC} +0.5 V
0x.	I _{IK} , I _{OK}	Clamp Diode Current	MW.100	±20mA
00,	lout	DC Output Current, per pin	W.100 1.	±25mA
100	Icc	DC V _{CC} or GND Current, per pin	W 1.100x	±50mA
 10	T _{STG}	Storage Temperature Range	WW.100	−65°C to +150°C
V. 2	PD PD	Power Dissipation	WW 110	Y.COM.TW
W.	ON CON		N. M. M.	600mW
NW	Jun CO		W WWW.	500mW
	1.104	Lead Temperature (Soldering 10 seconds)	WW.	260°C
	CO	V _{CC} V _{IN} V _{OUT} I _{IK} , I _{OK} I _{OUT} I _{CC} T _{STG}	V _{CC} Supply Voltage V _{IN} DC Control Input Voltage V _{OUT} DC Switch I/O Voltage I _{IK} , I _{OK} Clamp Diode Current I _{OUT} DC Output Current, per pin I _{CC} DC V _{CC} or GND Current, per pin T _{STG} Storage Temperature Range	V _{CC} Supply Voltage V _{IN} DC Control Input Voltage V _{OUT} DC Switch I/O Voltage I _{IK} , I _{OK} Clamp Diode Current I _{OUT} DC Output Current, per pin I _{CC} DC V _{CC} or GND Current, per pin T _{STG} Storage Temperature Range P _D Power Dissipation Note 2 S.O. Package only

Notes:

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2. Power Dissipation temperature derating — plastic "N" package: –12mW/°C from 65°C to 85°C. WWW.100Y.COM.TW

WWW.100Y.COM.TW Recommended Operating Conditions

The Recommended Operating Conditions 4211
operating conditions Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the describ operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to absolute maximum ratings. MM ... TOOX.CC

Symbol	Para	nmeter	Min.	Max.	Units
V _{CC}	Supply Voltage	NWW.IOV.COM. TW	2	12	V
V _{IN} , V _{OUT}	DC Input or Output Voltage	WWW.100 COM.	0	V_{CC}	COV
T _A	Operating Temperature Range	COM.	-40	+85	Co
t _r , t _f	Input Rise or Fall Times V _{CC} = 2.0V	WWW.100Y.COM.TW	W	1000	ns on .
	V _{CC} = 4.5V	WWW.TIOOY.COM.TW	7	500	ns ns
	V _{CC} = 6.0V	WWW. 100Y.CO. TITY		400	ns
	WWW.100Y.COM.TW	WWW.100Y.COM.T	N	WWW	1.100 Y.CO
	WWW.100Y.COM.TW	M.M.M. 100X.COM.	TW	WW	W.1001.C
	W.100 1. COM.1	TWW.Ioo COM	-XX	*XIX	AM.TO

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DC Electrical Characteristics⁽³⁾

	TW	MMM.1007	V.COM	TW WWW	100Y.C	25°C	T _A = -40°C to 85°C	T _A = -55°C to 125°C	
ON	Symbol	Parameter	V _{CC} (V)	Conditions	Тур.	ON.CC	Guaranteed	Limits	Units
	V _{IH}	Minimum HIGH	2.0	WW WY	M.In	1.5	1.5	1.5	V
	MTW	Level Input Voltage	4.5		NW.1	3.15	3.15	3.15	
.0	OM.TW	M.M.	9.0	OM.TW W	LATAN	6.3	5.3	6.3	
0X.		MAN	12.0	WIM		8.4	8.4	8.4	
Y.	COVIL	Maximum LOW	2.0	COLLIN	MIN	0.5	0.5	0.5	V
	COM	Level Input Voltage	4.5	COM.TW	WW	1.35	1.35	1.35	
00°	O RON		9.0	Y.COM.TW		2.7	2.7	2.7	
100	MON.	TW	12.0	Y.COM.TW	-41	3.6	3.60	3.6	
J 19	RON	Maximum "ON"	4.5	$V_{CTL} = V_{IH}, I_{S} = 2.0 \text{mA},$	100	170	200	220	Ω
,	OUX.CO	Resistance ⁽⁴⁾	9.0	V _{IS} = V _{CC} to GND (Figure 1)	50	85	10 105	110	
W.	100Y.CC	TW	12.0	(i iguie i)	30	70	85	90	
W	.Too Y.C	OM.TW COM.TW	2.0	$V_{CTL} = V_{IH}, I_S = 2.0 \text{mA},$	120	180	215	240	
W	V.100	COM.TW	4.5	V _{IS} = V _{CC} or GND (Figure 1)	50	80	100	120	
- 1			9.0	(Figure) i)	35	60	75	CO 80	
NA	W.1007	CONTY	12.0	(Figure 1)	20	40	60	CO70	J
W	R _{ON}	Maximum "ON"	4.5		10	15	20.1	20	Ω
V	77 77	Resistance Matching	9.0	$V_{CTL} = V_{IH},$ $V_{IS} = V_{CC}$ to GND	5	10	15 10	15	N
-	MMN.	Matching	12.0	WWW.100Y.COM	5	10	15	15	LM
	IN	Maximum Control	V	$V_{IN} = V_{CC}$ or GND,	M.TV	±0.1	±1.0	±1.0	μA
	TAN W	Input Current	N.	V _{CC} = 2-6V	- 100	N	MW.	Lany COM	
	I _{IZ}	Maximum Switch "OFF" Leakage	6.0	$V_{OS} = V_{CC}$ or GND, $V_{IS} = GND$ or V_{CC} ,	10	±60	±600	±600	nA
	WW	Current	9.0	$V_{IS} = GND \text{ of } V_{CC}$, $V_{CTL} = V_{IL} \text{ (Figure 3)}$	15	±80	±800	1 ±800	M.
	WW	100Y.COM	12.0	M. 1007.	20	±100	±1000	±1000	om^{Ω}
	I _{IZ}	Maximum Switch	6.0	$V_{IS} = V_{CC}$ to GND,	10	±40	±150	±150	nA
	N/	"ON" Leakage Current	9.0	$V_{CTL} = V_{IH},$ $V_{OS} = OPEN (Figure 2)$	15	±50	±200	±200	
		MM.Ing C	12.0	I WWW.re	20	±60	±300	±300	.COA
	Icc	Maximum	6.0	$V_{IN} = V_{CC}$ or GND,	o√J C	2.0	20	40	μA
		Quiescent Supply Current	9.0	$I_{OUT} = 0\mu A$	00 1.	4.0	40	80	ov.C
		WW 1007.	12.0	M MM	100 X.	8.0	80	160	10 7.

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- 3. For a power supply of 5V ±10% the worst case on resistance (R_{ON}) occurs for HC at 4.5V. Thus the 4.5V values should be used when designing with this supply. Worst case V_{III} and V_{III} occur at V_{II} = 5.5V and 4.5V. should be used when designing with this supply. Worst case V_{IH} and V_{IL} occur at V_{CC} = 5.5V and 4.5V respectively. (The V_{IH} value at 5.5V is 3.85V.) The worst case leakage current occurs for CMOS at the bisher.
- Therefore it is recommended that these devices be used to transmit digital only when using these supply voltages. 4. At supply voltages (V_{CC}-GND) approaching 2V the analog switch on resistance becomes extremely non-linear. WWW.100 COM.TW WWW.100Y.CC WWW.100Y.COM.TW WWW.100Y.COM.T

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oN co	WT.	ctrical Character V-6.0V V _{EE} = 0V-12V,	COM.T	W WWW.	140 X	25°C	T _A = -40°C to 85°C	T _A = -55°C to 125°C	
U 14	Symbol	Parameter	V _{CC} (V)	Conditions	Тур.		Guaranteed	Limits	Units
30	t _{PHL} , t _{PLH}	Maximum Propagation	2.0V	TW	25	50	30	75	ns
.C(OM.TW	Delay Switch In to Out	4.5V	WW WIN	5	10	13	15	
J.C	OM	NWW.10	9.00	W WY	4	87.	10	12	
Y.C	COM.TY	MW.	12.0V	M. T	3	7	CO 11	13	
DX.	t_{PZL}, t_{PZH}	Maximum Switch Turn	102.0V	$R_L = 1k\Omega$	30	100	125	150	ns
00	Y.COM	"ON" Delay	4.5V	COM.TW	12	20	25	30	
100	Y.Com	TW WW	9.0V	COM.TW	6	12	15	18	
.46	oy.CON	WW WY	12.0V		5	10	13	15	
1.1	t_{PHZ} , t_{PLZ}	Maximum Switch Turn "OFF" Delay	2.0V	$R_L = 1k\Omega$	60	168	210	252	ns
W.	~ ✓ C	OFF Delay	4.5V	$R_L = 1k\Omega$	25	36	45	54	
W	.100Y.CG	OW.IV	9.0V	OV.COM.TW	20	32	40 CO	48	
√ 1 1	V.100Y.C	COM.TW		OOY. COM.TV	15	30	1038	45	
W.	f _{MAX}	Minimum Frequency Response (Figure 7)	4.5V	$R_L = 600\Omega$,	40	M.	W.100 Y.	OMITWO	MHz
W	100	20 log $(V_O/V_I) = -3dB$	9.0V	$V_{IS} = 2 V_{PP} \text{ at}$ $(V_{CC}/2)^{(5)(6)}$	100	W	100Y.	T.MOT	
W	MM.	Crosstalk Between	4.5V	$R_1 = 600\Omega$.	-52	V	V 100Y	.Com.TW	dB
V	WW.10	any Two Switches (Figure 8)	9.0V	$F = 1MHz^{(6)(7)}$	- 50	1	VV 100	Y.COM.TV	
-	MMM.	Peak Control to Switch	4.5V	$R_L = 600\Omega$, $F = 1MHz$,	100		WW - 10	T.M.T	mV
	WWW.	Feedthrough Noise (Figure 9)	9.0V	C _L = 50pF	250		WW.1	OOX.COM	TW
	MMA	Switch OFF Signal	4.5V	$R_L = 600\Omega, F = 1MHz,$	-42		N W	1007.	dB
	WW	Feedthrough Isolation (Figure 10)	9.0V	V _(CT) V _{IL} (6)(7)	-44	W	WWW	100X.CO	M.TV
	THD	Total Harmonic	4.5V	$R_L = 10k\Omega$, $C_L = 50pF$,	.013		N. W.	N 100Y.	%
	W	Distortion (Figure 11)	9.0V	$F = 1 \text{kHz}, V_{\text{IS}} = 4 V_{\text{PP}},$ $V_{\text{IS}} = 8 V_{\text{PP}}$.008	TW	WW	W.100Y.C	Jow.
	C _{IN}	Maximum Control Input Capacitance	TW	WW.100Y	5	10	10	WW. 100Y.	pF
	C _{IN}	Maximum Switch Input Capacitance	V.I.N	WWW.100	20	M.T	N	VWW.1001	(pF)
	C _{IN}	Maximum Feedthrough Capacitance	OM.TW	V _{CTL} = GND	0.5	COM:	TW	WW 100	pF
	C _{PD}	Power Dissipation Capacitance	COM.T	ttenuation).	1015	COM		WWW.I	p _F

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5. Adjust 0dBm for F = 1kHz (Null R_L/R_{ON} Attenuation). WWW.100Y.COM.TV

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- 6. V_{IS} is centered at V_{CC}/2. WWW.100Y.COM.TW
- 7. Adjust input for 0dBm. WWW.100Y.COM.TW

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WWW.100V.COM.TW

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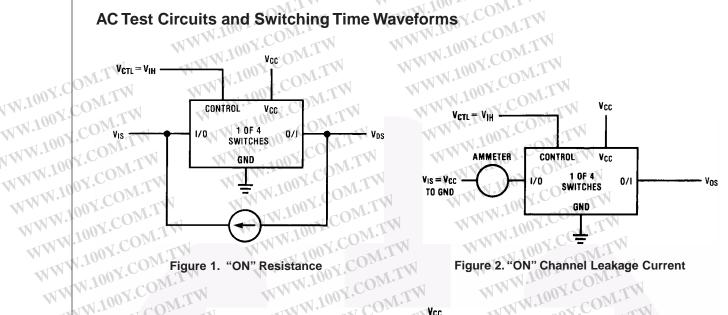
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Figure 2. "ON" Channel Leakage Current

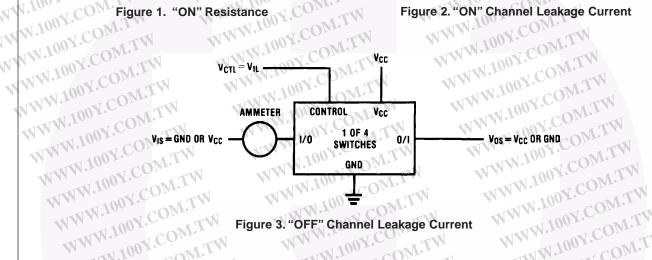


Figure 3. "OFF" Channel Leakage Current

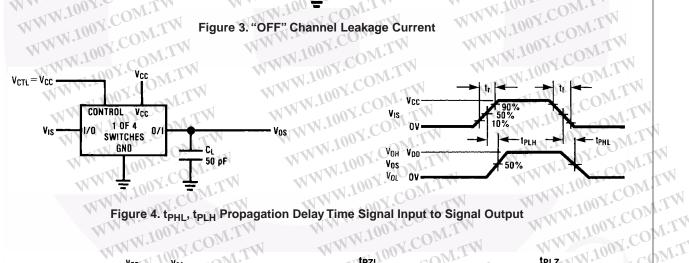


Figure 4. t_{PHL}, t_{PLH} Propagation Delay Time Signal Input to Signal Output

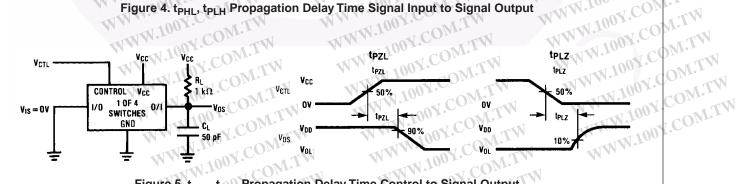
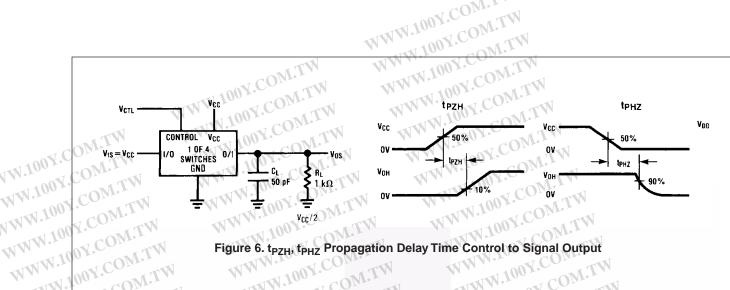


Figure 5. t_{PZL}, t_{PLZ} Propagation Delay Time Control to Signal Output WWW.100Y.CC WWW.100Y.C WWW.100Y.COM.TW



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Figure 6. t_{PZH}, t_{PHZ} Propagation Delay Time Control to Signal Output

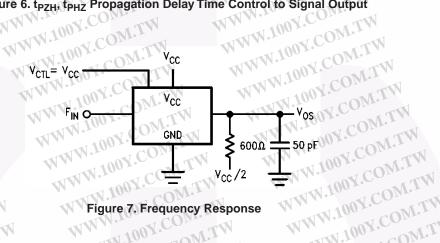
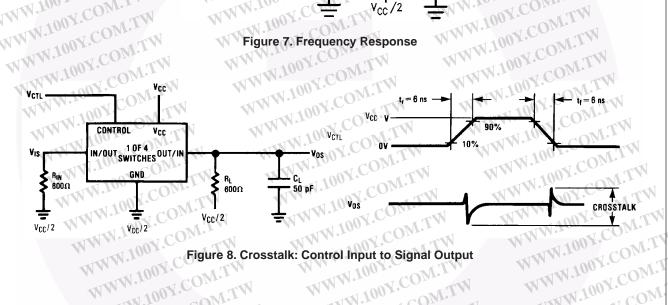


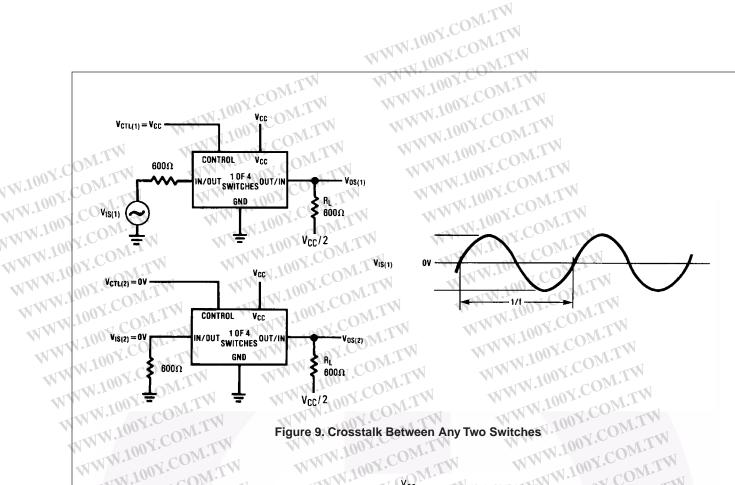
Figure 7. Frequency Response

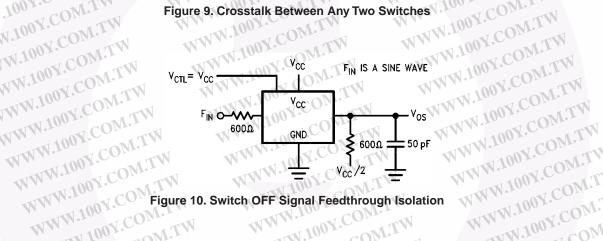


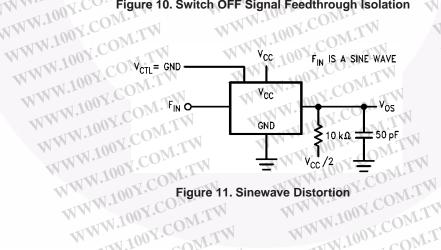
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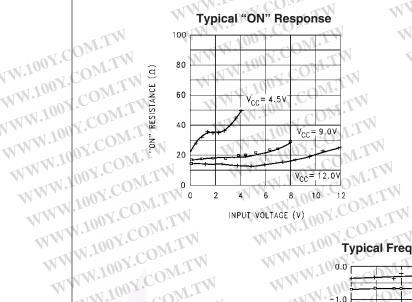
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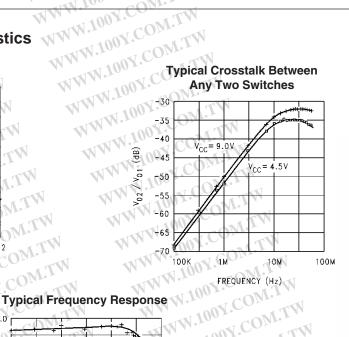
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Typical Performance Characteristics

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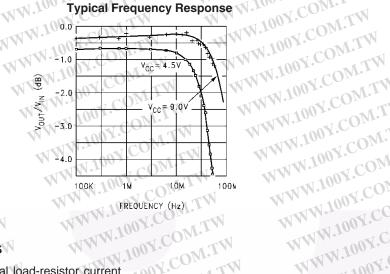
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avoid drawing V_{CC} and signal line components. To avoid drawing V_{CC} current when switch current flows into the analog switch input pins, the voltage drop across the switch must not exceed a switch must not excee switch input pins, the voltage drop across the switch must not exceed 0.6V (calculated from the ON resistance). WWW.100Y.COM.TW WWW.100Y.COM.TW WWW.100Y.COM.T WWW.100Y WWW.100Y.COM.TW WWW.100Y.COM.TW

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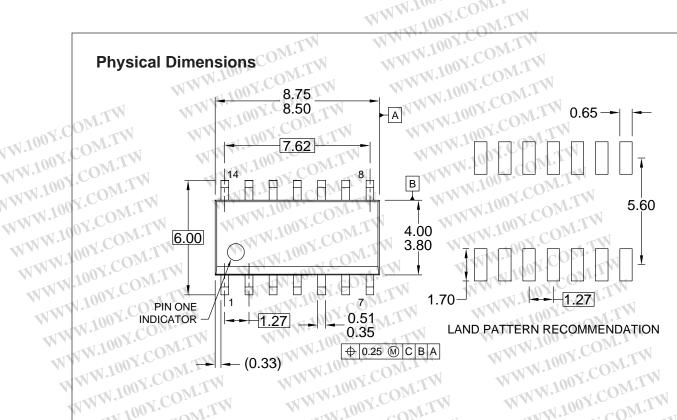
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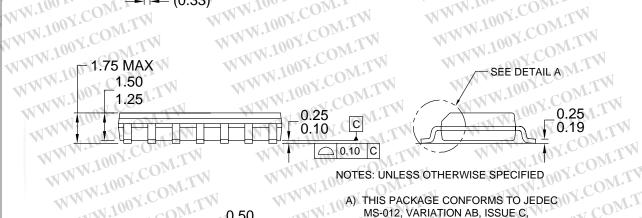
Physical Dimensions COM.TW

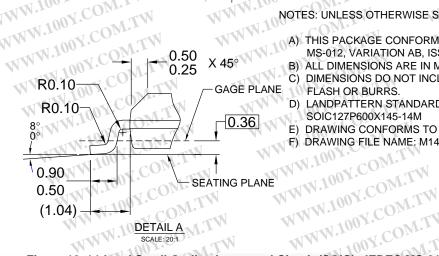
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- A) THIS PACKAGE CONFORMS TO JEDEC
 MS-012, VARIATION AB, ISSUE C

 X 45°
 B) ALL DIMENSIONAL INIS-U12, VARIATION AB, ISSUE C,
 B) ALL DIMENSIONS ARE IN MILLIMETERS.
 C) DIMENSIONS DO NOT INCLUDE MODERN
 FLASH OR SOME

 - W.100Y.COM.TW FLASH OR BURRS.
 - D) LANDPATTERN STANDARD: E) DRAWING CONFORMS TO ASME Y14.5M-1994
 F) DRAWING FILE NAME: M144REV/12
 - WWW.100Y.COM.7

Figure 12. 14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150" Narrow awings are provided as a service to customers considering Exist. Package drawings are provided as a service to customers considering Fairchild components. Drawings may change in any manner without notice. Please note the revision and/or date on the drawing and contact a Fairchild Semiconductor representative to verify or obtain the most recent revision. Package specifications do not expand the terms of Fairchild's worldwide terms and conditions, specifically the warranty therein, which covers Fairchild products.

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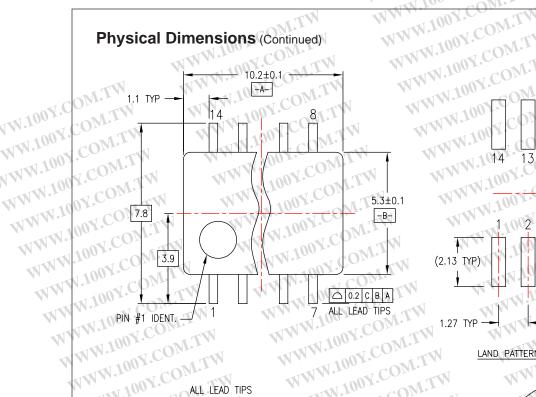
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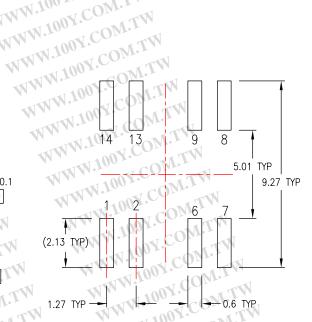
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Physical Dimensions (Continued)

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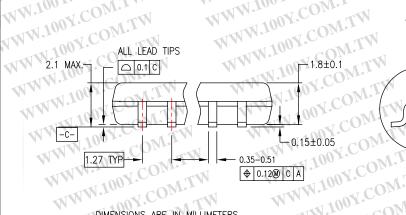


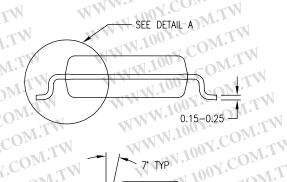
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LAND PATTERN RECOMMENDATION

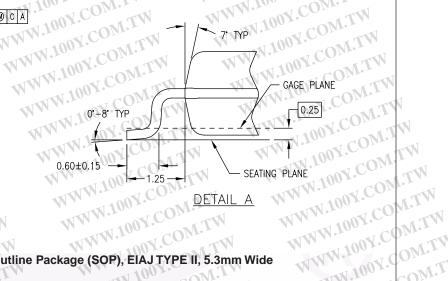




DIMENSIONS ARE IN MILLIMETERS

- NOTES: WW.1007.COM. OTES:
 CONFORMS TO EIAJ EDR-7320 REGISTRATION,
 ESTABLISHED IN DECEMBER, 1998.
 DIMENSIONS ARE IN MILLIMETERS.
 DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD
 FLASH, AND TIE BAR EXTRUSIONS.
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M14DREVC

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W.100Y.COM.TW WWW.100Y.COM.TW WWW.100Y.COM.T WWW.100Y.COM.TW Physical Dimensions (Continued) VW.100Y.COM.TW 5.0±0.1 WWW.1005 0.65 0.43 TYP OM.TW .COM.TW WWW.100 Y.COM.TW WWW.190Y.COM.T 6.4 4.4±0.1 WWW.Jooy.COM.T -B-WWW.100Y.COM 3.2 WWW.100Y.CO! 0.2 CBA ALL LEAD TIPS PIN #1 IDENT. W.100Y.COM: 6.10 LAND PATTERN RECOMMENDATION SEE DETAIL A **ALL LEAD TIPS** 0.90+0.15 1.2 MAX 0.1 C 0.09-0.20 W V-C-0.10±0.05 WWW.100 V 0.65 0.19 - 0.30⊕ 0.13M A BS CS 12.00°TOP & BOTTOM WWW.100Y.COM.TW R0.09 min W.100Y.COM.TW **GAGE PLANE** 0.25 NOTES: 0.6±0.1 A. CONFORMS TO JEDEC REGISTRATION MO-153. SEATING PLANE WWW.100Y.COM.TW VARIATION AB, REF NOTE 6 B. DIMENSIONS ARE IN MILLIMETERS WWW.100Y.COM.TV **DETAIL A** C. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, WWW.100Y.COM.T AND TIE BAR EXTRUSIONS

WWW.100Y.COM.TW

WWW.100Y.COM Figure 14. 14-Lead Thin Shrink Small Outline Package (TSSOP), JEDEC MO-153, 4.4mm Wide

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E. LANDPATTERN STANDARD: SOP65P640X110-14M

F. DRAWING FILE NAME: MTC14REV6

Y14.5M, 1982

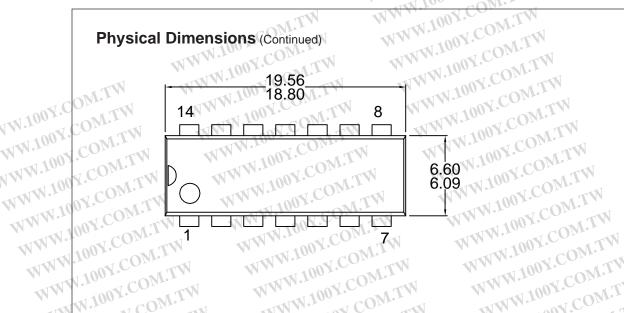
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Physical Dimensions (Continued)

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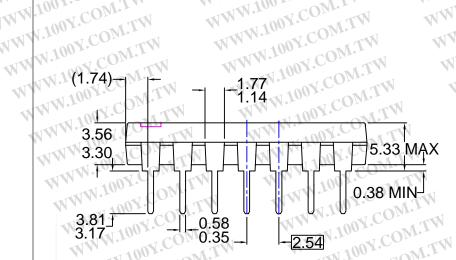
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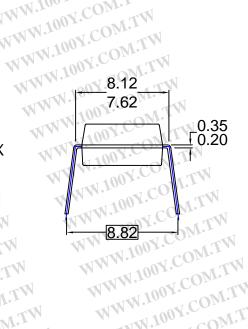


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NOTES: UNLESS OTHERWISE SPECIFIED THIS PACKAGE CONFORMS TO

- B) ALL DIMENSIONS ARE IN MILLIMETERS.

 DIMENSIONS ARE EXC. DIMENSIONS ARE EXCLUSIVE OF BURRS,
 MOLD FLASH, AND THE BAD EXCLUSIVE OF BURRS,
- C) MOLD FLASH, AND TIE BAR EXTRUSIONS.

 D) DIMENSIONS AND TO: --
- E) DRAWING FILE NAME: MKT-N14AREV7 D) DIMENSIONS AND TOLERANCES PER

WWW.100Y.CO. Figure 15. 14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300" Wide

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