# 5V／3．3V or Adjustable，Low－Dropout， Low IQ，500mA Linear Regulators 

## General Description

The MAX603／MAX604 low－dropout，low quiescent cur－ rent，linear regulators supply $5 \mathrm{~V}, 3.3 \mathrm{~V}$ ，or an adjustable output for currents up to 500 mA ．They are available in a 1.8 W SO package．Typical dropouts are 320 mV at 5 V and 500 mA ，or 240 mV at 3.3 V and 200 mA ．Quiescent currents are $15 \mu \mathrm{~A}$ typ and $35 \mu \mathrm{~A}$ max．Shutdown turns off all circuitry and puts the regulator in a $2 \mu \mathrm{~A}$ off mode． A unique protection scheme limits reverse currents when the input voltage falls below the output．Other fea－ tures include foldback current limiting and thermal overload protection．
The output is preset at 3.3 V for the MAX604 and 5 V for the MAX603．In addition，both devices employ Dual Mode ${ }^{\text {TM }}$ operation，allowing user－adjustable outputs from 1.25 V to 11 V using external resistors．The input voltage supply range is 2.7 V to 11.5 V ．
The MAX603／MAX604 feature a 500mA P－channel MOSFET pass transistor．This transistor allows the devices to draw less than $35 \mu \mathrm{~A}$ over temperature，inde－ pendent of the output current．The supply current remains low because the P－channel MOSFET pass tran－ sistor draws no base currents（unlike the PNP transis－ tors of conventional bipolar linear regulators）．Also， when the input－to－output voltage differential becomes small，the internal P－channel MOSFET does not suffer from excessive base current losses that occur with sat－ urated PNP transistors．

Applications
5 V and 3．3V Regulators
1.25 V to 11 V Adjustable Regulators

Battery－Powered Devices
Pagers and Cellular Phones
Portable Instruments
Solar－Powered Instruments
－500mA Output Current，with Foldback Current Limiting
－High－Power（1．8W）8－Pin SO Package
－Dual Mode ${ }^{\text {TM }}$ Operation：Fixed or Adjustable Output from 1．25V to 11V
－Large Input Range（2．7V to 11．5V）
－Internal 500mA P－Channel Pass Transistor
－15 AA Typical Quiescent Current
－ $2 \mu \mathrm{~A}$（Max）Shutdown Mode
－Thermal Overload Protection
－Reverse－Current Protection

## Ordering Information

| PART | TEMP．RANGE | PIN－PACKAGE |
| :--- | :---: | :--- |
| MAX603CPA | $0^{\circ} \mathrm{C}$ to $+70^{\circ} \mathrm{C}$ | 8 8 Plastic DIP |
| MAX603CSA | $0^{\circ} \mathrm{C}$ to $+70^{\circ} \mathrm{C}$ | 8 SO |
| MAX603C／D | $0^{\circ} \mathrm{C}$ to $+70^{\circ} \mathrm{C}$ | Dice |
| MAX603EPA | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | 8 Plastic DIP |
| MAX603ESA | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | 8 SO |
| MAX603MJA | $-55^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$ | 8 CERDIP＊＊ |
| MAX604CPA | $0^{\circ} \mathrm{C}$ to $+70^{\circ} \mathrm{C}$ | 8 Plastic DIP |
| MAX604CSA | $0^{\circ} \mathrm{C}$ to $+70^{\circ} \mathrm{C}$ | 8 SO |
| MAX604C／D | $0^{\circ} \mathrm{C}$ to $+70^{\circ} \mathrm{C}$ | Dice |
| MAX604EPA | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | 8 Plastic DIP |
| MAX604ESA | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | 8 SO |
| MAX604MJA | $-55^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$ | 8 CERDIP＊＊ |

＊Dice are tested at $T_{A}=+25^{\circ} \mathrm{C}, D C$ parameters only．
＊＊Contact factory for availability．

Pin Configuration

## TOP VIEW


${ }^{\text {TM }}$ Dual Mode is a trademark of Maxim Integrated Products．

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## ABSOLUTE MAXIMUM RATINGS



Operating Temperature Ranges
$\qquad$
$\qquad$
$\qquad$
Junction Temperature ．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．．． $150^{\circ} \mathrm{C}$
Storage Temperature Range ．．．．．．．．．．．．．．．．．．．．．．．．．．．．． $65^{\circ} \mathrm{C}$ to $+160^{\circ} \mathrm{C}$
Lead Temperature（soldering，10sec）．．．．．．．．．．．．．．．．．．．．．．．．．．．．．$+300^{\circ} \mathrm{C}$

Stresses beyond those listed under＂Absolute Maximum Ratings＂may cause permanent damage to the device．These are stress ratings only，and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied．Exposure to absolute maximum rating conditions for extended periods may affect device reliability．

## ELECTRICAL CHARACTERISTICS

$\left(\mathrm{V}_{\mathrm{IN}}=6 \mathrm{~V}\right.$（MAX603）or 4.3 V （MAX604）， $\mathrm{C}_{\mathrm{IN}}=\mathrm{COUT}=10 \mu \mathrm{~F}, \mathrm{OFF}=\mathrm{V}_{\mathrm{IN}}, \mathrm{SET}=\mathrm{GND}, \mathrm{T}_{\mathrm{J}}=\mathrm{T}_{\mathrm{MIN}}$ to $\mathrm{T}_{\mathrm{MAX}}$ ，unless otherwise noted． Typical values are at $\mathrm{T}_{\mathrm{J}}=+25^{\circ} \mathrm{C}$ ．）（Note 1）

| PARAMETER | SYMBOL | CONDITIONS |  | MIN | TYP | MAX | UNITS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Input Voltage | V IN | $\mathrm{SET}=\mathrm{OUT}, \mathrm{R}_{\mathrm{L}}=1 \mathrm{k} \Omega$ | MAX60＿C | 2.7 |  | 11.5 | V |
|  |  |  | MAX60＿E | 2.9 |  | 11.5 |  |
|  |  |  | MAX60＿M | 3.0 |  | 11.5 |  |
| Output Voltage（Note 2） | Vout | $\begin{aligned} & \text { IOUT }=20 \mu \mathrm{~A} \text { to } 500 \mathrm{~mA}, \\ & 6.0 \mathrm{~V}<\mathrm{V}_{\text {IN }}<11.5 \mathrm{~V} \end{aligned}$ | MAX603 | 4.75 | 5.00 | 5.25 | V |
|  |  | $\begin{aligned} & \text { IOUT }=20 \mu \mathrm{~A} \text { to } 300 \mathrm{~mA}, \\ & 4.3 \mathrm{~V}<\mathrm{V} \text { IN }<11.5 \mathrm{~V} \end{aligned}$ | MAX604 | 3.15 | 3.30 | 3.45 |  |
| Load Regulation | $\Delta \mathrm{V}$ LDR | lout $=1 \mathrm{~mA}$ to 500 mA | MAX603C／E |  | 60 | 100 | mV |
|  |  |  | MAX603M |  |  | 150 |  |
|  |  | $\left(\mathrm{V}_{\text {OUT }}+0.5 \mathrm{~V}\right) \leq \mathrm{V}_{\text {IN }} \leq 11.5 \mathrm{~V} \text {, IOUT }=25 \mathrm{~mA}$ |  |  | 30 | 100 |  |
| Line Regulation | $\Delta \mathrm{V}_{\text {LNR }}$ |  |  |  | 7 | 40 | mV |
| Dropout Voltage（Note 3） | $\Delta \mathrm{V}_{\text {DO }}$ | IOUT $=200 \mathrm{~mA}$ | MAX603 |  | 130 | 220 | mV |
|  |  | IOUT $=500 \mathrm{~mA}$ |  |  | 320 | 550 |  |
|  |  | IOUT $=200 \mathrm{~mA}$ | MAX604 |  | 240 | 410 |  |
|  |  | IOUT $=400 \mathrm{~mA}$ |  |  | 480 | 820 |  |
| Quiescent Current | lQ | $3.0 \mathrm{~V} \leq \mathrm{V}$ IN $\leq 11.5 \mathrm{~V}, \mathrm{SET}=\mathrm{OUT}$ | MAX60＿C／E |  | 15 | 35 | $\mu \mathrm{A}$ |
|  |  |  | MAX60＿M |  |  | 40 |  |
| OFF Quiescent Current | lQ OFF | $\begin{aligned} & \mathrm{OFF} \leq 0.4 \mathrm{~V}, \mathrm{R}_{\mathrm{L}}=1 \mathrm{k} \Omega, \\ & (\mathrm{~V} \text { OUT }+1 \mathrm{~V}) \leq \mathrm{V}_{\text {IN }} \leq 11.5 \mathrm{~V} \end{aligned}$ | MAX60＿C |  | 0.01 | 2 | $\mu \mathrm{A}$ |
|  |  |  | MAX60＿E |  |  | 10 |  |
|  |  |  | MAX60＿M |  |  | 20 |  |
| Minimum Load Current | lout min | V IN $=11.5 \mathrm{~V}, \mathrm{SET}=\mathrm{OUT}$ | MAX60＿C |  |  | 2 | $\mu \mathrm{A}$ |
|  |  |  | MAX60＿E |  |  | 6 |  |
|  |  |  | MAX60＿M |  |  | 20 |  |
| Foldback Current Limit （Note 4） | ILIM | V ${ }_{\text {OUT }}<0.8 \mathrm{~V}$ |  |  | 350 |  | mA |
|  |  | VOUT $>0.8 \mathrm{~V}$ and $\mathrm{V}_{\text {IN }}$－ $\mathrm{V}_{\text {OUT }}>0.7 \mathrm{~V}$ |  |  | 1200 |  |  |
| Thermal Shutdown Temperature | TSD |  | － |  | 160 |  | ${ }^{\circ} \mathrm{C}$ |
| Thermal Shutdown Hysteresis | $\Delta \mathrm{TSD}$ | 1 |  |  | 10 |  | ${ }^{\circ} \mathrm{C}$ |

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## ELECTRICAL CHARACTERISTICS（continued）

$\left(\mathrm{V}_{\mathrm{IN}}=6 \mathrm{~V}\right.$（MAX603）or 4.3 V （MAX604）， $\mathrm{C}_{\mathrm{IN}}=\mathrm{C}_{\mathrm{OUT}}=10 \mu \mathrm{~F}, \overline{\mathrm{OFF}}=\mathrm{V}_{\mathrm{IN}}, \mathrm{SET}=\mathrm{GND}, \mathrm{T}_{\mathrm{J}}=\mathrm{T}_{\mathrm{MIN}}$ to $\mathrm{T}_{\mathrm{MAX}}$ ，unless otherwise noted． Typical values are at $\mathrm{T}_{\mathrm{J}}=+25^{\circ} \mathrm{C}$ ．）（Note 1）

| PARAMETER | SYMBOL | CONDITIONS |  | MIN | TYP | MAX | UNITS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Reverse－Current Protection Threshold（Note 5） | $\Delta \mathrm{V}_{\text {RTH }}$ | VOUT $=4.5 \mathrm{~V}$ | MAX603 |  | 6 | 20 | mV |
|  |  | VOUT $=3.0 \mathrm{~V}$ | MAX604 |  | 6 | 20 |  |
| Reverse Leakage Current | IRVL | $\mathrm{V}_{\mathrm{IN}}=0 \mathrm{~V}, \mathrm{~V}_{\mathrm{OUT}}=4.5 \mathrm{~V}$（MAX603） <br> VOUT $=3.0 \mathrm{~V}$（MAX604） | MAX60＿C |  | 0.01 | 10 | $\mu \mathrm{A}$ |
|  |  |  | MAX60＿E |  |  | 20 |  |
|  |  |  | MAX60＿M |  |  | 100 |  |
| Start－Up Overshoot | VOSH | $\mathrm{R}_{\mathrm{L}}=1 \mathrm{k} \Omega$ ，Cout $=10 \mu \mathrm{~F}$ ，OFF rise time $\leq 1 \mu \mathrm{~s}$ |  | 2 |  |  | \％VOUT |
| Time Required to Exit Shutdown | tstart | $\mathrm{V}_{\mathrm{IN}}=9 \mathrm{~V}, \mathrm{R}_{\mathrm{L}}=18 \Omega, \mathrm{~V} \overline{\mathrm{OFF}}$ switched from OV to VIN，time from $0 \%$ to $95 \%$ of VOUT |  | 200 |  |  | $\mu \mathrm{s}$ |
| Dual－Mode SET Threshold | $V_{\text {SET TH }}$ | For internal feedback |  |  | 80 | 30 | mV |
|  |  | For external feedback |  | 150 | 80 |  |  |
| SET Reference Voltage | $\mathrm{V}_{\text {SET }}$ | SET＝OUT， $\mathrm{R}_{\mathrm{L}}=1 \mathrm{k} \Omega$ |  | 1.16 | 1.20 | 1.24 | V |
| SET Input Leakage Current | ISET | V SET $=1.5 \mathrm{~V}$ or 0 V |  |  | $\pm 0.01$ | $\pm 10$ | nA |
| OUT Leakage Current | Iout LKG | $\begin{aligned} & \mathrm{V} \text { IN }=11.5 \mathrm{~V}, \mathrm{~V}_{\text {OUT }}=2 \mathrm{~V}, \\ & \text { SET }=\mathrm{OUT} \end{aligned}$ | MAX60＿C |  | 0.01 | 2 | $\mu \mathrm{A}$ |
|  |  |  | MAX60＿E |  |  | 6 |  |
|  |  |  | MAX60＿M |  |  | 20 |  |
| $\overline{\text { OFF }}$ Threshold Voltage | $\mathrm{V}_{\mathrm{IL}} \overline{\mathrm{OFF}}$ | Off |  |  |  | 0.4 | V |
|  | VIH $\overline{\text { OFF }}$ | On，SET＝OUT， $\mathrm{V}^{\text {IN }}=4 \mathrm{~V}$ |  | 2.0 |  |  |  |
|  |  | On，SET＝OUT， $\mathrm{V}_{\text {IN }}=6 \mathrm{~V}$ |  | 3.0 |  |  |  |
|  |  | On，SET＝OUT， $\mathrm{V}_{\text {IN }}=11.5 \mathrm{~V}$ |  | 4.0 |  |  |  |
| $\overline{\text { OFF }}$ Input Leakage Current | IOFF | V $\overline{\text { OFF }}=\mathrm{V}_{\text {IN }}$ or GND |  |  | $\pm 0.01$ | $\pm 10$ | nA |
| Output Noise（Note 6） | $e_{n}$ | $\begin{aligned} & 10 \mathrm{~Hz} \text { to } 10 \mathrm{kHz}, \mathrm{SET}=\mathrm{OUT}, \mathrm{RL}=1 \mathrm{k} \Omega \text {, } \\ & \text { COUT }=10 \mu \mathrm{~F} \end{aligned}$ |  |  | $250$ |  | $\mu \mathrm{V}_{\mathrm{RMS}}$ |

Note 1：Electrical specifications are measured by pulse testing and are guaranteed for a junction temperature（ $\mathrm{T}_{\mathrm{J}}$ ）equal to the operating temperature range．C and E grade parts may be operated up to a $T_{J}$ of $+125^{\circ}$ ．Expect performance similar to M grade specifications．For $\mathrm{T}_{J}$ between $+125^{\circ} \mathrm{C}$ and $+150^{\circ} \mathrm{C}$ ，the output voltage may drift more．
Note 2：（ $\mathrm{V}_{\text {IN }}-\mathrm{V}_{\text {OUT }}$ ）is limited to keep the product（IOUT $\times\left(\mathrm{V}_{\text {IN }}-\mathrm{V}_{\text {OUT }}\right)$ ）from exceeding the package power dissipation limits．
Note 3：Dropout Voltage is（ $\mathrm{V}_{\mathrm{IN}}-\mathrm{V}_{\text {OUT }}$ ）when VOUT falls to 100 mV below its nominal value at $\mathrm{V}_{\text {IN }}=\mathrm{V}_{\text {OUT }}+2 \mathrm{~V}$ ．For example，the MAX603 is tested by measuring the VOUT at $\mathrm{V}_{\text {IN }}=7 \mathrm{~V}$ ，then $\mathrm{V}_{\text {IN }}$ is lowered until VOUT falls 100 mV below the measured value． The difference（ $\mathrm{V}_{\text {IN }}$－ $\mathrm{V}_{\text {OUT }}$ ）is then measured and defined as $\Delta \mathrm{V}_{\mathrm{DO}}$ ．
Note 4：Foldback Current Limit was characterized by pulse testing to remain below the maximum junction temperature．
Note 5：The Reverse－Current Protection Threshold is the output／input differential voltage（VOUT－VIN）at which reverse－current protection switchover occurs and the pass transistor is turned off．
Note 6：Noise is tested using a bandpass amplifier with two poles at 10 Hz and two poles at 10 kHz ．

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$\left(\mathrm{V}_{\mathrm{IN}}=7 \mathrm{~V}\right.$ for $\mathrm{MAX603}, \mathrm{~V} \operatorname{IN}=5.3 \mathrm{~V}$ for $\mathrm{MAX604}, \overline{\mathrm{OFF}}=\mathrm{V}_{\mathrm{IN}}, \mathrm{SET}=\mathrm{GND}, \mathrm{CIN}=\mathrm{COUT}=10 \mu \mathrm{~F}, \mathrm{R} \mathrm{L}=1 \mathrm{k} \Omega, \mathrm{T}_{J}=+25^{\circ} \mathrm{C}$ ，unless otherwise noted． ．


# 5V／3．3V or Adjustable，Low－Dropout， Low IQ，500mA Linear Regulators <br> Typical Operating Characteristics（continued） 

$\left(\mathrm{V}_{\mathrm{IN}}=7 \mathrm{~V}\right.$ for $\mathrm{MAX603}, \mathrm{~V} \operatorname{IN}=5.3 \mathrm{~V}$ for $\mathrm{MAX604}, \overline{\mathrm{OFF}}=\mathrm{V}_{\mathrm{IN}}, \mathrm{SET}=\mathrm{GND}, \mathrm{CIN}=\mathrm{COUT}=10 \mu \mathrm{~F}, \mathrm{R} \mathrm{L}=1 \mathrm{k} \Omega, \mathrm{T}_{\mathrm{J}}=+25^{\circ} \mathrm{C}$ ，unless otherwise noted． ．


A：OUTPUT VOLTAGE（ $100 \mathrm{mV} / \mathrm{div}$ ）
B：IOUT $=500 \mathrm{~mA}$（HIGH），IOUT $=5 \mathrm{~mA}$（LOW）

OVERSHOOT AND TIME EXITING SHUTDOWN MODE


A：$\overline{\text { OFF PIN VOLTAGE（1V／div）}}$
RISE TIME $=13 \mu \mathrm{~s}$
B：MAX603 OUTPUT VOLTAGE（1V／div）
DELAY $=4.936 \mathrm{~ms}$, OVERSHOOT $=1 \%$, RISE TIME $=55 \mu \mathrm{~s}$

Pin Description

| PIN | NAME | DESCRIPTION |
| :---: | :---: | :--- |
| 1 | IN | Regulator Input．Supply voltage can range from 2.7 V to 11.5 V ． |
| $2,3,6,7$ | GND | Ground．These pins function as heatsinks，only in the SOIC package．All GND pins must be soldered to the <br> circuit board for proper power dissipation．Connect to large copper pads or planes to channel heat from the IC． |
| 4 | $\overline{\text { OFF }}$ | Shutdown，active low．Switch logic levels in less than $1 \mu \mathrm{~s}$ with the high level above the $\overline{\text { OFF threshold．}}$ |
| 5 | SET | Feedback for Setting the Output Voltage．Connect to GND to set the output voltage to the preselected 3.3 V <br> or 5V．Connect to an external resistor network for adjustable output operation． |
| 8 | OUT | Regulator Output．Fixed or adjustable from 1．25V to 11.0 V ．Sources up to 500 mA for input voltages above 4V． |



Figure 1．Test Circuit

## 5V／3．3V or Adjustable，Low－Dropout， Low IQ，500mA Linear Regulators



Figure 2．Functional Diagram

## Detailed Desc ription

The MAX603／MAX604 are low－dropout，low－quiescent－ current linear regulators designed primarily for battery－ powered applications．They supply an adjustable 1.25 V to 11 V output or a preselected 5 V （MAX603）or 3.3 V （MAX604）output for load currents up to 500 mA ．As illustrated in Figure 2，they consist of a 1.20 V reference， error amplifier，MOSFET driver，P－channel pass transis－ tor，dual－mode comparator，and internal feedback volt－ age divider．
The 1.20 V bandgap reference is connected to the error amplifier＇s inverting input．The error amplifier compares this reference with the selected feedback voltage and amplifies the difference．The MOSFET driver reads the error signal and applies the appropriate drive to the P－ channel pass transistor．If the feedback voltage is lower than the reference，the pass transistor gate is pulled lower，allowing more current to pass and increasing the output voltage．If the feedback voltage is too high，the pass transistor gate is pulled up，allowing less current to pass to the output．

The output voltage is fed back through either an internal resistor voltage divider connected to the OUT pin，or an external resistor network connected to the SET pin．The dual－mode comparator examines the SET voltage and selects the feedback path used．If SET is below 80 mV ， internal feedback is used and the output voltage is regulat－ ed to 5 V for the MAX603 or 3．3V for the MAX604． Additional blocks include a foldback current limiter，reverse current protection，thermal sensor，and shutdown logic．

## Internal P－Channel Pass Transistor

The MAX603／MAX604 feature a 500 mA P－channel MOSFET pass transistor．This provides several advan－ tages over similar designs using PNP pass transistors， including longer battery life．
The P－channel MOSFET requires no base drive，which reduces quiescent current considerably．PNP based regulators waste considerable amounts of current in dropout when the pass transistor saturates．They also use high base－drive currents under large loads．The MAX603／MAX604 do not suffer from these problems and consume only $15 \mu \mathrm{~A}$ of quiescent current under light and heavy loads，as well as in dropout．

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## Output Voltage Selection

The MAX603／MAX604 feature dual－mode operation．In preset voltage mode，the output of the MAX603 is set to 5 V and the output of the MAX604 is set to 3.3 V using internal，trimmed feedback resistors．Select this mode by connecting SET to ground．
In adjustable mode，an output between 1.25 V and 11 V is selected using two external resistors connected as a voltage divider to SET（Figure 3）．The output voltage is set by the following equation：

$$
\mathrm{V}_{\mathrm{OUT}}=\mathrm{V}_{\mathrm{SET}}\left(1+\frac{\mathrm{R} 1}{\mathrm{R} 2}\right)
$$

where $\mathrm{V}_{\text {SET }}=1.20 \mathrm{~V}$ ．To simplify resistor selection：

$$
\mathrm{R} 1=\mathrm{R} 2\left(\frac{\mathrm{~V}_{\mathrm{OUT}}}{\mathrm{~V}_{\mathrm{SET}}}-1\right)
$$

Since the input bias current at SET is nominally zero， large resistance values can be used for R1 and R2 to minimize power consumption without losing accuracy．Up to $1.5 \mathrm{M} \Omega$ is acceptable for R2．Since the VSET tolerance is less than $\pm 40 \mathrm{mV}$ ，the output can be set using fixed resistors instead of trim pots．
In preset voltage mode，impedances between SET and ground should be less than $10 \mathrm{k} \Omega$ ．Otherwise，spurious conditions could cause the voltage at SET to exceed the 80 mV dual－mode threshold．

## Shutdown

A low input on the OFF pin shuts down the MAX603／ MAX604．In the off mode，the pass transistor，control circuit，reference，and all biases are turned off，reduc－ ing the supply current below $2 \mu \mathrm{~A}$ ．$\overline{\mathrm{OFF}}$ should be con－ nected to IN for normal operation．
Use a fast comparator，Schmitt trigger，or CMOS or TTL logic to drive the OFF pin in and out of shutdown．Rise times should be shorter than $1 \mu \mathrm{~s}$ ．Do not use slow RC circuits，leave OFF open，or allow the input to linger between thresholds；these measures will prevent the output from jumping to the positive supply rail in response to an indeterminate input state．
Since the OFF threshold varies with input supply volt－ age（see Electrical Characteristics），do not derive the drive voltage from 3.3 V logic．With $\mathrm{V}_{\mathrm{IN}}$ at 11.5 V ，the high OFF logic level needs to be above 4 V ．

Foldback Current Limiting The MAX603／MAX604 also include a foldback current limiter．It monitors and controls the pass transistor＇s gate voltage，estimating the output current and limiting it to 1.2 A for output voltages above 0.8 V and $\mathrm{V}_{\mathbb{I N}}-\mathrm{V}_{\text {OUT }}$


Figure 3．Adjustable Output Using External Feedback Resistors
$>0.7 \mathrm{~V}$ ．For $\mathrm{V}_{\text {IN }}-\mathrm{V}_{\text {OUT }}<0.7 \mathrm{~V}$（dropout operation），there is no current limit．If the output voltage drops below 0.8 V ，implying a short－circuit condition，the output cur－ rent is limited to 350 mA ．The output can be shorted to ground for one minute without damaging the device if the package can dissipate $\mathrm{V}_{\mathrm{IN}} \times 350 \mathrm{~mA}$ without exceeding $\mathrm{T}_{J}=+150^{\circ} \mathrm{C}$ ．

## Thermal Overload Protection

 Thermal overload protection limits total power dissipa－ tion in the MAX603／MAX604．When the junction temper－ ature exceeds $T_{J}=+160^{\circ} \mathrm{C}$ ，the thermal sensor sends a signal to the shutdown logic，turning off the pass tran－ sistor and allowing the IC to cool．The thermal sensor will turn the pass transistor on again after the IC＇s junc－ tion temperature cools by $10^{\circ} \mathrm{C}$ ，resulting in a pulsed output during thermal overload conditions．Thermal overload protection is designed to protect the MAX603／MAX604 in the event of fault conditions．For continual operation，the absolute maximum junction tem－ perature rating of $\mathrm{T}_{\mathrm{J}}=+150^{\circ} \mathrm{C}$ should not be exceeded．

## Operating Region and Power Dissipation

Maximum power dissipation of the MAX603／MAX604 depends on the thermal resistance of the case and cir－ cuit board，the temperature difference between the die junction and ambient air，and the rate of air flow．The power dissipation across the device is $P=I_{\text {OUT }}\left(\mathrm{V}_{\mathrm{IN}}\right.$－ $\left.\mathrm{V}_{\text {OUT }}\right)$ ．The resulting maximum power dissipation is：

$$
\mathrm{P}_{\mathrm{MAX}}=\left(\frac{\left(\mathrm{T}_{\mathrm{J}}-\mathrm{T}_{\mathrm{A}}\right)}{\left(\theta_{\mathrm{JB}}+\theta_{\mathrm{BA}}\right)}\right)
$$

where $\left(T_{J}-T_{A}\right)$ is the temperature difference between the MAX603／MAX604 die junction and the surrounding

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Figure 4．Typical Maximum Power Dissipation vs．Ground Pad Size．
air，$\theta_{\mathrm{JB}}$（or $\theta_{\mathrm{JC}}$ ）is the thermal resistance of the package chosen，and $\theta_{\mathrm{BA}}$ is the thermal resistance through the printed circuit board，copper traces and other materials to the surrounding air．The 8－pin SOIC package for the MAX603／MAX604 features a special lead frame with a lower thermal resistance and higher allowable power dissipation．The thermal resistance of this package is $\theta \mathrm{JB}=42^{\circ} \mathrm{C} / \mathrm{W}$ ，compared with $\theta \mathrm{JB}=110^{\circ} \mathrm{C} / \mathrm{W}$ for an $8-$ pin plastic DIP package and $\theta \mathrm{JB}=125^{\circ} \mathrm{C} / \mathrm{W}$ for an 8 －pin ceramic DIP package．
The GND pins of the MAX603／MAX604 SOIC package perform the dual function of providing an electrical con－ nection to ground and channeling heat away．Connect all GND pins to ground using a large pad or ground plane．Where this is impossible，place a copper plane on an adjacent layer．The pad should exceed the dimensions in Figure 4.
Figure 4 assumes the IC is an 8 －pin SOIC package，is soldered directly to the pad，has a $+125^{\circ} \mathrm{C}$ maximum junction temperature and a $+25^{\circ} \mathrm{C}$ ambient air tempera－ ture，and has no other heat sources．Use larger pad sizes for other packages，lower junction temperatures， higher ambient temperatures，or conditions where the IC is not soldered directly to the heat－sinking ground pad．
The MAX603／MAX604 can regulate currents up to 500 mA and operate with input voltages up to 11.5 V ，but not simultaneously．High output currents can only be sustained when input－output differential voltages are


Figure 5．Power Operating Regions：Maximum Output Current vs．Differential Supply Voltage
low，as shown in Figure 5．Maximum power dissipation depends on packaging，board layout，temperature，and air flow．The maximum output current is：

$$
\operatorname{lOUT}(\text { max })=\frac{\mathrm{P}_{\text {MAX }} \times\left(\mathrm{T}_{J}-\mathrm{T}_{A}\right)}{\left(\mathrm{V}_{\mathrm{IN}}-\mathrm{V}_{\mathrm{OUT}}\right) \times 100^{\circ} \mathrm{C}}
$$

where PMAX is derived from Figure 4.

## Reverse－Current Protection

The MAX603／MAX604 has a unique protection scheme that limits reverse currents when the input voltage falls below the output．It monitors the voltages on IN and OUT and switches the IC＇s substrate and power bus to

# 5V／3．3V or Adjustable，Low－Dropout， Low IQ，500mA Linear Regulators 



Figure 6．3．3V or 5V Linear－Regulator Application


Figure 7．Power－Supply Rejection Ratio vs．Ripple Frequency
the more positive of the two．The control circuitry can then remain functioning and turn the pass transistor off， limiting reverse currents back through the device．This feature allows a backup regulator or battery pack to maintain $\mathrm{V}_{\text {OUT }}$ when the supply at IN fails．
Reverse－current protection activates when the voltage on IN falls 6 mV （ 20 mV maximum）below the voltage on OUT．Before this happens，currents as high as several milliamperes can flow back through the device．After switchover，typical reverse currents are limited to $0.01 \mu \mathrm{~A}$ for as long as the condition exists．

## Applications Information

Figure 6 illustrates the typical application for the MAX603／MAX604．

## Capacitor Selection and <br> Regulator Stability

Normally，use $0.1 \mu \mathrm{~F}$ to $10 \mu \mathrm{~F}$ capacitors on the input and $10 \mu \mathrm{~F}$ on the output of the MAX603／MAX604．The larger input capacitor values provide better supply－ noise rejection and line－transient response．Improve load－transient response，stability，and power－supply rejection by using large output capacitors．For stable operation over the full temperature range and with load currents up to $500 \mathrm{~mA}, 10 \mu \mathrm{~F}$ is recommended．Using capacitors smaller than $3.3 \mu \mathrm{~F}$ can result in oscillation．

## Noise

The MAX603／MAX604 exhibit $3 m V p-p$ to $4 m V p-p$ of noise during normal operation．This is negligible in most applications．When using the MAX603／MAX604 in appli－ cations that include analog－to－digital converters of greater than 12 bits，consider the ADC＇s power－supply rejection specifications．Refer to the output noise plot in the Typical Operating Characteristics．

## PSRR and Operation from Sources Other than Batteries

The MAX603／MAX604 are designed to deliver low dropout voltages and low quiescent currents in battery－ powered systems．Achieving these objectives requires trading off power－supply noise rejection and swift response to supply variations and load transients． Power－supply rejection is 80 dB at low freqencies and rolls off above 10 Hz ．As the frequency increases above 10 kHz ，the output capacitor is the major contributor to the rejection of power－supply noise（Figure 7）．Do not use power supplies with ripple above 100 kHz ，especial－ ly when the ripple exceeds $100 \mathrm{mVp}-\mathrm{p}$ ．When operating from sources other than batteries，improved supply－ noise rejection and transient response can be achieved by increasing the values of the input and output capaci－ tors，and through passive filtering techniques．The Typical Operating Characteristics show the MAX603／ MAX604 supply and load－transient responses．

Transient Considerations The Typical Operating Characteristics show the MAX603／MAX604 load－transient response．Two compo－ nents of the output response can be observed on the load－transient graphs－a DC shift from the output imped－ ance due to the different load currents，and the transient response．Typical transients for step changes in the load current from 5 mA to 500 mA are 0.2 V ．Increasing the out－ put capacitor＇s value attenuates transient spikes．

# 5V／3．3V or Adjustable，Low－Dropout， Low IQ，500mA Linear Regulators 

Input－Output（Dropout）Voltage
A regulator＇s minimum input－output voltage differential， or dropout voltage，determines the lowest usable supply voltage．In battery－powered systems，this will determine the useful end－of－life battery voltage．Because the MAX603／MAX604 use a P－channel MOSFET pass tran－ sistor，their dropout voltage is a function of $r_{\mathrm{DS}(\mathrm{ON})}$ multi－ plied by the load current（see Electrical Characteristics）．
Quickly stepping up the input voltage from the dropout voltage can result in overshoot．This occurs when the pass transistor is fully on at dropout and the IC is not given time to respond to the supply voltage change． Prevent this by slowing the input voltage rise time．

勝 特 力 材 料 886－3－5753170
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Http：／／www． $100 y$ ．com．tw

Chip Topography


TRANSISTOR COUNT： 111
NO DIRECT SUBSTRATE CONNECTION．THE N－SUBSTRATE IS INTERNALLY SWITCHED BETWEEN THE MORE POSITIVE OF IN OR OUT．

## 5V／3．3V or Adjustable，Low－Dropout， Low IQ，500mA Linear Regulators

Package Information


| DIM | INCHES |  | MILLIMETERS |  |
| :---: | :---: | :---: | :---: | :---: |
|  | MIN | MAX | MIN | MAX |
| A | - | 0.200 | - | 5.08 |
| A1 | 0.015 | - | 0.38 | - |
| A2 | 0.125 | 0.175 | 3.18 | 4.45 |
| A3 | 0.055 | 0.080 | 1.40 | 2.03 |
| B | 0.016 | 0.022 | 0.41 | 0.56 |
| B1 | 0.045 | 0.065 | 1.14 | 1.65 |
| C | 0.008 | 0.012 | 0.20 | 0.30 |
| D1 | 0.005 | 0.080 | 0.13 | 2.03 |
| E | 0.300 | 0.325 | 7.62 | 8.26 |
| E1 | 0.240 | 0.310 | 6.10 | 7.87 |
| e | 0.100 | - | 2.54 | - |
| eA | 0.300 | - | 7.62 | - |
| eB | - | 0.400 | - | 10.16 |
| L | 0.115 | 0.150 | 2.92 | 3.81 |
|  |  |  |  |  |

P PACKAGE PLASTIC
DUAL－IN－LINE

| DIM | PINS | INCHES |  | MILLIMETERS |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | MIN | MAX | MIN | MAX |
| D | 8 | 0.348 | 0.390 | 8.84 | 9.91 |
| D | 14 | 0.735 | 0.765 | 18.67 | 19.43 |
| D | 16 | 0.745 | 0.765 | 18.92 | 19.43 |
| D | 18 | 0.885 | 0.915 | 22.48 | 23.24 |
| D | 20 | 1.015 | 1.045 | 25.78 | 26.54 |
| D | 24 | 1.14 | 1.265 | 28.96 | 32.13 |



| DIM | INCHES |  | MILLIMETERS |  |
| :---: | :---: | :---: | :---: | :---: |
|  | MIN | MAX | MIN | MAX |
| A | 0.053 | 0.069 | 1.35 | 1.75 |
| A1 | 0.004 | 0.010 | 0.10 | 0.25 |
| B | 0.014 | 0.019 | 0.35 | 0.49 |
| C | 0.007 | 0.010 | 0.19 | 0.25 |
| E | 0.150 | 0.157 | 3.80 | 4.00 |
| e | 0.050 |  | 1.27 |  |
| H | 0.228 | 0.244 | 5.80 | 6.20 |
| L | 0.016 | 0.050 | 0.40 | 1.27 |



## S PACKAGE SMALL OUTLINE

| DIM | PINS | INCHES |  | MILLIMETERS |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | MIN | MAX | MIN | MAX |  |
| D | 8 | 0.189 | 0.197 | 4.80 | 5.00 |  |
| D | 14 | 0.337 | 0.344 | 8.55 | 8.75 |  |
| D | 16 | 0.386 | 0.394 | 9.80 | 10.00 |  |
| $21-0041 \mathrm{~A}$ |  |  |  |  |  |  |

## 5V／3．3V or Adjustable，Low－Dropout， Low IQ，500mA Linear Regulators



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