AUTOMOTIVE CURRENT TRANSDUCER
DHAB S/18

Introduction

The DHAB family is best suited for DC, AC, or pulsed currents measurement in high power and low voltage automotive applications. Its contains galvanic isolation between the primary circuit (high power) and the secondary circuit (electronic circuit).

The DHAB family gives you a choice of having different current measuring ranges in the same housing (from ± 20 up to ± 600 A).

Features

- Open Loop transducer using the Hall effect sensor
- Low voltage application
- Unipolar + 5 V DC power supply
- Primary current measuring range up to ± 30 A for range 1 and ± 350 A for range 2
- Maximum rms primary admissible limited by the busbar, the magnetic core or the ASIC temperature T° < + 150°C
- Operating temperature range: - 40°C < T° < + 125°C
- Output voltage: fully ratiometric (in sensitivity and offset) 2 measuring ranges to have a better accuracy.

Advantages

- Good accuracy for high and low current range
- Good linearity
- Low thermal offset drift
- Low thermal sensitivity drift
- Hermetic package.

Automotive applications

- Battery Pack Monitoring
- Hybrid Vehicles
- EV and Utility Vehicles.

Principle of DHAB Family

The open loop transducers use an Hall effect integrated circuit.
The magnetic flux density **B**, contributing to the rise of the Hall voltage, is generated by the primary current $\mathbf{I}_P$ to be measured.
The current to be measured $\mathbf{I}_P$ is supplied by a current source i.e. battery or generator (Fig. 1).
Within the linear region of the hysteresis cycle, **B** is proportional to:

$$\mathbf{B}\ (\mathbf{I}_P) = \text{constant (a)} \times \mathbf{I}_P$$

The Hall voltage is thus expressed by:

$$\mathbf{V}_H = (R_H/d) \times \mathbf{I} \times \text{constant (a)} \times \mathbf{I}_P$$

Except for $\mathbf{I}_P$, all terms of this equation are constant. Therefore:

$$\mathbf{V}_H = \text{constant (b)} \times \mathbf{I}_P$$

The measurement signal $\mathbf{V}_H$ amplified to supply the user output voltage or current.

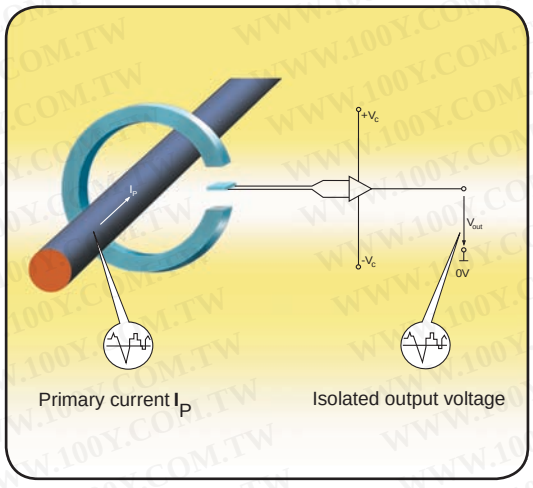

Fig. 1: Principle of the open loop transducer

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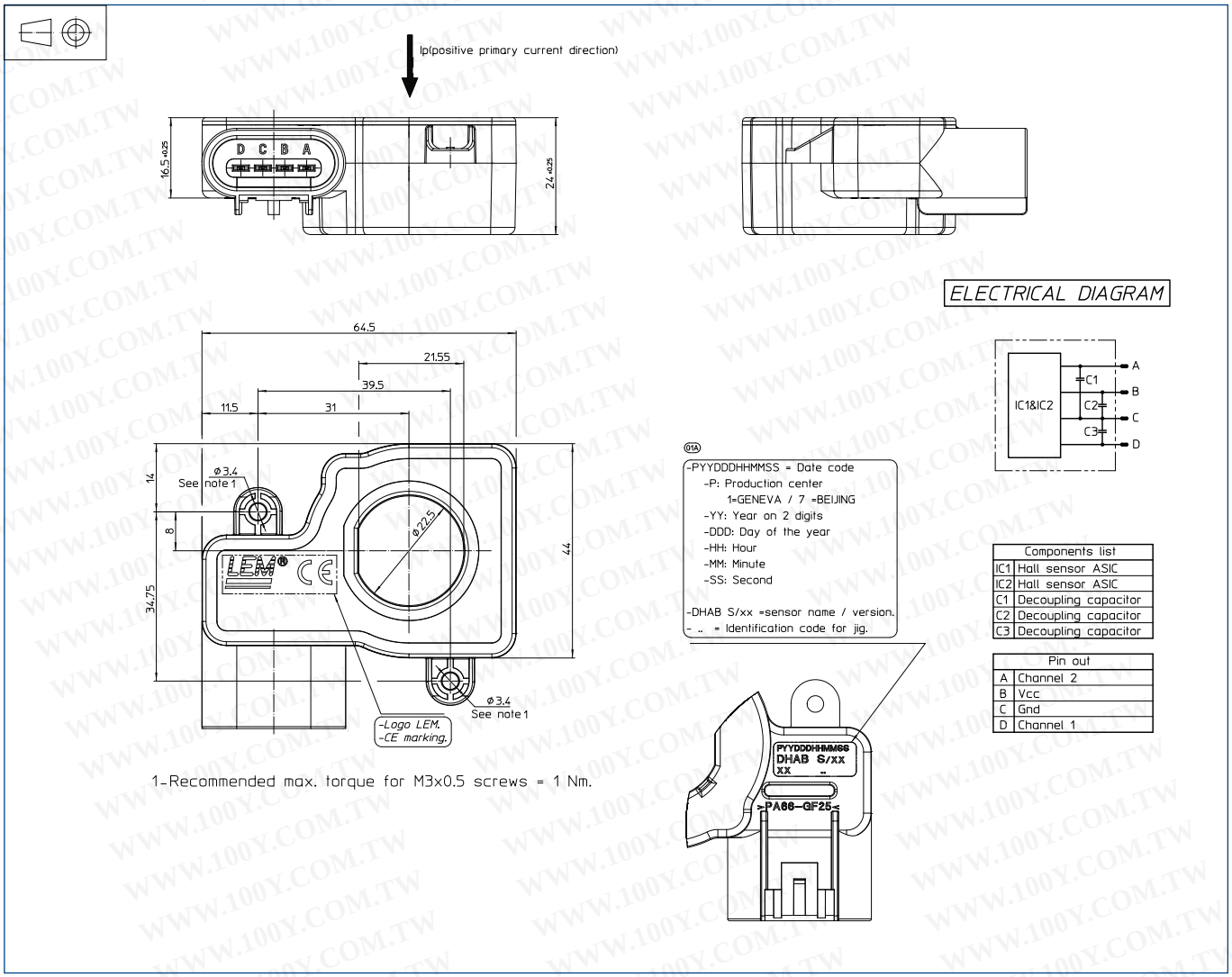
Dimensions DHAB S/18 (in mm.)

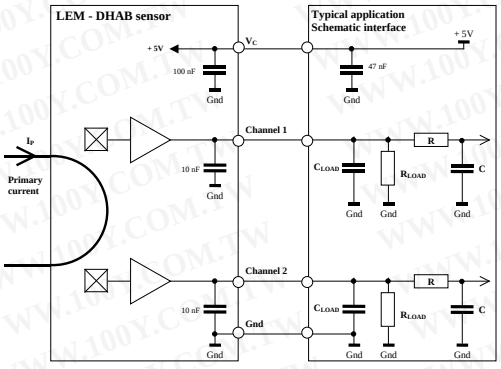


1-Recommended max. torque for M3x0.5 screws = 1 Nm.

Bill of materials

- Plastic case >PA66-GF25<
- Magnetic core Channel 1: FeNi alloy
  Channel 2: FeSi alloy
- Pins Brass tin plated
- Mass 69.5 g

System architecture (example)



R_L > 10 kΩ optional resistor for signal line diagnosis
C_L < 100 nF EMC protection
RC Low pass filter EMC protection (optional)

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Absolute maximum ratings

Parameter	Symbol	Unit	Specification			Conditions
			Min	Typ	Max	
Electrical Data						
Supply voltage	V_C	V			8.5	
Over voltage					14	1 min
Reverse voltage			-14			1 min @ T_A = 25°C
Output voltage (Analog)	V_{OUT}	V			8.5	
Output over voltage (Analog)		V			14	1 min @ T_A = 25°C
Continuous output current	I_{OUT}	mA	-10		10	
Output short-circuit duration	T_c	min			2	
Ambient storage temperature	T_S	°C	-40		125	

Operating characteristics

Parameter	Symbol	Unit	Specification			Conditions
			Min	Typ	Max	
Electrical Data						
Supply voltage	V_C	V	4.75	5	5.25	
Output current (Analog)	I_{OUT}	mA	- 1		1	
Current consumption	I_C	mA		15	20	
Power up inrush current					40	@ V_C < 3.5 V
Load resistance	R_L	KΩ	10			
Capacitive loading	C_L	nF	1		100	
Ambient operating temperature	T_A	°C	-10		65	High accuracy
			-40		125	Reduced accuracy

Channel 1

Parameter	Symbol	Unit	Specification			Conditions
			Min	Typ	Max	
Electrical Data						
Primary current	$I_{P\ channel\ 1}$	A	-30		30	
Calibration current	I_{CAL}		-30		30	@ T_A = 25°C
Offset voltage [1)]	V_O	V		2.5		@ V_C = 5 V
Sensitivity [1)]	G	mV/A		66.7		@ V_C = 5 V
Resolution		mV		2.5		@ V_C = 5 V
Output clamping voltage min [1)]	V_{SZ}	V	0.24	0.25	0.26	@ V_C = 5 V
Output clamping voltage max [1)]			4.74	4.75	4.76	@ V_C = 5 V
Output internal resistance	R_{OUT}	Ω		1	10	
Frequency bandwidth	BW	KHz		1		@ -3 dB
Power up time		ms			110	
Setting time after over load		ms			25	

Channel 2

Parameter	Symbol	Unit	Specification			Conditions
			Min	Typ	Max	
Electrical Data						
Primary current	$I_{P\ channel\ 2}$	A	-350		350	
Calibration current	I_{CAL}		-350		350	@ T_A = 25°C
Offset voltage [1)]	V_O	V		2.5		@ V_C = 5 V
Sensitivity [1)]	G	mV/A		5.7		@ V_C = 5 V
Resolution		mV		2.5		@ V_C = 5 V
Output clamping voltage min [1)]	V_{SZ}	V	0.24	0.25	0.26	@ V_C = 5 V
Output clamping voltage max [1)]			4.74	4.75	4.76	@ V_C = 5 V
Output internal resistance	R_{OUT}	Ω		1	10	
Frequency bandwidth	BW	KHz		1		@ -3 dB
Power up time		ms			110	
Setting time after over load		ms			25	

Note: [1)] The output voltage V_{OUT} is fully ratiometric (concerning V_O, sensitivity and clamping) and is dependent on the supply voltage V_C relative to the following formula:

$$I_P = \left(V_{OUT} - \frac{V_C}{2}\right) \times \frac{1}{G} \times \frac{5}{V_C} \qquad \text{with G in (V / A)}$$

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ACCURACY

Channel 1

Parameter	Symbol	Unit	Specification			Conditions
			Min	Typ	Max	
Electrical Data						
Electrical offset current	$I_{OE\ channel\ 1}$	mA		± 50		@ T_A = 25°C
Magnetic offset current	$I_{OM\ channel\ 1}$	mA		± 50		@ T_A = 25°C
Global offset current	$I_{O\ channel\ 1}$	mA		± 100		@ T_A = 25°C
			- 300		300	@ - 10°C < T° < 65°C
			- 500		500	@ - 40°C < T° < 125°C
Sensitivity error	ε_G	%		± 0.5		@ T_A = 25°C
			-2.5		2.5	@ - 10°C < T° < 65°C
			- 4		4	@ - 40°C < T° < 125°C
Linearity error	ε_L	%	- 1		1	off full range

Channel 2

Parameter	Symbol	Unit	Specification			Conditions
			Min	Typ	Max	
Electrical Data						
Electrical offset current	$I_{OE\ channel\ 2}$	A		± 0.7		@ T_A = 25°C
Magnetic offset current	$I_{OM\ channel\ 2}$	A		± 2.3		@ T_A = 25°C
Global offset current	$I_{O\ channel\ 2}$	A		± 3		@ T_A = 25°C
			- 4		4	@ - 10°C < T° < 65°C
			- 4.5		4.5	@ - 40°C < T° < 125°C
Sensitivity error	ε_G	%		± 0.5		@ T_A = 25°C
			-2.5		2.5	@ - 10°C < T° < 65°C
			- 4		4	@ - 40°C < T° < 125°C
Linearity error	ε_L	%	- 1		1	off full range

Note: In case of short circuit of any DHAB output to + batt, a current is reinjected in the power supply. If the output voltage is not protected against this current, this voltage may increase or decrease, which must be taken into account for the second channel.

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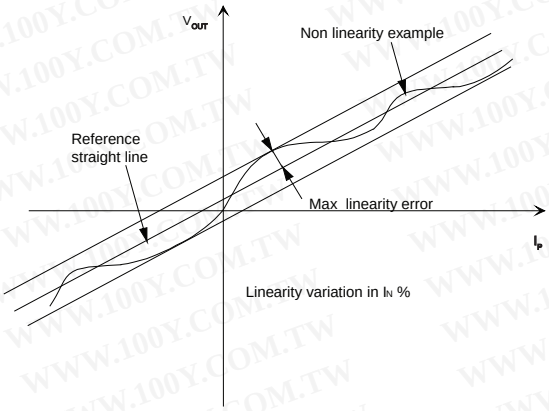
PERFORMANCES PARAMETERS DEFINITIONS

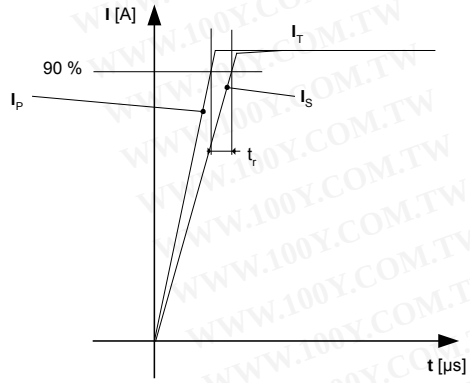
Output noise voltage:
The output voltage noise is the result of the noise floor of the Hall elements and the linear I_C amplifier gain.

Magnetic offset:
The magnetic offset is the consequence of an over-current on the primary side. It's defined after an excursion of $I_{P\ max}$.

Linearity:
The maximum positive or negative discrepancy with a reference straight line $V_{OUT} = f(I_P)$.
Unit: linearity (%) expressed with full scale of $I_{P\ max}$.
Linearity is measured on cycle + I_P, O, - I_P, O, + I_P without magnetic offset (average values used)



Response time (delay time) t_r:
The time between the primary current signal and the output signal reach at 90 % of its final value



Typical:
Theorical value or usual accuracy recorded during the production.

Sensitivity:
The Transducer's sensitivity **G** is the slope of the straight line $V_{out} = f(I_P)$, it must establish the relation:
$V_{out}(I_P) = V_C/5\ (G \times I_P + 2.5)$ (*)
(*) For all symetrics transducers.

Offset with temperature:
The error of the offset in the operating temperature is the variation of the offset in the temperature considered with the initial offset at 25°C.
The offset variation I_{OT} is a maximum variation the offset in the temperature range:
$I_{OT} = I_{OE}$ max - I_{OE} min
The Offset drift TCI_{OEAV} is the I_{OT} value divided by the temperature range.

Sensitivity with temperature:
The error of the sensitivity in the operating temperature is the relative variation of sensitivity with the temperature considered with the initial offset at 25°C.
The sensitivity variation G_T is the maximum variation (in ppm or %) of the sensitivity in the temperature range:
G_T = (Sensitivity max - Sensitivity min) / Sensitivity at 25°C.
The sensitivity drift TCG_{AV} is the G_T value divided by the temperature range.

Offset voltage @ I_P = 0 A:
Is the output voltage when the primary current is null. The ideal value of V_O is $V_C/2$ at V_C = 5 V. So, the difference of V_O -V_C/2 is called the total offset voltage error. This offset error can be attributed to the electrical offset (due to the resolution of the ASIC quiescent voltage trimming), the magnetic offset, the thermal drift and the thermal hysteresis.

Environmental test specifications

Name	Standard	Conditions
Thermal shocks	GM &5.5.5 (IEC 60068 Part 2-14)	T° - 40°C to 125°C / 300 cycles not connected. Criteria: ε_G < 3 % @ 25°C
Power temperature	GM &5.5.6 (IEC 60068 Part 2-14 Nb	T° -40 + 125°C/595 cycles, supply voltage = 5 V Criteria: ε_G < 3 % @ 25°C
Temperature humidity cycle test	GM &6.18.1 (IEC 60068 2-38)	T° -10 + 65°C/10 cycles, supply voltage = 5 V Criteria: ε_G < 3 % @ 25°C
Mechanical tests		
Vibration test	GM &6.6.2 (IEC 60068 2-64)	Acceleration 30m/s2, 25°C, frequency 20 to 1000 Hz/8h each axis
Drop test	GM &6.10 (IEC 60068 2-32)	Drop 1m, 2 falls/part, 1 part/axis, 3 axes, criteria: relative sensitivity error 3%
EMC Test		
Rms voltage for AC isolation test	GM &6.4-13 (IEC 60068 2-38)	
Bulk current injection immunity	ISO 11452-4	Criteria B
Electrostatic discharge immunity test		2 KV, Criteria B