

## Solid Tantalum Chip Capacitors Tantamount® Commercial, Surface Mount for Switch Mode Power Supplies and Converters



Effective September 2005, new capacitor ratings will not be added to the 593D series. All new ratings are available in the new TR3 series. The TR3 series offers state-of-the-art Low ESR for switch Mode Power Supplies and DC/DC Converters.

### PERFORMANCE/ELECTRICAL CHARACTERISTICS

**Operating Temperature:** - 55 °C to + 85 °C

(To + 125 °C with voltage derating)

**Capacitance Range:** 0.47 µF to 680 µF

**Capacitance Tolerance:** ± 20 %, ± 10 % standard

**Voltage Rating:** 4 WVDC to 50 WVDC

### FEATURES

- Terminations: 100 % Tin, Standard. SnPb available
- Molded case available in five case codes
- Compatible with "High Volume" automatic pick and place equipment
- High Ripple Current carrying capability
- Low ESR
- Meets EIA 535BAAE and IEC Specification QC300801/US0001



RoHS\*  
 COMPLIANT

### Compliant Terminations

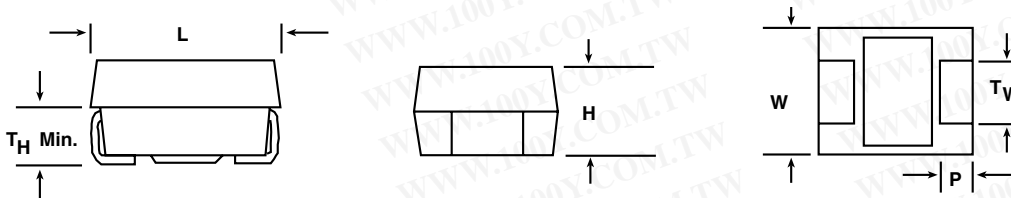
100 % Surge Current Tested (B, C, D & E Case Sizes)

### ORDERING INFORMATION

593D TYPE	107 CAPACITANCE	X9 CAPACITANCE TOLERANCE	010 DC VOLTAGE RATING AT + 85 °C	D CASE CODE	2WE3 TERMINATION AND PACKAGING
This is expressed in picofarads. The first two digits are the significant figures. The third is the number of zeros to follow.	X0 = ± 20 % X9 = ± 10 % X5 = ± 5 % (Special Order)	This is expressed in volts. To complete the three-digit block, zeros precede the voltage rating. A decimal point is indicated by an "R" (6R3 = 6.3 volts).	See Ratings and Case Codes Table.	2TE3: 100 % tin terminations, 7" (178 mm) reel 2WE3: 100 % tin terminations, 13" (330 mm) reel 8T: 90/10 SnPb Solder Plate terminations, 7" (178 mm) reel 8W: 90/10 SnPb Solder Plate terminations, 13" (330 mm) reel 2T: Solderable coating, non-preferred 2W: Solderable coating, non-preferred	

**Note:** We reserve the right to supply higher voltage ratings and tighter capacitance tolerance capacitors in the same case size. Voltage substitutions will be marked with the higher voltage rating.

### DIMENSIONS in inches [millimeters]



CASE CODE	EIA SIZE	L	W	H	P	Tw	Th (Min.)
A	3216-18	0.126 ± 0.008 [3.2 ± 0.20]	0.063 ± 0.008 [1.6 ± 0.20]	0.063 ± 0.008 [1.6 ± 0.20]	0.031 ± 0.012 [0.80 ± 0.30]	0.047 ± 0.004 [1.2 ± 0.10]	0.028 [0.70]
B	3528-21	0.138 ± 0.008 [3.5 ± 0.20]	0.110 ± 0.008 [2.8 ± 0.20]	0.075 ± 0.008 [1.9 ± 0.20]	0.031 ± 0.012 [0.80 ± 0.30]	0.087 ± 0.004 [2.2 ± 0.10]	0.028 [0.70]
C	6032-28	0.236 ± 0.012 [6.0 ± 0.30]	0.126 ± 0.012 [3.2 ± 0.30]	0.098 ± 0.012 [2.5 ± 0.30]	0.051 ± 0.012 [1.3 ± 0.30]	0.087 ± 0.004 [2.2 ± 0.10]	0.039 [1.0]
D	7343-31	0.287 ± 0.012 [7.3 ± 0.30]	0.170 ± 0.012 [4.3 ± 0.30]	0.110 ± 0.012 [2.8 ± 0.30]	0.051 ± 0.012 [1.3 ± 0.30]	0.095 ± 0.004 [2.4 ± 0.10]	0.039 [1.0]
E	7343-43	0.287 ± 0.012 [7.3 ± 0.30]	0.170 ± 0.012 [4.3 ± 0.30]	0.158 ± 0.012 [4.0 ± 0.30]	0.051 ± 0.012 [1.3 ± 0.30]	0.095 ± 0.004 [2.4 ± 0.10]	0.039 [1.0]

\* Pb containing terminations are not RoHS compliant, exemptions may apply



Solid Tantalum Chip Capacitors  
Tantamount® Commercial, Surface Mount  
for Switch Mode Power Supplies and Converters

Vishay Sprague

RATINGS AND CASE CODES																
μF	4 V		6.3 V		10 V		16 V		20 V		25 V		35 V		50 V	
	Std.	Ext.	Std.	Ext.	Std.	Ext.	Std.	Ext.	Std.	Ext.	Std.	Ext.	Std.	Ext.	Std.	Ext.
0.47														A		
0.68														A		
1.0									A			A	B	A	C	B
1.5												A	C	B	C	B
2.2										A	B	A	C	B	D	C
3.3							A			A		B	C		D	C
4.7					A		B	A	B	A	C	B		C	D	E
6.8						A		A		B	C		D	C		D/E
10				A		A	C	A/B	C	B		C	D	C		E
15		A		A		A/B	C	B		B/C	D	C		D		
22		A		A/B	C	A/B		B/C	D	C		D		D/E		
33		A/B		A/B		B/C	D	B/C	D	C		D/E				
47		A/B		B/C	D	B/C	D	C		D/E		E				
68		B/C		B/C	D	C		D		D/E						
100		B/C	D	B/C		C/D		D/E		E						
150	D	B/C	E	C/D		D/E		E								
220		C/D		D/E		D/E										
330		D		D/E		E										
470		D/E		E												
680		E														

### CONSTRUCTION AND MARKING

#### CONSTRUCTION

#### MARKING

Capacitance Code, pF

Indicates Lead (Pb)-free

Polarity Band

Voltage Code

A Case

Capacitance μF

Voltage

Indicates Lead (Pb)-free

Polarity Band

Data Code

Vishay Sprague Logo

B, C, D, E Case

Volts	Code
4.0	G
6.3	J
10	A
16	C
20	D
25	E
35	V
50	T

**Marking:**  
Capacitor marking includes an anode (+) polarity band, capacitance in microfarads and the voltage rating of + 85 °C. 'A' Case capacitors use a letter code for the voltage and EIA capacitance code. The Sprague® trademark will be included if space permits. Capacitors kept rated at 6.3 V shall be marked 6 V. A manufacturing date code is marked on all capacitors. Call the factory for further explanation.

勝特力材料 886-3-5753170  
 勝特力电子(上海) 86-21-54151736  
 勝特力电子(深圳) 86-755-83298787  
[Http://www.100y.com.tw](http://www.100y.com.tw)



<b>STANDARD/EXTENDED RATINGS</b>						
CAPACITANCE ( $\mu$ F)	CASE CODE	PART NUMBER	Max. DC Leakage AT + 25 °C ( $\mu$ A)	Max. DF AT + 25 °C 120 Hz (%)	Max. ESR AT + 25 °C 100 kHz (Ohms)	Max. RIPPLE 100 kHz I <sub>rms</sub> (Amps)
<b>4 WVDC AT + 85 °C, SURGE = 5.2 V . . 2.7 WVDC AT + 125 °C, SURGE = 3.4 V</b>						
15	A	593D156X_004A2_E3	0.6	6	1.500	0.22
22	A	593D226X_004A2_E3	0.9	6	1.500	0.22
33	A	593D336X_004A2_E3	1.3	6	1.500	0.22
33	B	593D336X_004B2_E3	1.3	6	0.500	0.41
47	A	593D476X_004A2_E3	1.9	14	0.800	0.31
47	B	593D476X_004B2_E3	1.9	6	0.500	0.41
68	B	593D686X_004B2_E3	2.7	6	0.500	0.41
68	C	593D686X_004C2_E3	2.7	6	0.275	0.63
100	B	593D107X_004B2_E3	4.0	6	0.450	0.43
100	C	593D107X_004C2_E3	4.0	6	0.225	0.66
150	B	593D157X_004B2_E3	6.0	14	0.500	0.41
150	C	593D157X_004C2_E3	6.0	8	0.250	0.66
150	D	593D157X_004D2_E3	6.0	8	0.150	1.00
220	C	593D227X_004C2_E3	8.8	8	0.200	0.74
220	D	593D227X_004D2_E3	8.8	8	0.150	1.00
330	D	593D337X_004D2_E3	13.2	8	0.150	1.00
470	D	593D477X_004D2_E3	18.8	10	0.125	1.10
470	E	593D477X_004E2_E3	18.8	10	0.100	1.28
680	E	593D687X_004E2_E3	27.2	12	0.100	1.28
<b>6.3 WVDC AT + 85 °C, SURGE = 8 V . . . 4 WVDC AT 125 °C, SURGE = 5 V</b>						
10	A	593D106X_6R3A2_E3	0.6	6	2.000	0.19
15	A	593D156X_6R3A2_E3	0.9	6	2.000	0.19
22	A	593D226X_6R3A2_E3	1.3	6	2.000	0.19
22	B	593D226X_6R3B2_E3	1.3	6	0.600	0.38
33	A	593D336X_6R3A2_E3	2.0	14	0.800	0.31
33	B	593D336X_6R3B2_E3	2.0	6	0.600	0.38
47	B	593D476X_6R3B2_E3	2.8	6	0.550	0.39
47	C	593D476X_6R3C2_E3	2.8	6	0.300	0.61
68	B	593D686X_6R3B2_E3	4.1	6	0.550	0.39
68	C	593D686X_6R3C2_E3	4.1	6	0.275	0.63
100	B	593D107X_6R3B2_E3	6.0	15	0.500	0.41
100	C	593D107X_6R3C2_E3	6.0	6	0.250	0.66
100	D	593D107X_6R3D2_E3	6.0	6	0.140	1.04
150	C	593D157X_6R3C2_E3	9.0	8	0.200	0.74
150	D	593D157X_6R3D2_E3	9.0	8	0.125	1.10
150	E	593D157X_6R3E2_E3	9.0	8	0.100	1.28
220	D	593D227X_6R3D2_E3	13.2	8	0.100	1.22
220	E	593D227X_6R3E2_E3	13.2	8	0.100	1.28
330	D	593D337X_6R3D2_E3	19.8	8	0.125	1.10
330	E	593D337X_6R3E2_E3	19.8	8	0.100	1.28
470	E	593D477X_6R3E2_E3	28.2	10	0.100	1.28
<b>10 WVDC AT + 85 °C, SURGE = 13 V . . . 7 WVDC AT 125 °C, SURGE = 8 V</b>						
4.7	A	593D475X_010A2_E3	0.5	6	3.000	0.16
6.8	A	593D685X_010A2_E3	0.7	6	3.000	0.16
10	A	593D106X_010A2_E3	1.0	6	2.000	0.19
15	A	593D156X_010A2_E3	1.5	6	2.000	0.19
15	B	593D156X_010B2_E3	1.5	6	0.700	0.35
22	A	593D226X_010A2_E3	2.2	8	1.500	0.22
22	B	593D226X_010B2_E3	2.2	6	0.700	0.35
22	C	593D226X_010C2_E3	2.2	6	0.345	0.56
33	B	593D336X_010B2_E3	3.3	6	0.600	0.38
33	C	593D336X_010C2_E3	3.3	6	0.300	0.61

For 10 % tolerance, specify "9"; for 20 % tolerance, change to "0". Extended Ratings in bold print.

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<b>STANDARD/EXTENDED RATINGS</b>						
CAPACITANCE (µF)	CASE CODE	PART NUMBER	Max. DC Leakage AT + 25 °C (µA)	Max. DF AT + 25 °C 120 Hz (%)	Max. ESR AT + 25 °C 100 kHz (Ohms)	Max. RIPPLE 100 kHz Irms (Amps)
<b>10 WVDC AT + 85 °C, SURGE = 13 V ... 7 WVDC AT 125 °C, SURGE = 8 V</b>						
47	B	593D476X_010B2_E3	4.7	6	0.600	0.38
47	C	593D476X_010C2_E3	4.7	6	0.300	0.61
47	D	593D476X_010D2_E3	4.7	6	0.200	0.87
68	C	593D686X_010C2_E3	6.8	6	0.275	0.63
68	D	593D686X_010D2_E3	6.8	6	0.150	1.00
100	C	593D107X_010C2_E3	10.0	8	0.200	0.74
100	D	593D107X_010D2_E3	10.0	6	0.100	1.22
150	D	593D157X_010D2_E3	15.0	8	0.100	1.22
150	E	593D157X_010E2_E3	15.0	8	0.100	1.28
220	D	593D227X_010D2_E3	22.0	8	0.125	1.10
220	E	593D227X_010E2_E3	22.0	8	0.100	1.28
330	E	593D337X_010E2_E3	33.0	10	0.100	1.28
<b>16 WVDC AT + 85 °C, SURGE = 20 V ... 10 WVDC AT + 125 °C, SURGE = 12 V</b>						
3.3	A	593D335X_016A2_E3	0.5	6	3.500	0.15
4.7	A	593D475X_016A2_E3	0.8	6	2.500	0.17
4.7	B	593D475X_016B2_E3	0.8	6	1.500	0.24
6.8	A	593D685X_016A2_E3	1.1	6	3.000	0.16
10	A	593D106X_016A2_E3	1.6	6	1.700	0.21
10	B	593D106X_016B2_E3	1.6	6	0.800	0.33
10	C	593D106X_016C2_E3	1.6	6	0.450	0.49
15	B	593D156X_016B2_E3	2.4	6	0.800	0.33
15	C	593D156X_016C2_E3	2.4	6	0.400	0.52
22	B	593D226X_016B2_E3	3.5	6	0.700	0.35
22	C	593D226X_016C2_E3	3.5	6	0.350	0.56
33	B	593D336X0016B2_E3	5.3	6	0.700	0.35
33	C	593D336X_016C2_E3	5.3	6	0.300	0.61
33	D	593D336X_016D2_E3	4.2	4	0.225	0.82
47	C	593D476X_016C2_E3	7.5	6	0.300	0.61
47	D	593D476X_016D2_E3	7.5	6	0.150	1.00
68	D	593D686X_016D2_E3	10.9	6	0.150	1.00
100	D	593D107X_016D2_E3	16.0	8	0.125	1.10
100	E	593D107X_016E2_E3	16.0	8	0.100	1.28
150	E	593D157X_016E2_E3	24.0	8	0.100	1.28
<b>20 WVDC AT + 85 °C, SURGE = 26 V ... 13 WVDC AT + 125 °C, SURGE = 16 V</b>						
1.0	A	593D105X_020A2_E3	0.5	4	5.500	0.12
2.2	A	593D225X_020A2_E3	0.5	6	4.000	0.14
3.3	A	593D335X_020A2_E3	0.7	6	4.000	0.14
4.7	A	593D475X_020A2_E3	0.9	6	3.500	0.15
4.7	B	593D475X_020B2_E3	0.9	6	1.000	0.29
6.8	B	593D685X_020B2_E3	1.4	6	1.000	0.29
10	B	593D106X_020B2_E3	2.0	6	1.000	0.29
10	C	593D106X_020C2_E3	2.0	6	0.450	0.49
15	B	593D156X_020B2_E3	3.0	6	1.000	0.29
15	C	593D156X_020C2_E3	3.0	6	0.400	0.52
22	C	593D226X_020C2_E3	4.4	6	0.375	0.54
22	D	593D226X_020D2_E3	3.5	4	0.225	0.82
33	C	593D336X_020C2_E3	6.6	6	0.350	0.56
33	D	593D336X_020D2_E3	6.6	6	0.200	0.87
47	D	593D476X_020D2_E3	9.4	6	0.200	0.87
47	E	593D476X_020E2_E3	7.5	4	0.150	1.05
68	D	593D686X_020D2_E3	13.6	6	0.175	0.93
68	E	593D686X_020E2_E3	13.6	6	0.150	1.05
100	E	593D107X_020E2_E3	20.0	8	0.150	1.05

For 10 % tolerance, specify "9"; for 20 % tolerance, change to "0". Extended Ratings in bold print.



<b>STANDARD/EXTENDED RATINGS</b>						
CAPACITANCE ( $\mu$ F)	CASE CODE	PART NUMBER	Max. DC Leakage AT + 25 °C ( $\mu$ A)	Max. DF AT + 25 °C 120 Hz (%)	Max. ESR AT + 25 °C 100 kHz (Ohms)	Max. RIPPLE 100 kHz I <sub>rms</sub> (Amps)
<b>25 WVDC AT + 85 °C, SURGE = 32 V . . . 17 WVDC AT + 125 °C, SURGE = 20 V</b>						
1.0	A	593D105X_025A2_E3	0.5	4	4.000	0.14
1.5	A	593D155X_025A2_E3	0.5	6	4.000	0.14
2.2	A	593D225X_025A2_E3	0.5	6	4.000	0.14
2.2	B	593D225X_025B2_E3	0.6	6	1.500	0.24
3.3	B	593D335X_025B2_E3	0.8	6	1.500	0.24
4.7	B	593D475X_025B2_E3	1.2	6	1.500	0.24
4.7	C	593D475X_025C2_E3	1.2	6	0.525	0.46
6.8	C	593D685X_025C2_E3	1.7	6	0.500	0.47
10	C	593D106X_025C2_E3	2.5	6	0.450	0.49
15	C	593D156X_025C2_E3	3.8	6	0.425	0.51
15	D	593D156X_025D2_E3	3.8	6	0.250	0.77
22	D	593D226X_025D2_E3	5.5	6	0.200	0.87
33	D	593D336X_025D2_E3	8.3	6	0.200	0.87
33	E	593D336X_025E2_E3	8.3	6	0.200	0.91
47	E	593D476X_025E2_E3	11.8	6	0.200	0.91
<b>35 WVDC AT + 85 °C, SURGE = 46 V . . . 23 WVDC AT + 125 °C, SURGE = 28 V</b>						
0.47	A	593D474X_035A2_E3	0.5	4	4.000	0.14
0.68	A	593D684X_035A2_E3	0.5	4	4.000	0.14
1.0	A	593D105X_035A2_E3	0.5	4	4.000	0.14
1.0	B	593D105X_035B2_E3	0.5	4	2.000	0.21
1.5	B	593D155X_035B2_E3	0.5	6	2.000	0.21
1.5	C	593D155X_035C2_E3	0.5	6	0.900	0.35
2.2	B	593D225X_035B2_E3	0.8	6	2.000	0.21
2.2	C	593D225X_035C2_E3	0.8	6	0.900	0.40
3.3	C	593D335X_035C2_E3	1.2	6	0.700	0.45
4.7	C	593D475X_035C2_E3	1.6	6	0.500	0.47
6.8	C	593D685X_035C2_E3	2.4	6	0.475	0.48
6.8	D	593D685X_035D2_E3	2.4	6	0.300	0.71
10	C	593D106X_035C2_E3	3.5	6	0.450	0.49
10	D	593D106X_035D2_E3	3.5	6	0.300	0.71
15	D	593D156X_035D2_E3	5.3	6	0.300	0.71
22	D	593D226X_035D2_E3	7.7	6	0.300	0.71
22	E	593D226X_035E2_E3	7.7	6	0.275	0.77
<b>50 WVDC AT + 85 °C, SURGE = 65 V . . . 33 WVDC AT + 125 °C, SURGE = 40 V</b>						
1.0	B	593D105X_050B2_E3	0.5	4	2.000	0.21
1.0	C	593D105X_050C2_E3	0.5	4	1.600	0.26
1.5	B	593D155X_050B2_E3	0.8	6	2.000	0.21
1.5	C	593D155X_050C2_E3	0.8	6	1.500	0.27
2.2	C	593D225X_050C2_E3	1.1	6	1.500	0.27
2.2	D	593D225X_050D2_E3	1.1	6	0.800	0.43
3.3	C	593D335X_050C2_E3	1.7	6	1.500	0.27
3.3	D	593D335X_050D2_E3	1.7	6	0.800	0.43
4.7	D	593D475X_050D2_E3	2.4	6	0.600	0.50
4.7	E	593D475X_050E2_E3	2.4	6	0.600	0.50
6.8	D	593D685X_050D2_E3	3.4	6	0.600	0.50
6.8	E	593D685X_050E2_E3	3.4	6	0.550	0.55
10	E	593D106X_050E2_E3	5.0	6	0.550	0.55

For 10 % tolerance, specify "9"; for 20 % tolerance, change to "0". **Extended Ratings in bold print.**



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**PERFORMANCE CHARACTERISTICS**

- 1. **Operating Temperature:** Capacitors are designed to operate over the temperature range - 55 °C to + 85 °C.
- 1.1 Capacitors may be operated to + 125 °C with voltage derating to two-thirds the + 85 °C rating.

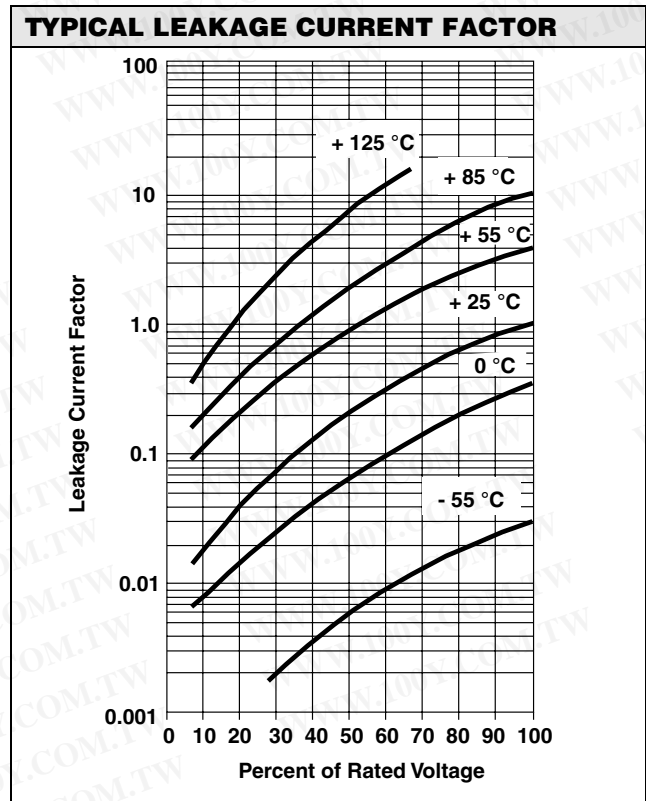
+ 85 °C Rating		+ 125 °C Rating	
Working Voltage (V)	Surge Voltage (V)	Working Voltage (V)	Surge Voltage (V)
4.0	5.2	2.7	3.4
6.3	8.0	4.0	5.0
10	13	7.0	8.0
16	20	10	12
20	26	13	16
25	32	17	20
35	46	23	28
50	65	33	40

- 2. **DC Working Voltage:** The DC working voltage is the maximum operating voltage for continuous duty at the rated temperature.
- 3. **Surge Voltage:** The surge DC rating is the maximum voltage to which the capacitors may be subjected under any conditions, including transients and peak ripple at the highest line voltage.
- 3.1 **Surge Voltage Test:** Capacitors shall withstand the surge voltage applied in series with a 33 ohm ± 5 % resistor at the rate of one-half minute on, one-half minute off, at + 85 °C, for 1000 successive test cycles.
- 3.2 Following the surge voltage test, the dissipation factor and the leakage current shall meet the initial requirements; the capacitance shall not have changed more than ± 10 %.
- 4. **Capacitance Tolerance:** The capacitance of all capacitors shall be within the specified tolerance limits of the normal rating.
- 4.1 Capacitance measurements shall be made by means of polarized capacitance bridge. The polarizing voltage shall be of such magnitude that there shall be no reversal of polarity due to the AC component. The maximum voltage applied to capacitors during measurement shall be 2 volts rms at 120 Hz at + 25 °C. If the AC voltage applied is less than one-half volt rms, no DC bias is required. Accuracy of the bridge shall be within ± 2 %.
- 5. **Capacitance Change With Temperature:** The capacitance change with temperature shall not exceed the following percentage of the capacitance measured at + 25 °C:

- 55 °C	+ 85 °C	+ 125 °C
- 10 %	+ 10 %	+ 12 %

- 6. **Dissipation Factor:** The dissipation factor, determined from the expression  $2\pi fRC$ , shall not exceed values listed in the Standard Ratings Table.
- 6.1 Measurements shall be made by the bridge method at, or referred to, a frequency of 120 Hz and a temperature of + 25 °C.
- 7. **Leakage Current:** Capacitors shall be stabilized at the rated temperature for 30 minutes. Rated voltage shall be applied to capacitors for 5 minutes using a steady source of power (such as a regulated power supply) with 1000 ohm resistor connected in series with the capacitor under test to limit the charging current. Leakage current shall then be measured.

*Note that the leakage current varies with temperature and applied voltage. See graph below for the appropriate adjustment factor.*



- 7.1 **At + 25 °C,** the leakage current shall not exceed the value listed in the Standard Ratings Table.
- 7.2 **At + 85 °C,** the leakage current shall not exceed 10 times the value listed in the Standard Ratings Table.
- 7.3 **At + 125 °C,** the leakage current shall not exceed 12 times the value listed in the Standard Ratings Table.

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**PERFORMANCE CHARACTERISTICS** (Continued)

8. **ESR**
- 8.1 **ESR (Equivalent Series Resistance)** shall not exceed the values listed in the Ratings Table. Measurement shall be made by the bridge method at a frequency of 100 kHz and a temperature of + 25 °C.
9. **Life Test:** Capacitors shall withstand rated DC voltage applied at + 85 °C or two-thirds rated voltage applied at + 125 °C for 1000 hours.
- 9.1 Following the life test, the dissipation factor shall meet the initial requirement; the capacitance change shall not exceed  $\pm 10 \%$ ; the leakage current shall not exceed 125 % of the initial requirement.
10. **Vibration Tests:** Capacitors shall be subjected to vibration tests in accordance with the following criteria.
- 10.1 Capacitors shall be secured for test by means of a rigid mounting using suitable brackets.
- 10.2 **Low Frequency Vibration:** Vibration shall consist of simple harmonic motion having an amplitude of 0.03" [0.76 mm] and a maximum total excursion of 0.06" [1.52 mm], in a direction perpendicular to the major axis of the capacitors.
- 10.2.1 Vibration frequency shall be varied uniformly between the approximate limits of 10 Hz to 55 Hz during a period of approximately one minute, continuously for 1.5 hours.
- 10.2.2 An oscilloscope or other comparable means shall be used in determining electrical intermittency during the final 30 minutes of the test. The AC voltage applied shall not exceed 2 volts rms.
- 10.2.3 Electrical tests shall show no evidence of intermittent contacts, open circuits or short circuits during these tests.
- 10.2.4 Following the low frequency vibration test, capacitors shall meet the original requirements for capacitance, dissipation factor and leakage current.
- 10.3 **High Frequency Vibration:** Vibration shall consist of a simple harmonic motion having an amplitude of 0.06" [1.52]  $\pm 10 \%$  maximum total excursion or 20 g peak whichever is less.
- 10.3.1 Vibration frequency shall be varied logarithmically from 50 Hz to 2000 Hz and return to 50 Hz during a cycle period of 20 minutes.
- 10.3.2 The vibration shall be applied for 4 hours in each of 2 directions, parallel and perpendicular to the major axis of the capacitors.
- 10.3.3 Rated DC voltage shall be applied during the vibration cycling.
- 10.3.4 An oscilloscope or other comparable means shall be used in determining electrical intermittency during the last cycle. The AC voltage applied shall not exceed 2 volts rms.
- 10.3.5 Electrical tests shall show no evidence of intermittent contacts, open circuits or short circuits during these tests.
- 10.3.6 There shall be no mechanical damage to these capacitors as a result of these tests.
- 10.3.7 Following the high frequency vibration test, capacitors shall meet the original limits for capacitance, dissipation factor and leakage current.
11. **Acceleration Test:**
- 11.1 Capacitors shall be rigidly mounted by means of suitable brackets.
- 11.2 Capacitors shall be subjected to a constant acceleration of 100 g for a period of 10 seconds in each of 2 mutually perpendicular planes.
- 11.2.1 The direction of motion shall be parallel to and perpendicular to the longitudinal axis of the capacitors.
- 11.3 Rated DC voltage shall be applied during acceleration test.
- 11.3.1 An oscilloscope or other comparable means shall be used in determining electrical intermittency during test. The AC voltage applied shall not exceed 2 volts rms.
- 11.4 Electrical tests shall show no evidence of intermittent contacts, open circuits or short circuits during these tests.
- 11.5 There shall be no mechanical damage to these capacitors as a result of these tests.
- 11.6 Following the acceleration test, capacitors shall meet the original limits for capacitance, dissipation factor and leakage current.
12. **Shock Test:**
- 12.1 Capacitors shall be rigidly mounted by means of suitable brackets. The test load shall be distributed uniformly on the test platform to minimize the effects of unbalanced loads.
- 12.1.1 Test equipment shall be adjusted to produce a shock of 100 g peak with the duration of 6 ms and sawtooth waveform at a velocity change of 9.7 ft./sec.
- 12.2 Capacitors shall be subjected to 3 shocks applied in each of 3 directions corresponding to the 3 mutually perpendicular axes of the capacitors.
- 12.3 Rated DC voltage shall be applied during test.



Solid Tantalum Chip Capacitors  
Tantamount® Commercial, Surface Mount  
for Switch Mode Power Supplies and Converters

Vishay Sprague

**PERFORMANCE CHARACTERISTICS** (Continued)

- 12.3.1 An oscilloscope or other comparable means shall be used in determining electrical intermittency during tests. The replacement voltage applied shall not exceed 2 volts rms.
- 12.4 Electrical tests shall show no evidence of intermittent contacts, open circuits or short circuits during these tests.
- 12.5 There shall be no mechanical damage to these capacitors as a result of these tests.
- 12.6 Following the shock test, capacitors shall meet the original limits for capacitance, dissipation factor and leakage current.
- 13. **Moisture Resistance:**
- 13.1 Capacitors shall be subjected to temperature cycling at 90 % to 95 % relative humidity, from + 25 °C to + 65 °C to + 25 °C (+ 10 °C, - 2 °C) over a period of 8 hours per cycle for 1000 hours.
- 13.2 Following the moisture resistance test, the leakage current and dissipation factor shall meet the initial requirements, and the change in capacitance shall not exceed ±10 %.
- 14. **Thermal Shock:**
- 14.1 Capacitors shall be conditioned prior to temperature cycling for 15 minutes at + 25 °C, at less than 50 % relative humidity and a barometric pressure at 28 to 31"
- 14.2 Capacitors shall be subjected to thermal shock in a cycle of exposure to ambient air at :  
- 55 °C (+ 0 °C, - 5 °C) for 30 minutes, then  
+ 25 °C (+ 10 °C, - 5 °C) for 5 minutes, then  
+ 125 °C (+ 3 °C, - 0 °C) for 30 minutes, then  
+ 25 °C (+ 10 °C, - 5 °C) for 5 minutes for 5 cycles.
- 14.3 Capacitors shall show no evidence of harmful or extensive corrosion, obliteration of marking or other visible damage.
- 14.4 Following the thermal shock test, capacitors shall meet the original requirements for leakage current and dissipation factor. Capacitance change shall not exceed ± 5 % of the original measured value.
- 15. **Soldering Compatibility:**
- 15.1 **Resistance to Solder Heat:** Capacitors will withstand exposure to + 260 °C + 5 °C for 10 seconds.
- 15.1.1 Following the resistance to soldering heat test, capacitance, dissipation factor and DC leakage current shall meet the initial requirement.
- 15.2 **Solderability:** Capacitors will meet the solderability requirements of ANSI/J-STD-002, Test B, Category 3.
- 16. **Terminal Strength:** Per UEC-384-3, minimum of 5N shear force.

- 17. **Environmental:** Mercury, CFC and ODS materials are not used in the manufacture of these capacitors.
- 18. **Flammability:** Encapsulant materials meet UL94 V0 with an oxygen index of 32 %.
- 19. **Capacitor Failure Mode:** The predominant failure mode for solid tantalum capacitors is increased leakage current resulting in a shorted circuit. Capacitor failure may result from excess forward or reverse DC voltage, surge current, ripple current, thermal shock or excessive temperature.  
  
The increase in leakage is caused by a breakdown of the Ta<sub>2</sub>O<sub>5</sub> dielectric. For additional information on leakage failure of solid tantalum chip capacitors, refer to Vishay Sprague Technical Paper, "Leakage Failure Mode in Solid Tantalum Chip Capacitors."
- 20. **Surge Current:** All B, C, D and E case code 593D capacitors are 100 % surge current tested at + 25 °C and rated voltage. The total series circuit resistance is 0.5 ohms. Each charge cycle of 0.10 seconds is followed by a discharge cycle of 0.10 seconds. Three surge cycles are applied. Each capacitor is tested individually to maximize the peak charging current.

**GUIDE TO APPLICATION**

1. **Recommended Voltage Derating Guidelines:**

Standard Conditions, for example; output filters

Capacitor Voltage Rating (V)	Operating Voltage (V)
4.0	2.5
6.3	3.6
10	6.0
16	10
20	12
25	15
35	24
50	28

Severe Conditions, for example; input filters

Capacitor Voltage Rating (V)	Operating Voltage (V)
4.0	2.5
6.3	3.3
10	5.0
16	8.0
20	10
25	12
35	15
50	24

- 2. **A-C Ripple Current:** The maximum allowable ripple current shall be determined from the formula:

$$I_{rms} = \sqrt{\frac{P}{R_{ESR}}}$$

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**GUIDE TO APPLICATION** (Continued)

where,

$P =$  Power Dissipation in Watts at + 25 °C as given in the table in Paragraph Number 5 (Power Dissipation)

$R_{ESR} =$  The capacitor Equivalent Series Resistance at the specified frequency.

3. **A-C Ripple Voltage:** The maximum allowable ripple voltage shall be determined from the formula:

$$V_{rms} = Z \sqrt{\frac{P}{R_{ESR}}}$$

or, from the formula:

$$V_{rms} = I_{rms} \times Z$$

where,

$P =$  Power Dissipation in Watts at + 25 °C as given in the table in Paragraph Number 5 (Power Dissipation).

$R_{ESR} =$  The capacitor Equivalent Series Resistance at the specified frequency.

$Z =$  The capacitor impedance at the specified frequency.

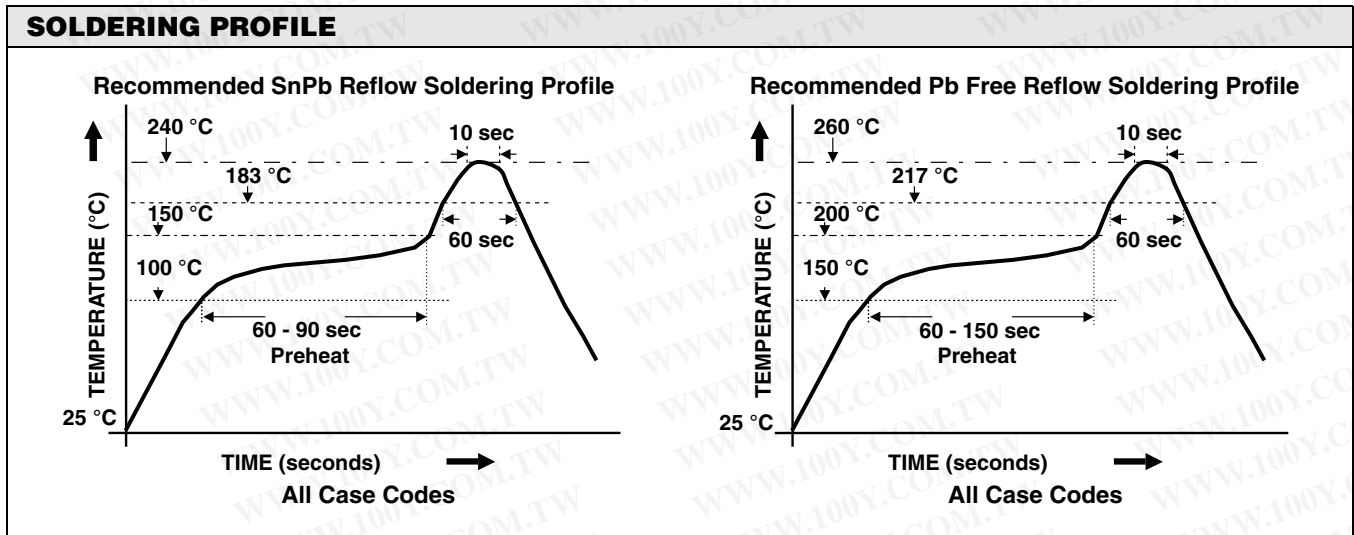
- 3.1 The sum of the peak AC voltage plus the applied DC voltage shall not exceed the DC voltage rating of the capacitor.
- 3.2 The sum of the negative peak AC voltage plus the applied DC voltage shall not allow a voltage reversal exceeding 10 % of the DC working voltage at + 25 °C.
4. **Reverse Voltage:** These capacitors are capable of withstanding peak voltages in the reverse direction equal to 10 % of the DC rating at + 25 °C, 5 % of the DC rating at + 85 °C and 1 % of the DC rating at + 125 °C.
5. **Temperature Derating:** If these capacitors are to be operated at temperatures above + 25 °C, the permissible rms ripple current or voltage shall be calculated using the derating factors as shown:

Temperature	Derating Factor
+ 25 °C	1.0
+ 85 °C	0.9
+ 125 °C	0.4

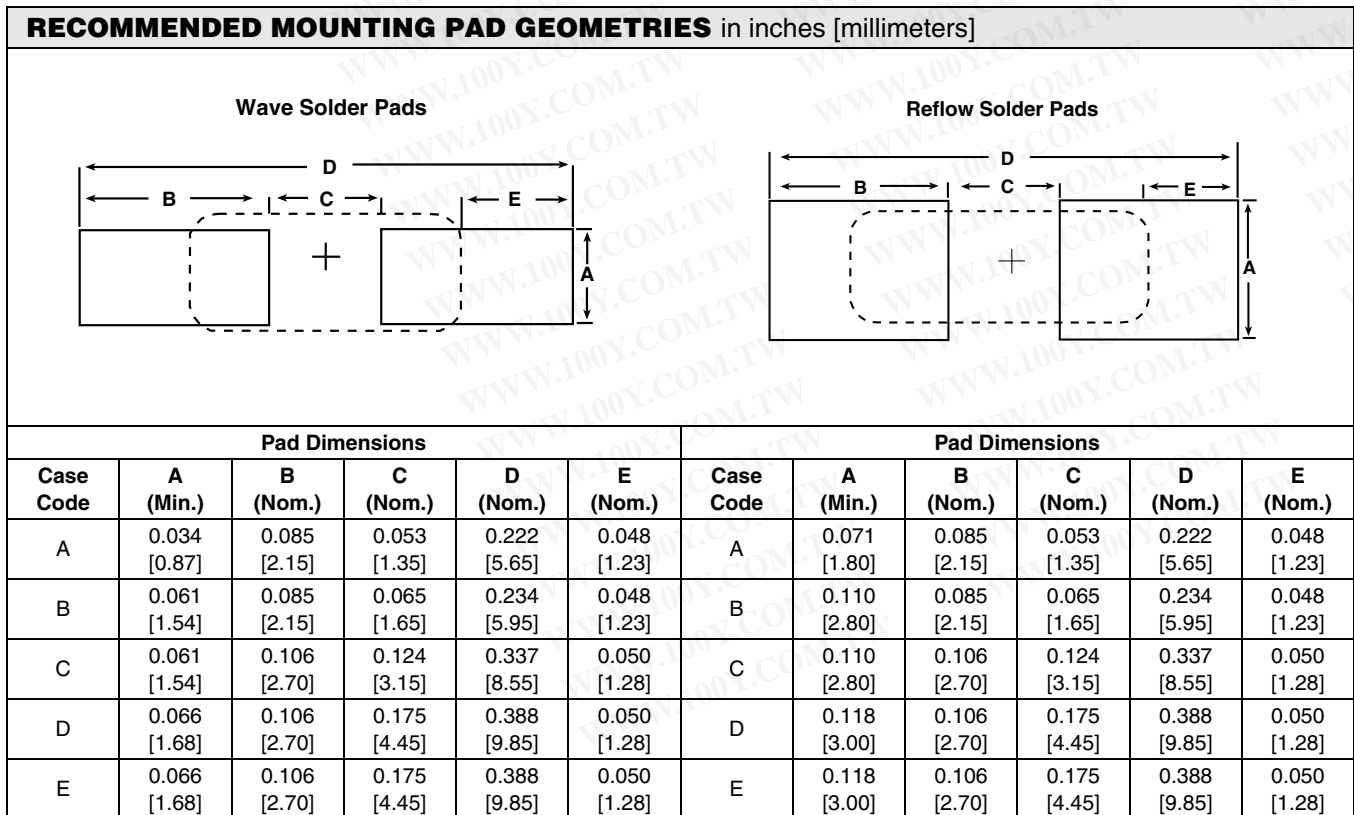
6. **Power Dissipation:** Power dissipation will be affected by the heat sinking capability of the mounting surface. Non-sinusoidal ripple current may produce heating effects which differ from those shown. It is important that the equivalent  $I_{rms}$  value be established when calculating permissible operating levels. (Power Dissipation calculated using + 25 °C temperature rise.)

Case Code	Maximum Permissible Power Dissipation at + 25 °C (Watts) in free air
A	0.075
B	0.085
C	0.110
D	0.150
E	0.165

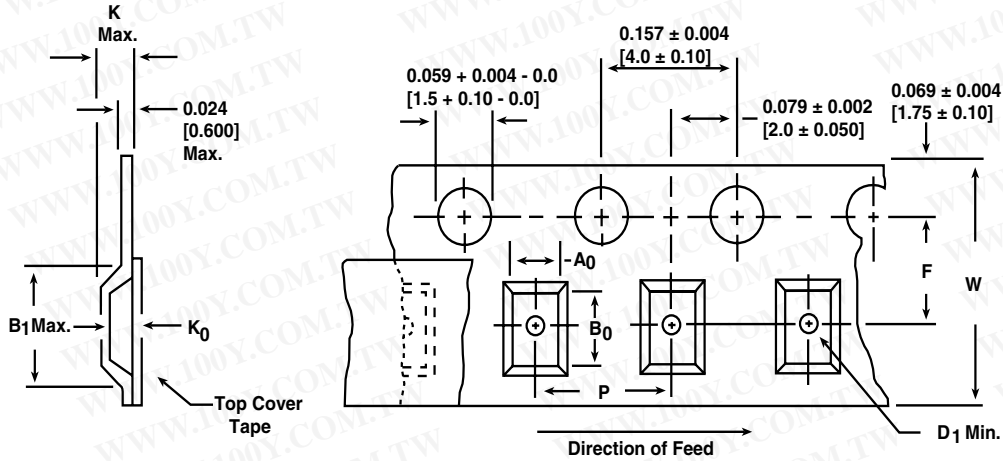
7. **Printed Circuit Board Materials:** Type 593D capacitors are compatible with commonly used printed circuit board materials (alumina substrates, FR4, FR5, G10, PTFE-fluorocarbon and porcelainized steel).
8. **Attachment:**
- 8.1 **Solder Paste:** The recommended thickness of the solder paste after application is 0.007" ± 0.001" [0.178 mm ± 0.025 mm]. Care should be exercised in selecting the solder paste. The metal purity should be as high as practical. The flux (in the paste) must be active enough to remove the oxides formed on the metallization prior to the exposure to soldering heat. In practice this can be aided by extending the solder preheat time at temperatures below the liquidous state of the solder.
- 8.2 **Soldering:** Capacitors can be attached by conventional soldering techniques; vapor phase, convection reflow, infrared reflow, wave soldering and hot plate methods. The Soldering Profile charts show recommended time/temperature conditions for soldering. Preheating is recommended. The recommended maximum ramp rate is 2 °C per second. Attachment with a soldering iron is not recommended due to the difficulty of controlling temperature and time at temperature. The soldering iron must never come in contact with the capacitor.
- 8.2.1 **Backward and Forward Compatibility:** Capacitors with SnPb or 100 % tin termination finishes can be soldered using SnPb or lead (Pb)-free soldering processes.
9. **Cleaning (Flux Removal) After Soldering:** The 593D is compatible with all commonly used solvents such as TES, TMS, Prelete, Chloroethane, Terpene and aqueous cleaning media. However, CFC/ODS products are not used in the production of these devices and are not recommended. Solvents containing methylene chloride or other epoxy solvents should be avoided since these will attack the epoxy encapsulation material.
- 9.1 When using ultrasonic cleaning, the board may resonate if the output power is too high. This vibration can cause cracking or a decrease in the adherence of the termination. DO NOT EXCEED 9W/I AT 40 kHz for 2 minutes.

**GUIDE TO APPLICATION** (Continued)


10. **Recommended Mounting Pad Geometries:** Proper mounting pad geometries are essential for successful solder connections. These dimensions are highly process sensitive and should be designed to minimize component rework due to unacceptable solder joints. The dimensional configurations shown are the recommended pad geometries for both wave and reflow soldering techniques. These dimensions are intended to be a starting point for circuit board designers and may be fine tuned if necessary based upon the peculiarities of the soldering process and/or circuit board design.



**TAPE AND REEL PACKAGING** in inches [millimeters]

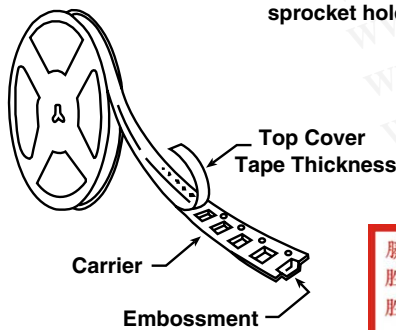


TAPE SIZE	B <sub>1</sub> (Max.)	D <sub>1</sub> (Min.)	F	K (Max.)	P	W	A <sub>0</sub> B <sub>0</sub> K <sub>0</sub>
8 mm	0.165 [4.2]	0.039 [1.0]	0.138 ± 0.002 [3.5 ± 0.05]	0.094 [2.4]	0.157 ± 0.004 [4.0 ± 1.0]	0.315 ± 0.012 [8.0 ± 0.30]	<b>Notes:</b> A <sub>0</sub> B <sub>0</sub> K <sub>0</sub> are determined by component size. The clearance between the component and the cavity must be within 0.002" [0.05 mm] minimum to 0.020" [0.50 mm] maximum for 8 mm tape and 0.002" [0.05 mm] minimum to 0.026" [0.65 mm] maximum for 12 mm tape.
12 mm	0.323 [8.2]	0.059 [1.5]	0.217 ± 0.002 [5.5 ± 0.05]	0.177 [4.5]	0.315 ± 0.004 [8.0 ± 1.0]	0.472 ± 0.012 [12.0 ± 0.30]	

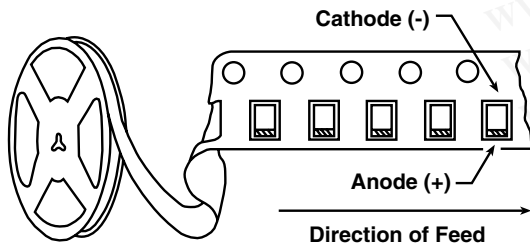
Standard orientation is with the cathode (-) nearest to the sprocket holes per EIA-481-1.

**Tape and Reel Specifications:** All case codes are available on plastic embossed tape per EIA-481-1. Standard reel diameter is 13" [330 mm]. 7" [178 mm] reels are available.

The most efficient packaging quantities are full reel increments on a given reel diameter. The quantities shown allow for the sealed empty pockets required to be in conformance with EIA-481-1. Reel size must be specified in the Vishay Sprague part number.



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Case Code	Tape Width	Component Pitch	Units Per Reel	
			7" [178 mm] Reel	13" [330 mm] Reel
A	8 mm	4 mm	2000	9000
B	8 mm	4 mm	2000	8000
C	12 mm	8 mm	500	3000
D	12 mm	8 mm	500	2500
E	12 mm	8 mm	400	1500





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