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April 1988 Revised January 2004

74F30 8-Input NAND Gate

General Description

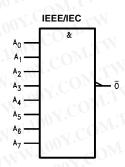
This device contains a single gate, which performs the logic NAND function.

Ordering Code:

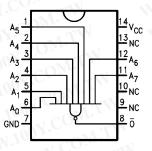
Ordering Code:							
Order Number	Package Number	Package Description					
74F30SC (Note 1)	M14A	14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150" Narrow					
74F30SJ	M14D	14-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide					
74F30PC	N14A	14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300" Wide					

Note 1: Devices also available in Tape and Reel. Specify by appending the letter "X" to the ordering code.

Logic Symbol



Connection Diagram



Unit Loading/Fan Out

Pin Names	Description	U.L. HIGH/LOW	Input I _{IH} /I _{IL} Output I _{OH} /I _{OL}
A ₀ -A ₇	Inputs	1.0/1.0	20 μA/-0.6 mA
A ₀ −A ₇ O	Output	50/33.3	-1 mA/20 mA

Function Table

0	U.L. C		Inputs								
n	HIGH/LOW	Input I _{IH} /I _{IL} Output I _{OH} /I _{OL}	A ₀	A ₁	A ₂	A ₃	A ₄	A ₅	A ₆	A ₇	ō
4.			L	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Н
	1.0/1.0	20 μA/-0.6 mA	Х	L	X	X	X	X	X	X	Н
	50/33.3	−1 mA/20 mA	Х	Х	L	X	X	Х	X	X	Н
N	44.	V.Co	X	Χ	X	L	Χ	X	X	X	Н
			X	Χ	X	X	L	Х	X	X	Н
			X	Χ	X	X	Χ	L	X	X	Н
			X	Χ	Χ	X	X	Χ	L	X	Н
			X	Χ	Χ	X	X	X	X	L	Н
			H	Н	Н	H	Н	Н	Н	Н	L
			H = HIG L = LOW X = Imm	V Voltag		l					

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Absolute Maximum Ratings(Note 2)

Recommended Operating Conditions

-65°C to +150°C Storage Temperature -55°C to +125°C Ambient Temperature under Bias Junction Temperature under Bias -55°C to +150C V_{CC} Pin Potential to Ground Pin -0.5V to +7.0V

Input Voltage (Note 3) -0.5V to +7.0V Input Current (Note 3) -30 mA to +5.0 mA

Voltage Applied to Output in HIGH State (with $V_{CC} = 0V$)

Standard Output -0.5V to V_{CC} 3-STATE Output -0.5V to +5.5V

Current Applied to Output

twice the rated I_{OL} (mA) in LOW State (Max)

Free Air Ambient Temperature 0°C to +70°C +4.5V to +5.5V Supply Voltage

Note 2: Absolute maximum ratings are values beyond which the device may be damaged or have its useful life impaired. Functional operation under these conditions is not implied.

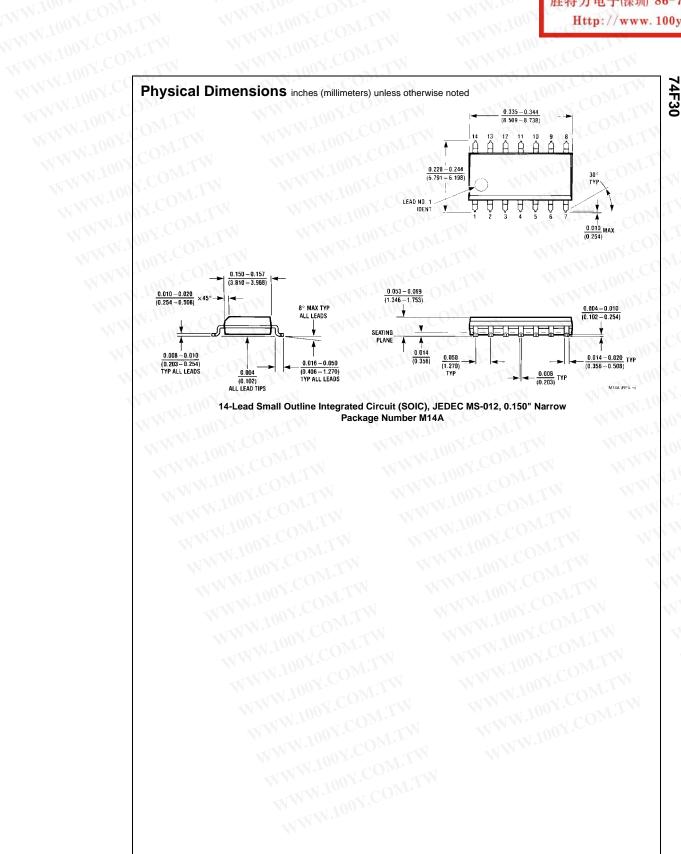
Note 3: Either voltage limit or current limit is sufficient to protect inputs.

DC Electrical Characteristics

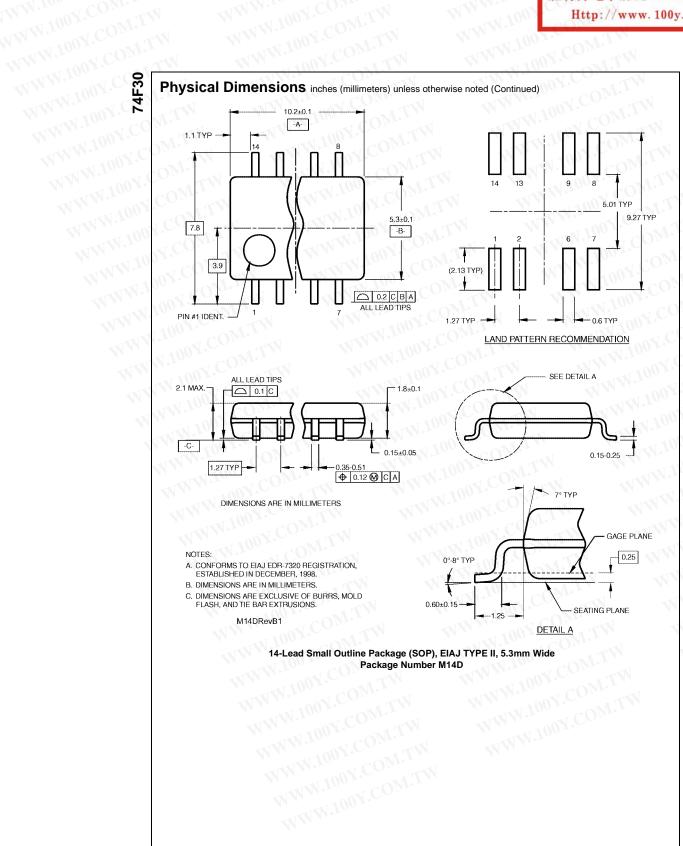
Symbol	Parameter	Min	Тур	Max	Units	V _{CC}	Conditions
V _{IH}	Input HIGH Voltage	2.0		-Ojar.	V		Recognized as a HIGH Signal
V _{IL}	Input LOW Voltage	41	1007.	0.8	V		Recognized as a LOW Signal
V _{CD}	Input Clamp Diode Voltage	STAN VV		-1.2	٧	Min	$I_{IN} = -18 \text{ mA}$
V _{OH}	Output HIGH 10% V _{CC} Voltage 5% V _{CC}	2.5 2.7	1.700	V.CO	V	Min	$I_{OH} = -1 \text{ mA}$ $I_{OH} = -1 \text{ mA}$
V _{OL}	Output LOW 10% V _{CC}	WW	N.100	0.5	V	Min	I _{OL} = 20 mA
I _{IH}	Input HIGH Current	W	MAIN	5.0	μА	Max	V _{IN} = 2.7V
I _{BVI}	Input HIGH Current Breakdown Test	V	WW.	7.0	μА	Max	V _{IN} = 7.0V
I _{CEX}	Output HIGH Leakage Current	4	MAIN	50	μА	Max	V _{OUT} = V _{CC}
V _{ID}	Input Leakage Test	4.75	MM	CV.100	V	0.0	I _{ID} = 1.9 μA All Other Pins Grounded
I _{OD}	Output Leakage Circuit Current	7	MA	3.75	μА	0.0	V _{IOD} = 150 mV All Other Pins Grounded
I _{IL}	Input LOW Current	N		-0.6	mA	Max	$V_{IN} = 0.5V$
los	Output Short-Circuit Current	-60	-4	-150	mA	Max	$V_{OUT} = 0V$
Іссн	Power Supply Current	44	0.5	1.5	mA	Max	$V_O = HIGH$
I _{CCL}	Power Supply Current	TXX		4.5	mA	Max	$V_O = LOW$

AC Electrical Characteristics

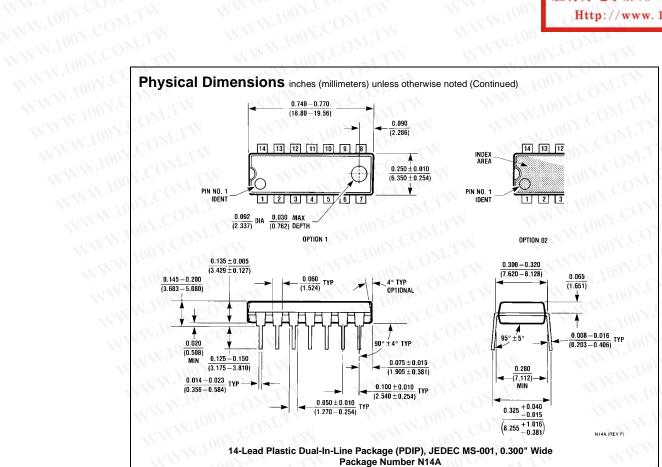
Symbol	Parameter	TW	$T_A = +25^{\circ}C$ $V_{CC} = +5.0V$ $C_L = 50 \text{ pF}$				Units
		Min	Тур	Max	Min	Max	
t _{PLH}	Propagation Delay	1.0	3.7	5.0	1.0	5.5	
t _{PHL}	A_n to \overline{O}	1.5	2.8	5.0	1.5	5.5	ns







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