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BD240/A/B/C

Medium Power Linear and Switching Applications

• Complement to BD239/A/B/C respectively



1.Base 2.Collector 3.Emitter

PNP Epitaxial Silicon Transistor

Absolute Maximum Ratings T_C=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V_{CEO}	Collector-Base Voltage	TAN IN WILL	ON CO
×110	: BD240	- 45	100V
	: BD240A	- 60	V
	: BD240B	- 80	V
	: BD240C	- 100	V
V _{CER}	Collector-Emitter Voltage		111.
1	: BD240	- 55	V
	: BD240A	- 70	V
	: BD240B	- 90	V
WW	: BD240C	- 115	V
V _{EBO}	Emitter-Base Voltage	- 5	V
Ic	Collector Current (DC)	- 2	A
I _{CP}	*Collector Current (Pulse)	-4	Α
I _B	Base Current	- 0.6	Α
P _C	Collector Dissipation (T _C =25°C)	30	W
T _J	Junction Temperature	150	°C
T _{STG}	Storage Temperature	- 65 ~ 150	°C

Electrical Characteristics T_C=25°C unless otherwise noted

Parameter	Test Condition	Min.	Тур.	Max.	Units
* Collector-Emitter Sustaining Voltage : BD240	$I_C = -30 \text{mA}, I_B = 0$	- 45	M. T	N	V
: BD240A	MW.IO	- 60	O_{Mr} .	CN	V
: BD240B	1	- 80	.Mc		V
: BD240C		- 100		W	V
Collector Cut-off Current : BD240/A	$V_{CE} = -30V, I_{B} = 0$	100.	COM	- 0.3	mA
: BD240B/C	$V_{CE} = -60V, I_{B} = 0$			- 0.3	mA
Collector Cut-off Current : BD240	$V_{CE} = -45V, V_{BE} = 0$	1.10		- 0.2	mA
: BD240A	$V_{CE} = -60V, V_{BE} = 0$			- 0.2	mA
: BD240B	$V_{CE} = -80V, V_{BE} = 0$			- 0.2	mA
: BD240C	$V_{CE} = -100V, V_{BE} = 0$			- 0.2	mA
Emitter Cut-off Current	$V_{EB} = -5V, I_{C} = 0$			- 1	mA
* DC Current Gain	$V_{CE} = -4V, I_{C} = -0.2A$	40			
Al Al	$V_{CE} = -4V, I_{C} = -1A$	15			
* Collector-Emitter Saturation Voltage	$I_C = -1A$, $I_B = -0.2A$			- 0.7	V
* Base-Emitter ON Voltage	$V_{CE} = -4V, I_{C} = -1A$			- 1.3	V
	: BD240 : BD240A : BD240B : BD240C Collector Cut-off Current : BD240/A : BD240B/C Collector Cut-off Current : BD240 : BD240A : BD240A : BD240C Emitter Cut-off Current * DC Current Gain	$ \begin{array}{c} : BD240 \\ : BD240A \\ : BD240B \\ : BD240C \\ \end{array} \qquad \begin{array}{c} I_C = -30 \text{mA}, \ I_B = 0 \\ \end{array} \\ \begin{array}{c} : BD240B \\ : BD240C \\ \end{array} \\ \begin{array}{c} : BD240C \\ \end{array} \\ \begin{array}{c} : BD240C \\ \end{array} \\ \begin{array}{c} : DCE = -30 \text{mA}, \ I_B = 0 \\ \text{model} \\ : DCE = -60 \text{model} \\ : DCE = -80 \text{model} \\ : DCE = -100 \text{model} \\ : DCE = -100 \text{model} \\ : DCE = -100 \text{model} \\ : DCE = -40 m$	$ \begin{array}{c} : BD240 \\ : BD240A \\ : BD240B \\ : BD240C \\ \end{array} \qquad \begin{array}{c} I_{C} = -30 \text{mA}, I_{B} = 0 \\ -60 \\ -80 \\ -100 \\ \end{array} $	$ \begin{array}{c} : BD240 \\ : BD240A \\ : BD240B \\ : BD240C \\ \end{array} \qquad \begin{array}{c} I_{C} = -30 \text{mA}, I_{B} = 0 \\ -60 \\ -80 \\ -100 \\ \end{array} $	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$

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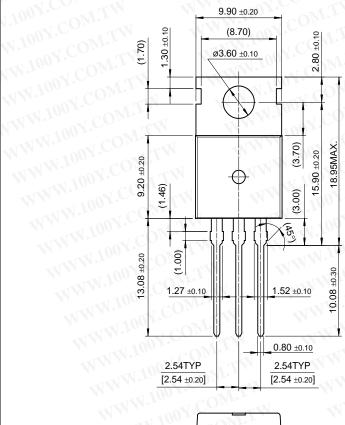
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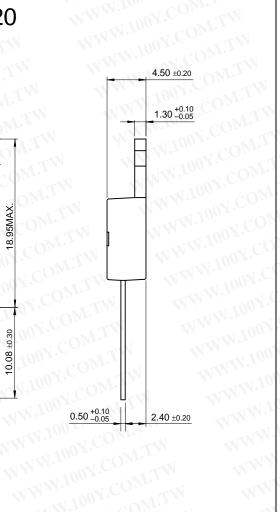
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