

LOW-VOLTAGE QUAD 2:1MUX/DEMUX BUS SWITCH

IDT74CBTLV3257

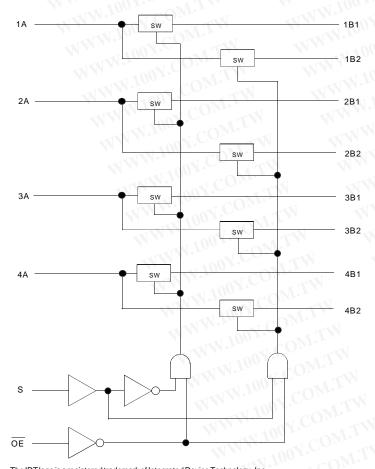
FEATURES:

- Functionally equivalent to QS3257
- 5 Ω Switch Connection between Two Ports
- Isolation Under Power-Off Conditions
- Over-voltage tolerant
- Latch-up performance exceeds 100ma
- Vcc = 2.3V 3.6V, normal range
- ESD > 2000V per MIL-STD-883, Method 3015;
 > 200V using machine model (C = 200pF, R = 0)
 Available in SSOP, QSOP, and TSSOP packages

APPLICATIONS:

3.3V High Speed Bus Switching, Multiplexing, and Bus Isolation

FUNCTIONAL BLOCK DIAGRAM



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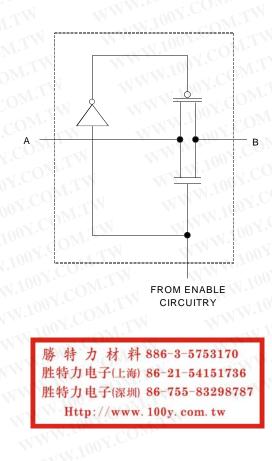
DESCRIPTION:

The CBTLV3257 is a quad 2:1 multiplexer/demultiplexer. The low onstate resistance of the switch allows connections to be made with minimal propagation delay.

The select (S) input controls the data flow. The multiplexers/demultiplexers are enabled when the output-enable (\overline{OE}) input is low.

To ensure the high-impedance state during power up or power down, \overline{OE} should be tied to Vcc through a pullup resistor; the minimum value of the resistor is determined by the current-sinking capability of the driver.

SIMPLIFIED SCHEMATIC, EACH SWITCH

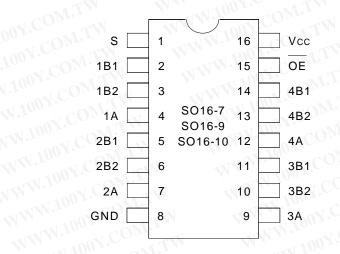


SEPTEMBER 2001

IDT74CBTLV3257 LOW-VOLTAGE QUAD 2:1 MUX/DEMUX BUS SWITCH

INDUSTRIAL TEMPERATURE RANGE

PIN CONFIGURATION



QSOP/ SSOP/ TSSOP **TOP VIEW**

勝特力材料 886-3-5753170 胜特力电子(上海) 86-21-54151736 胜特力电子(深圳) 86-755-83298787 Http://www.100y.com.tw

ABSOLUTE MAXIMUM RATINGS (1)

Symbol	Description	Max.	Unit
Vcc	Supply Voltage Range	-0.5 to 4.6	V
VI	Input Voltage Range	-0.5 to 4.6	V
	Continuous Channel Current	128	mA
Ік	Input Clamp Current, VI/0 < 0	-50	mA
TSTG	Storage Temperature	-65 to +150	°C

NOTES:

1. Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

FUNCTION TABLE⁽¹⁾

	puts		
OE	S 10	Function	
TT.M.		A Port = B1 Port	
W4	Ĥ	A Port = B2 Port	
ON-H-W	Х	Disconnect	

ymbol	Parameter	Test Conditions	Min.	Max.	Unit
Vcc	Supply Voltage	WWW ILOOX.C.	2.3	3.6	V
Vih	High-Level Control Input Voltage	Vcc = 2.3V to 2.7V	1.7	WW T 100Y	V
	WW.100 COM	Vcc = 2.7V to 3.6V	2	WW <u>r</u> N. 10	<.CO
Vil	Low-Level Control Input Voltage	Vcc = 2.3V to 2.7V	CONT,	0.7	VO
	WW 100Y.CO	Vcc = 2.7V to 3.6V		0.8	
Та	Operating Free-Air Temperature	MILL WAR	-40	+85	°C

WWW.100Y.COM.TW 1. All unused control inputs of the device must be held at Vcc or GND to ensure proper operation.

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DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE

Following Conditions Apply Unless Otherwise Specified: Operating Condition: $T_A = -40^{\circ}C$ to $+85^{\circ}C$

Symbol	Parameter	Test Conditions		Min.	Typ. ⁽¹⁾	Max.	Unit	
Vik	Control Inputs, Data I/O	Vcc = 3V, $II = -18mA$		A -	—	- 1.2	V	
ALCOM	Control Inputs, Data I/O	Vcc = 3.6V, VI = Vcc	c or GND	-117	_	±1	μA	
loz	Data I/O	Vcc = 3.6V, Vo = 0 0	or 3.6V, switch disabled	The second	_	20	μA	
IOFF	N.1. 1. 1.100	Vcc = 0, Vi or Vo = 0) to 3.6V		—	50	μA	
lcc	M.T.W WWW. 10	$V_{CC} = 3.6V$, $I_O = 0$, $V_I = V_{CC}$ or GND		V.L.	_	10	μA	
ΔICC ⁽²⁾	Control Inputs	Vcc = 3.6V, One input at 3V, Other inputs at Vcc or GND		MIT	_	300	μA	
Ci	Control Inputs	Vi = 3V or 0		TT	4	_	pF	
CIO(OFF)	A port	Vo = 3V or 0, \overline{OE} = Vcc = 3.3V		012	13	_	pF	
	B port			$CO\overline{B}r$	6	_		
N.100	Max at Vcc = 2.3V	VI = 0	lo = 64mA	COM	5	8		
	Typ at Vcc = 2.5V	100Y.COM	lo = 24ma	(TT)	5	8		
Ron ⁽³⁾	OY.COMMENTER WY	VI = 1.7V	lo = 15mA	N. <u>-</u>	27	40	Ω	
	NUNCONTENN	VI = 0	lo = 64mA	01-CC	5	7		
	Vcc = 3V	UNW.ING CC	lo = 24mA	oft.C	5	7	1	
	100x. OM.TW	VI = 2.4V	lo = 15mA		10	15	1	

NOTES:

1. Typical values are at Vcc = 3.3V, +25°C ambient.

2. The increase in supply current is attributable to each input that is at the specified voltage level rather than Vcc or GND.

3. This is measured by the voltage drop between the A and B terminals at the indicated current through the switch. On-state resistance is determined by the lower of the voltages of the two (A or B) terminals.

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SWITCHING CHARACTERISTICS

	WWW 100Y.CONLTN WWW	Vcc = 2.5V ± 0.2V		Vcc = 3.3V ± 0.3V		M.L.
Symbol	Parameter	Min.	Max.	Min.	Max.	Unit
tpd ⁽¹⁾	Propagation Delay A to B or B to A	W.1007.C	0.15	-7/17	0.25	ns
tsel	Select Time S to A or B	W.100X	6.1	1	5.3	ns
ten	Enable Time S to B	WW.1	6.1	1	5.3	ns
tois	Disable Time S to B	NW 1.10	4.8		4.5	ns
t _{EN}	Enable Time OE to A or B	WWW.I	5.6	TW ¹	5	ns
tois	Disable Time OE to A or B	WAW.	5.5	MITY	5.5	ns

NOTE:

 The propagation delay is the calculated RC time constant of the typical on-state resistance of the switch and the specified load capacitance when driven by an ideal voltage source (zero output impedance).

INDUSTRIAL TEMPERATURE RANGE

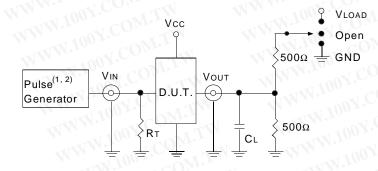
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TEST CIRCUITS AND WAVEFORMS

TEST CONDITIONS

Symbol	$Vcc^{(1)} = 3.3V \pm 0.3V$	Vcc ⁽²⁾ = 2.5V ± 0.2V	Unit
VLOAD	6	2 x Vcc	V
Vih	3	Vcc	V
VT	1.5	Vcc/2	V
VLZ	300	150	mV
VHZ	300	150	mV
CL	50	30	рF

TEST CIRCUITS FOR ALL OUTPUTS



DEFINITIONS:

- CL = Load capacitance: includes jig and load capacitance.
- RT = Termination resistance: should be equal to ZOUT of the pulse generator.

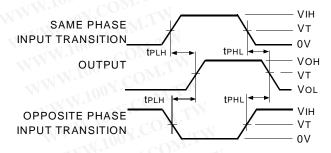
NOTES:

- 1. Pulse Generator for all pulses: Rate ≤ 10MHz; tF ≤ 2.5ns, tR ≤ 2.5ns
- Pulse Generator for all pulses: Rate \leq 10MHz; tF \leq 2ns, tR \leq 2ns 2.

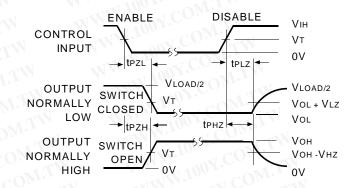
SWITCH POSITION

Test	Switch
tplz/tpzl	VLOAD
tрнz/tpzн	GND
t _{PD}	Open
t SEL	Open

PROPAGATION DELAY/ SELECT TIME



ENABLE AND DISABLE TIMES



NOTE:

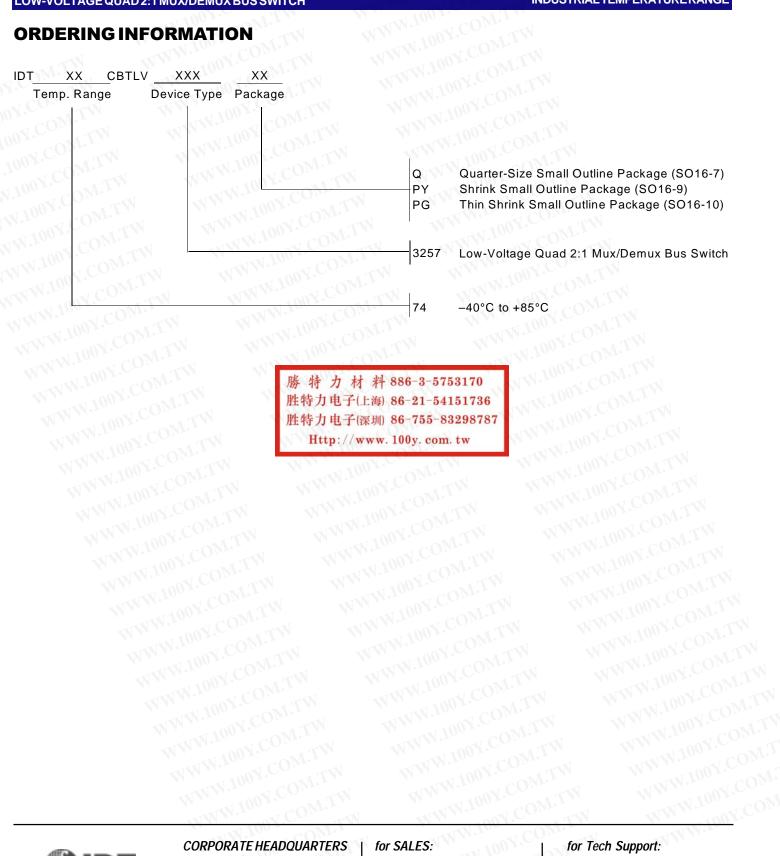
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1. Diagram shown for Input Control Enable-LOW and Input Control Disable-HIGH.

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INDUSTRIAL TEMPERATURE RANGE

ORDERING INFORMATION



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